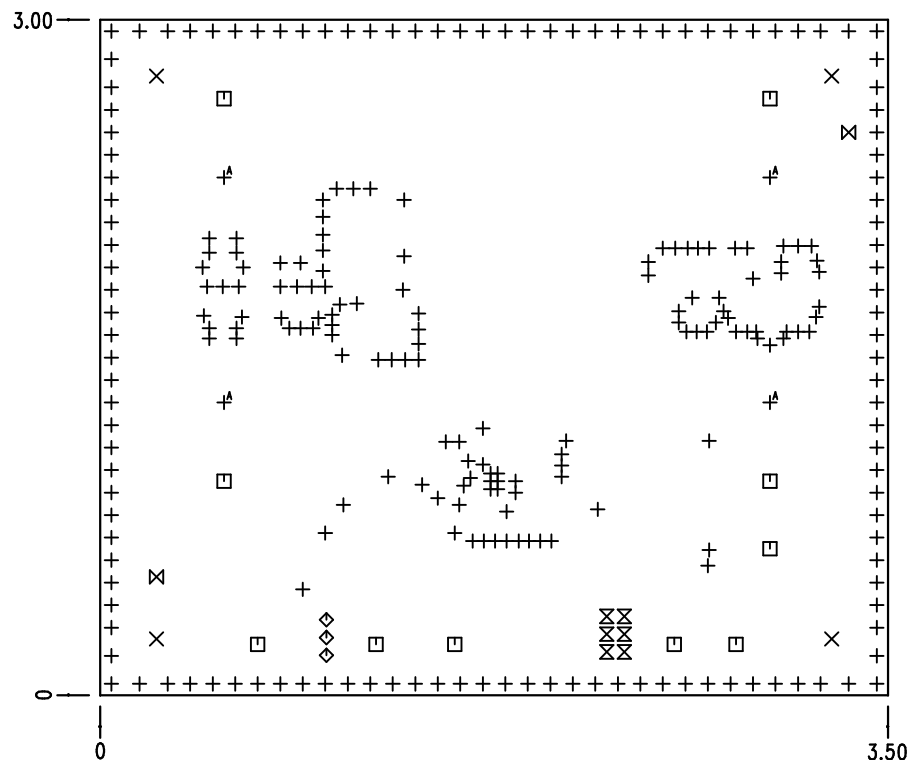


REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	2	PRODUCTION	VICTOR K	03-30-12

SHOWN FROM TOP SIDE



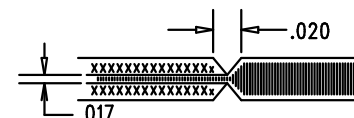
SIZE	QTY	SYM	PLATED	TOL
0.01	250	+	YES	+/-0.003
0.188	4	X	YES	+/-0.003
0.094	10	□	YES	+/-0.003
0.031	3	◇	YES	+/-0.003
0.035	6	⊗	YES	+/-0.003
0.07	2	⊗	NO	+/-0.003
0.215	4	+ ^A	YES	+/-0.003


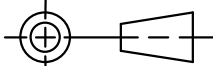
LAYER STRUCTURE

- LAYER 1 (COMPONENT SIDE)
- LAYER 2
- LAYER 3
- LAYER 4 (SOLDER SIDE)

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062+/- .005
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



UNLESS OTHERWISE SPECIFIED		APPROVALS		 LINEAR TECHNOLOGY		1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY			
DIMENSIONS ARE IN INCHES		PCB DES.	LT	TITLE: FABRICATION DRAWING					
TOLERANCES:		APP ENG.	VICTOR K						
0.XX = ± 0.01				LOW QUIESCENT CURRENT HIGH VOLTAGE STEP-DOWN CONVERTER					
0.XXX = ± 0.005									
INTERPRET DIM AND TOL PER ASME Y14.5M-1994				SIZE		IC NO.	LTC3891EUDC	REV	
THIRD ANGLE PROJECTION				N/A		DEMO CIRCUIT 1940A			2
		SCALE = NONE		FILENAME: DC1940A-2.PCB				SHT 1 OF 1	