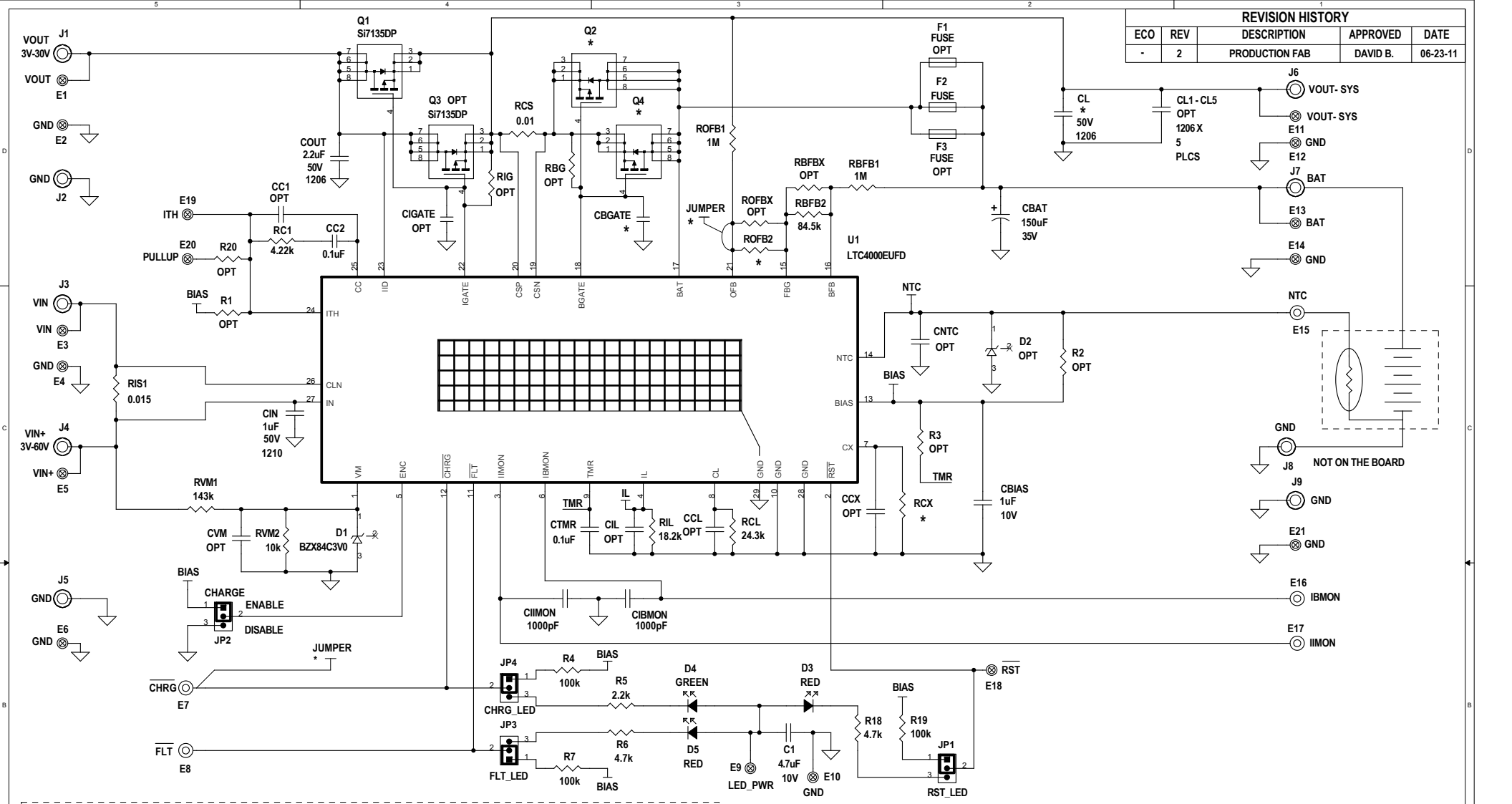
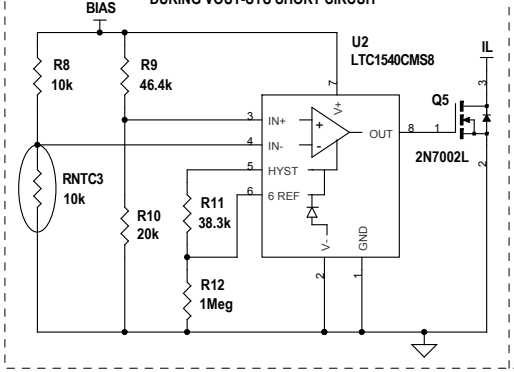
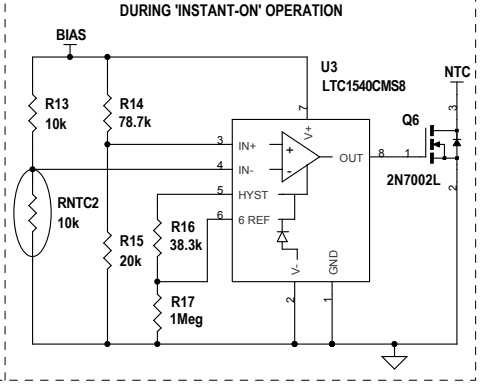


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	2	PRODUCTION FAB	DAVID B.	06-23-11

* OPTIONAL THERMAL PROTECTION FOR Q1 DURING VOUT-SYS SHORT CIRCUIT



* THERMAL REGULATION FOR Q2 DURING 'INSTANT-ON' OPERATION



*

ASSY	PROTECT CKT. FOR Q1	REG CKT. FOR Q2	JUMPER	Q2	Q4	ROFB2	RCX	CL	CBGATE
-A	LOADED	LOADED	NO INSTALL	SI7135DP	OPT	84.5k	10.0k	2.2uF	10nF
-B	NOT LOADED	NOT LOADED	INSTALL	LRC-LRZ-2010LF-R000	NO INSTALL	OPT	0 OHM	OPT	NO INSTALL

NOTE: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE IN OHMS, 0603.
   ALL CAPACITORS ARE IN MICROFARADS, 0603.

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

APPROVALS	
PCB DES.	NC
APP ENG.	DAVE B.

SCALE = NONE

LINEAR TECHNOLOGY
1630 McCarthy Blvd.
Milpitas, CA 95035
Phone: (408)432-1900
Fax: (408)434-0507
www.linear.com
LTC Confidential-For Customer Use Only

TITLE: SCHEMATIC

BATTERY CHARGER AND POWER MANAGEMENT BOARD

SIZE	IC NO.	REV.
N/A	LTC4000EUFD DEMO CIRCUIT 1830A - A / B	2

DATE: 06-23-11 SHEET 1 OF 1