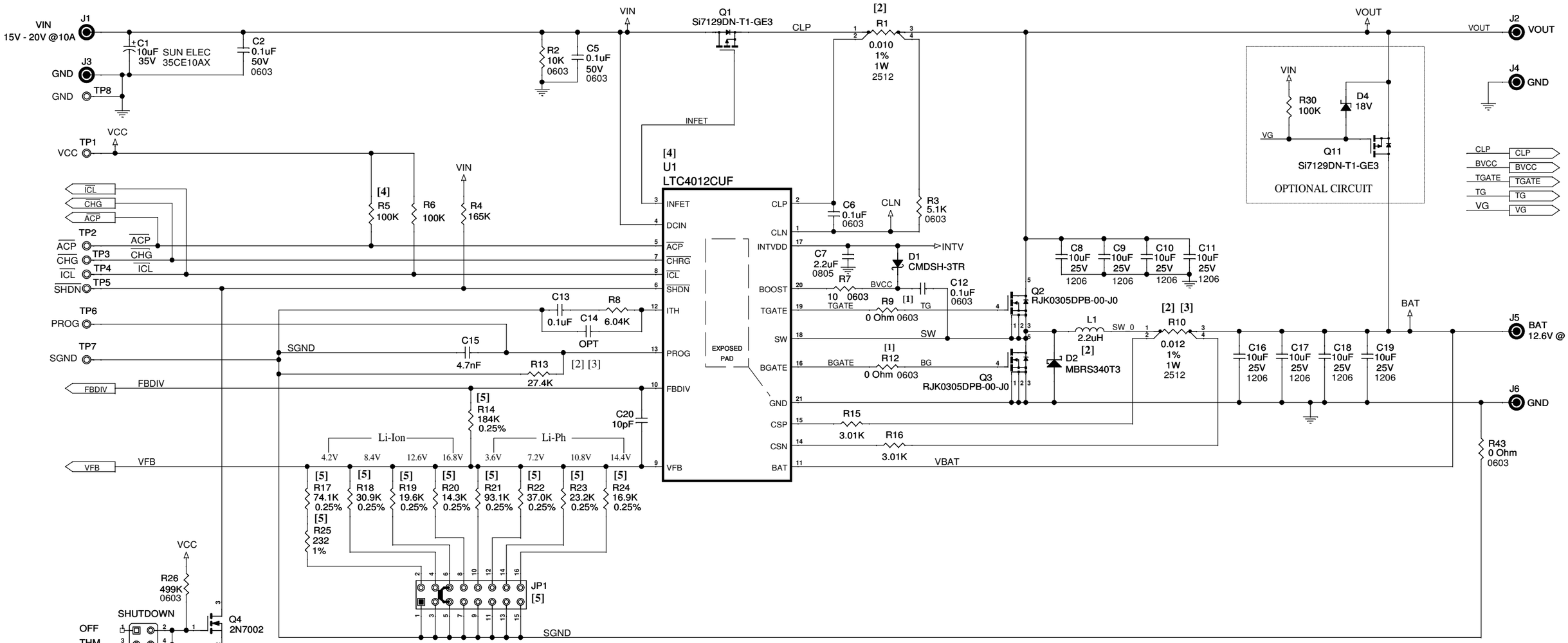
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	2	PRODUCTION	JOSH Y	05/27/10



NOTES: UNLESS OTHERWISE SPECIFIED,

1. CHANGE R9 AND R12 T0 100 OHMS WHEN USING GATE DRIVER CIRCUIT.
2. ADJUST R1, R10, R13 AND L1 APPROPRIATELY FOR INCREASED OUTPUT CURRENT.
3. R13 IS ADJUSTED WITH R10 TO OBTAIN 8A OUTPUT CURRENT.
4. OPTION: FOR SLA OPTIONAL CIRCUIT, DO NOT INSTALL R14, R17 THRU R25, JP1.
5. ALL CAPACITORS AND RESISTORS ARE 0402.

CUSTOMER NOTICE

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C:\PADS PROJECTS\1614A\SCH\1614A_REV3.DSN

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS	
PCB DES.	RB
APP ENG.	JOSH Y

SCALE = NONE

LINEAR TECHNOLOGY
1630 McCarthy Blvd.
Milpitas, CA 95035 www.linear.com
Phone: (408)432-1900
Fax: (408)434-0507

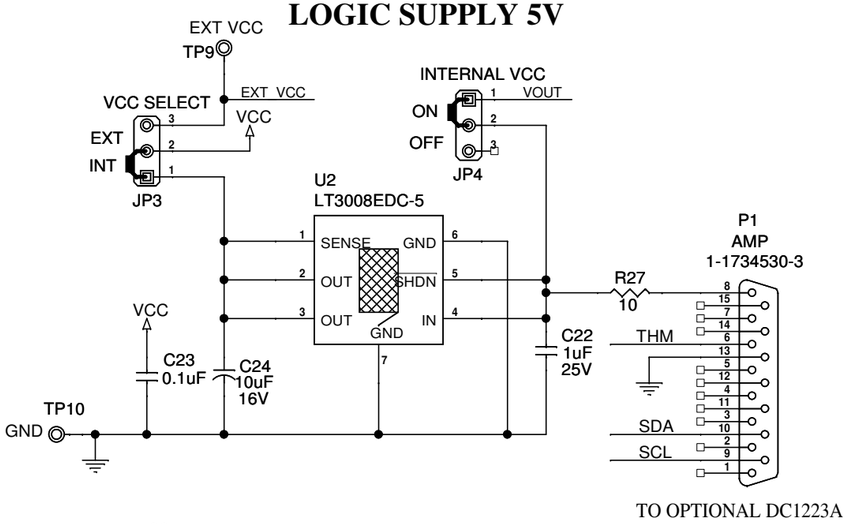
TITLE: SCHEMATIC

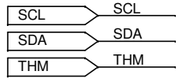
THREE CELL 12.6V@8A SWITCHING BATTERY CHARGER

SIZE	IC NO.	REV.
N/A	LTC4012CUF DEMO CIRCUIT 1614A	2

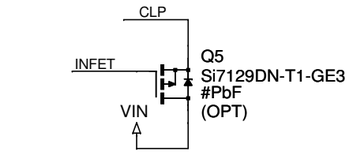
DATE: Monday, October 04, 2010 SHEET 1 OF 2

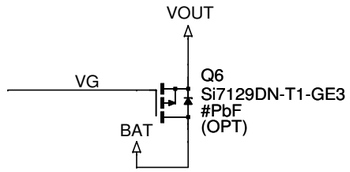
LOGIC SUPPLY 5V

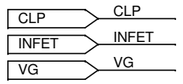




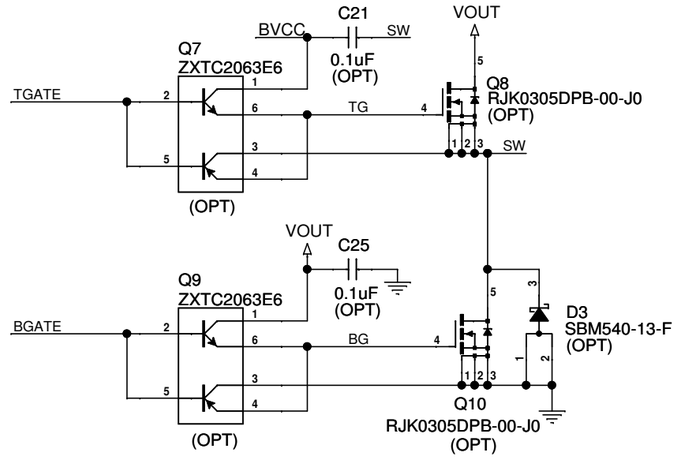
OPTIONAL CIRCUIT(INFET) & VOUT



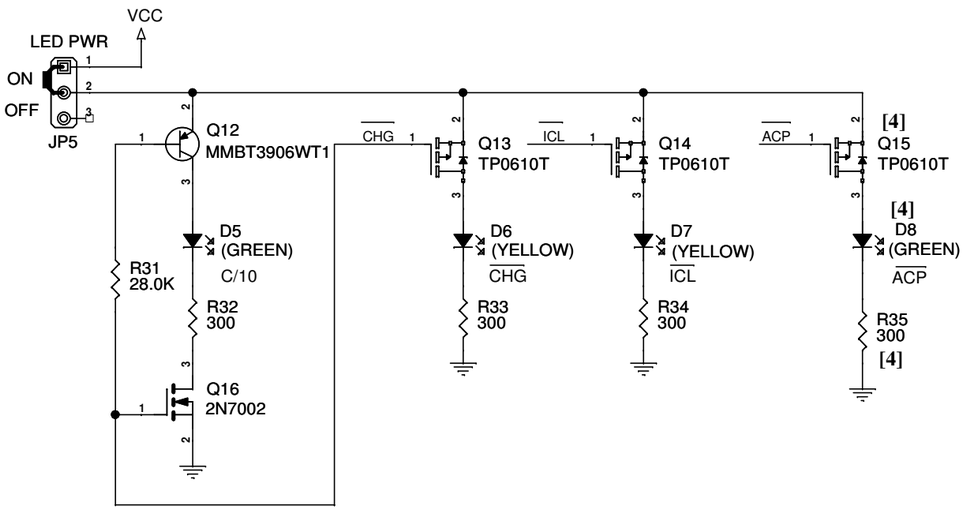




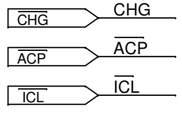
OPTIONAL
INCREASED GATE DRIVE CIRCUIT

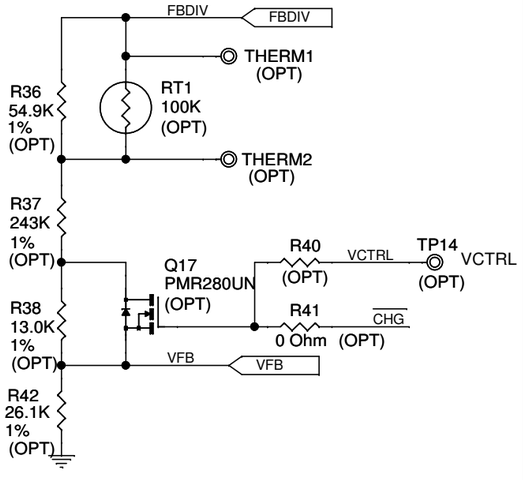


1. REMOVE ALL ABOVE COMPONENTS WHEN NOT USING GATE DRIVER CIRCUIT.
2. Q7 AND Q9 CAN BE PBSS4140DON OR CMLF3474 DEPENDING ON REQUIRED DRIVE STRENGTH.



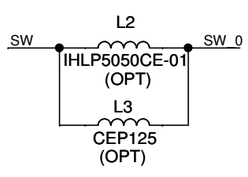
INDICATOR CIRCUIT



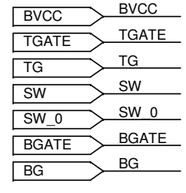


OPTIONAL CIRCUIT
(SEALED LEAD ACID)

1. REMOVE ALL ABOVE COMPONENTS WHEN NOT USING SLA CIRCUIT.



INDUCTOR
PACKAGE OPTIONS



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APPROVALS	
PCB DES.	RB
APP ENG.	JOSH Y

SCALE = NONE

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TITLE: SCHEMATIC

THREE CELL 12.6V@8A SWITCHING BATTERY CHARGER

SIZE	IC NO.	REV.
N/A	LTC4012CUF DEMO CIRCUIT 1614A	3

DATE: Monday, October 04, 2010