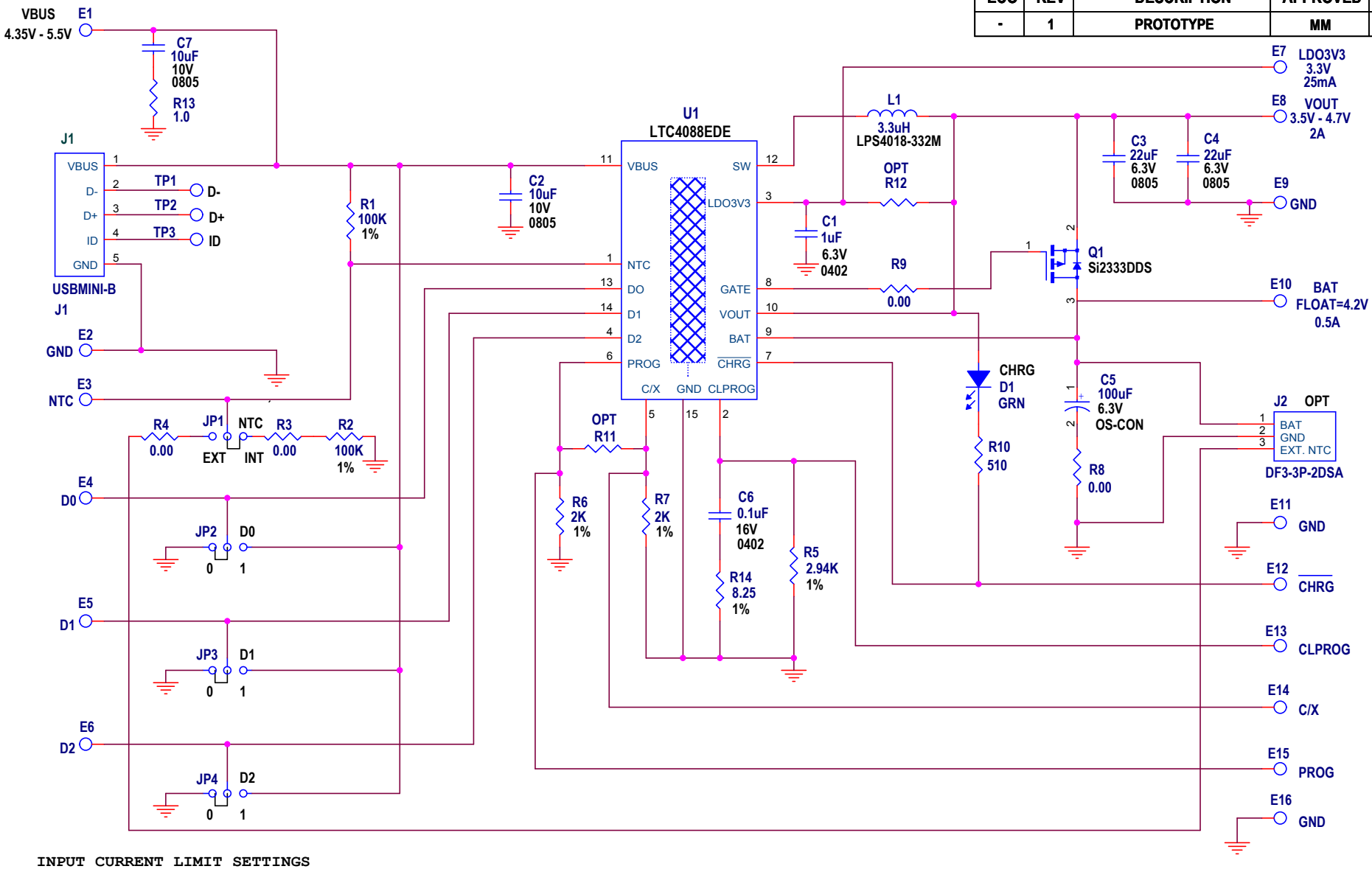
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	1	PROTOTYPE	MM	7 - 25 - 16



INPUT CURRENT LIMIT SETTINGS

D0	D1	D2	CURRENT LIMIT	CHARGER STATUS
0	0	0	100mA (1X)	ON
0	0	1	100mA (1X)	OFF
0	1	0	500mA (5X)	ON
0	1	1	500mA (5X)	OFF
1	0	0	1A (10X)	ON
1	0	1	1A (10X)	OFF
1	1	0	500uA (SUSP)	OFF
1	1	1	2.5mA (SUSP)	OFF

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS	
PCB DES.	NC
APP ENG.	MM

SCALE = NONE

LINEAR TECHNOLOGY
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TITLE: SCHEMATIC

High Efficiency Battery Charger / USB Power Manager

SIZE	IC NO.	REV.
N/A	LTC4088EDE DEMO CIRCUIT 1007C	1

DATE: 7 - 25 - 16

SHEET 1 OF 1