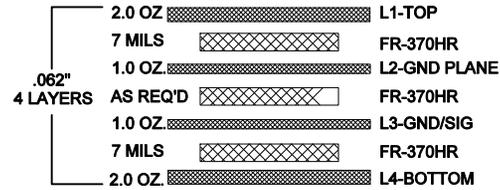


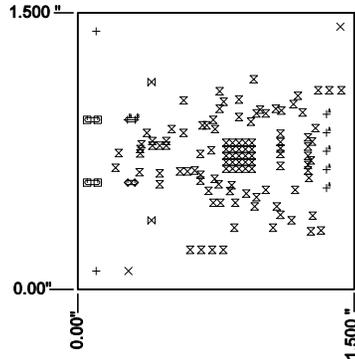
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPR	DATE
-	03	PRODUCTION	ZP	05-31-19

**FIG. 1
LAYER STRUCTURE**

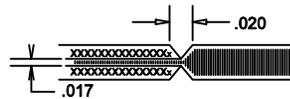


NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-800.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-O MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- DO NOT ALTER SOLDER MASK MAINTAIN .0018" OVERSIZE ON SMT PADS. A .005" WEBBING IS REQUIRED BETWEEN SMD PADS.
- SCORING FOR PANELIZED PCB: "PRODUCTION FAB ONLY"
- ANY ELECTRICAL TEST/QA STAMPS SHALL BE ON BOTTOM SIDE, "UNSHOWY AREA".



SIZE	QTY	SYM	PLATED	TOL
10	110	X	YES	+/-0.003
23.62 x 61.02	1	◇	YES	+/-0.003
23.62 x 64.96	1	⊕ ^B	YES	+/-0.003
23.62 x 80.71	2	□	YES	+/-0.003
45	5	⊕ ^A	YES	+/-0.003
70	2	X	NO	+/-0.003
94	2	⊕	YES	+/-0.003
137.8	2	X	YES	+/-0.003



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON ANGLE 1° 0.300° = 0.01° ± 0.300° = 0.005° ± INTERPRET DIM AND TOL PER ASME Y14.5M-1994	APPROVALS			1830 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 WWW.ANALOG.COM	
	PCB DES. NC	APP ENG. ZP		TITLE: FABRICATION DRAWING: USB-C POWER DELIVERY MANAGER DAUGHTER BOARD	
THIRD ANGLE PROJECTION			SIZE N/A	IC NO. LTC4162 DEMO CIRCUIT 2654A	REV. 03
DO NOT SCALE DRAWING	SCALE: NONE		SHT 1 of 1		