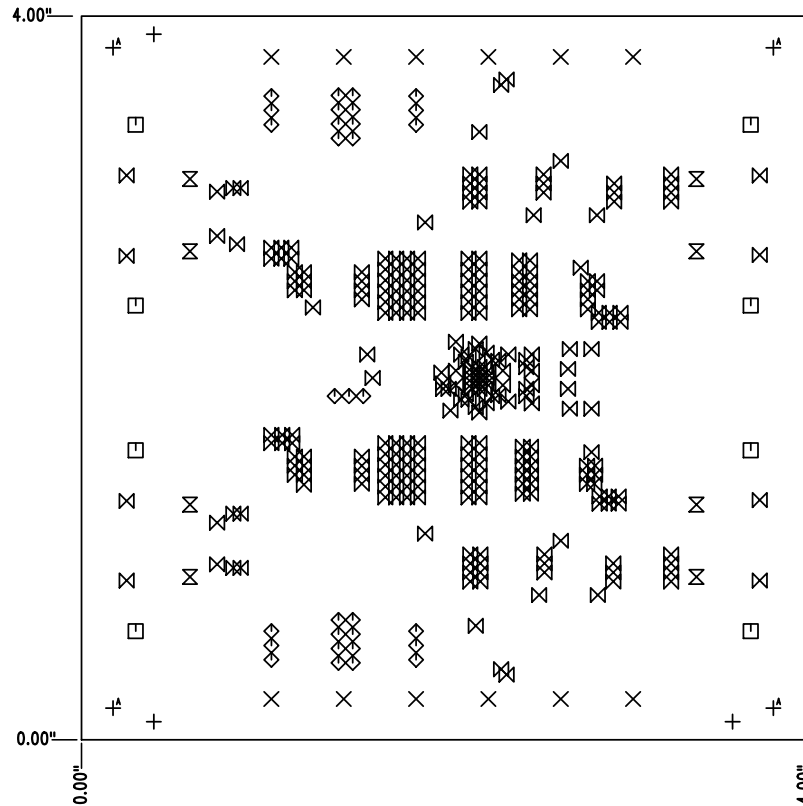
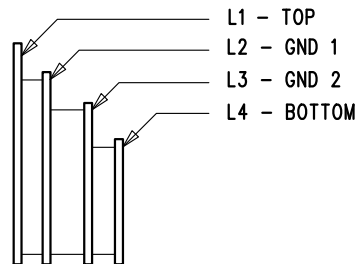


SHOWN FROM TOP SIDE



LAYER STRUCTURE



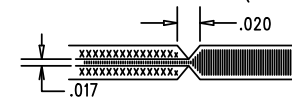
REVISION HISTORY


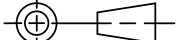
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	1	1ST PROTOTYPE	VLADIMIR O.	05-07-12

SIZE	QTY	SYM	PLATED	TOL
70	3	+	NO	+/-0.003"
64	12	X	YES	+/-0.003"
215	8	□	YES	+/-0.003"
35	31	◇	YES	+/-0.003"
94	8	⊗	YES	+/-0.003"
12	284	⊗	YES	+/-0.003"
190	4	+ ^A	YES	+/-0.003"

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN. 0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



UNLESS OTHERWISE SPECIFIED		APPROVALS		 <div>1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY</div>	
DIMENSIONS ARE IN INCHES		PCB DES.	KIM T.		
TOLERANCES:		APP ENG.	VLADIMIR O.		
0.XX" = ±0.01"					
0.XXX" = ±0.005"				TITLE: FABRICATION DRAWING	
INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION				DUAL IDEAL DIODE AND HOT SWAP CONTROLLER	
		SIZE	IC NO.	LTC4228IUFD-1/-2	
		N/A		DEMO CIRCUIT 1899A	
		REV		1	
SCALE = NONE		FILENAME: DC1899A-1.PCB		SHT 1 OF 1	