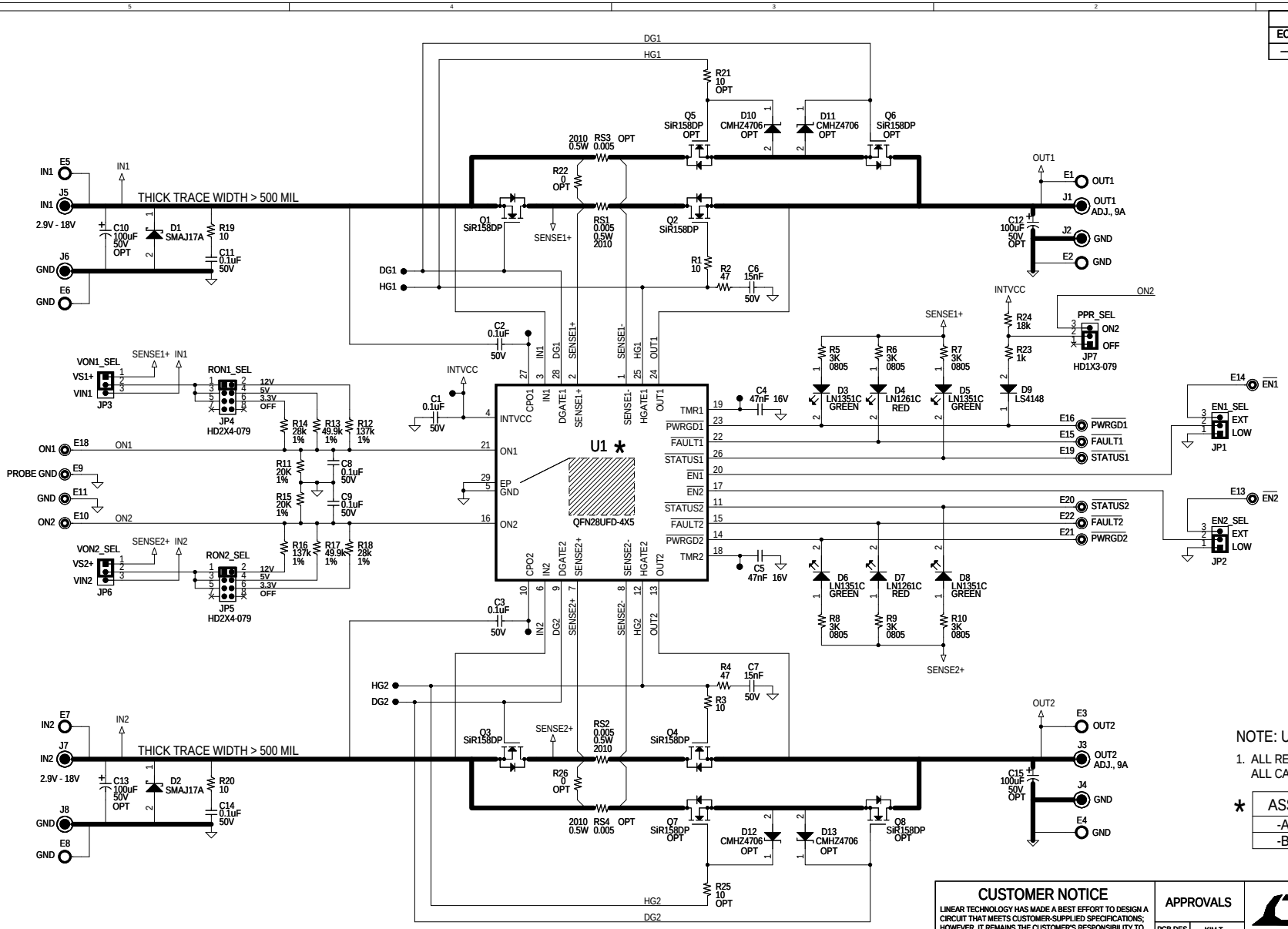

REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
—	1	1ST PROTOTYPE	VLADIMIR O.	08-07-12

NOTE: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE IN OHMS, 0603.
   ALL CAPACITORS ARE IN MICROFARADS, 0603.

*	ASSY	U1
	-A	LTC4228IUFD-1
	-B	LTC4228IUFD-2

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS	
PCB DES.	KIM T.
APP ENG.	VLADIMIR O.

SCALE = NONE

LINEAR TECHNOLOGY
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LTC Confidential-For Customer Use Only

TITLE: SCHEMATIC

DUAL IDEAL DIODE AND HOT SWAP CONTROLLER

SIZE	IC NO.	REV.
B	LTC4228IUFD-1/-2 DEMO CIRCUIT 1899A	1

DATE: 08/07/2012, 04:08 PM