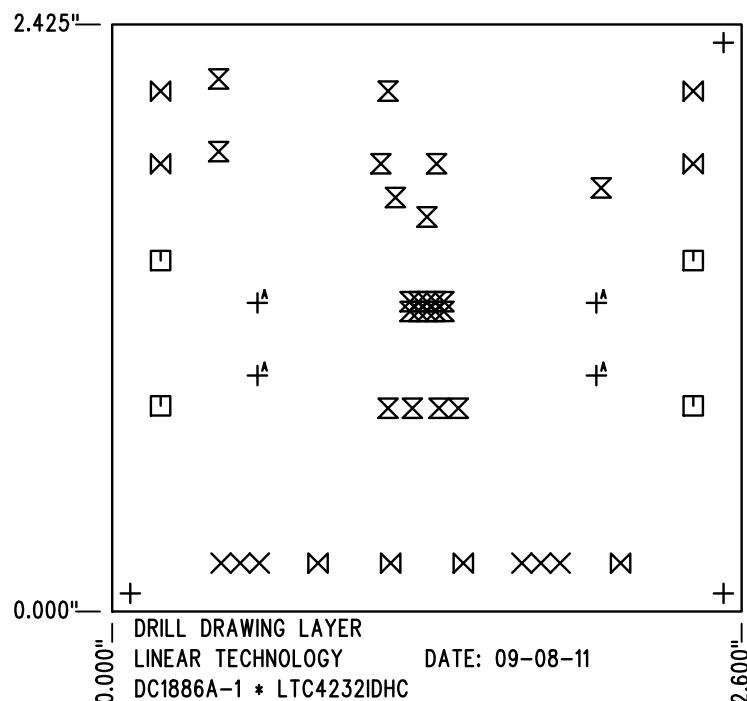


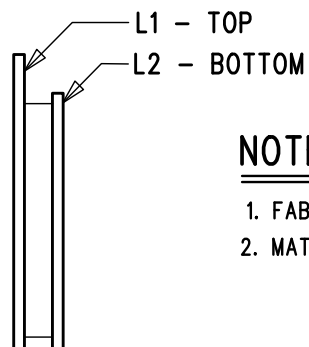
SHOWN FROM TOP SIDE



DRILL DRAWING LAYER
LINEAR TECHNOLOGY DATE: 09-08-11
DC1886A-1 * LTC4232IDHC
5A INTEGRATED HOT SWAP CONTROLLER

SIZE	QTY	SYM	PLATED	TOL
0.07	3	+	NO	+/-0.003"
0.035	6	X	YES	+/-0.003"
0.094	4	□	YES	+/-0.003"
0.012	22	⊗	YES	+/-0.003
0.064	8	⊗	YES	+/-0.003"
0.055	4	+	YES	+/-0.003"

LAYER STRUCTURE

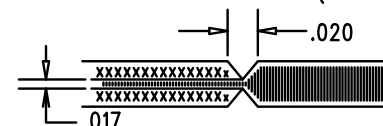


REVISION HISTORY

ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	1	1ST PROTOTYPE	VLADIMIR O.	09-08-11

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 2 LAYERS WITH 2 OZ. CU. ON EACH.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



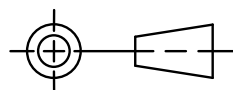
UNLESS OTHERWISE SPECIFIED

DIMENSIONS ARE IN INCHES
TOLERANCES:

0.XX" = ±0.01"

0.XXX" = ±0.005"

INTERPRET DIM AND TOL
PER ASME Y14.5M-1994
THIRD ANGLE PROJECTION



APPROVALS

PCB DES.	KIM T.
APP ENG.	VLADIMIR O.

SCALE = NONE

LINEAR TECHNOLOGY

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MILPITAS, CA 95035
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www.linear.com
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FOR CUSTOMER USE ONLY

TITLE: FABRICATION DRAWING

5A INTEGRATED HOT SWAP CONTROLLER

SIZE	IC NO.	REV
N/A	LTC4232IDHC DEMO CIRCUIT 1886A	1

FILENAME: DC1886A-1.PCB

SHT 1 OF 1