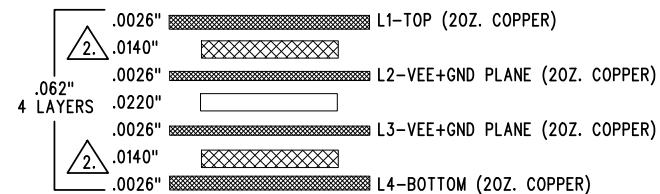


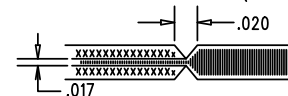
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	2	PRODUCTION	DILIAN R.	02-08-18

## LAYER STRUCTURE

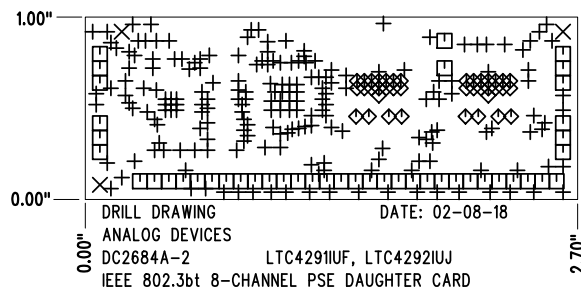


## NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.  
-FINISHED THICKNESS TO BE 0.062" +/- .005"  
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON ALL LAYERS  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



SHOWN FROM TOP SIDE



SIZE	QTY	SYM	PLATED	TOL
0.012	202	+	YES	+/-0.003"
0.07	3	X	NO	+/-0.003"
0.035	44	□	YES	+/-0.003"
0.017	38	◇	YES	+/-0.003"

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION	APPROVALS		1630 MCCARTHY BLVD. MILPITAS, CA 95035 TEL: (408)432-1900
	PCB DES.	KIM T.	
	APP ENG.	DILIAN R.	TITLE: FABRICATION DRAWING IEEE 802.3bt 8-CHANNEL PSE DAUGHTER CARD
	SIZE	IC NO.	REV.
	N/A	LTC4292IUJ, LTC4291UF DEMO CIRCUIT 2684A	2
SCALE = NONE		FILENAME: DC2684A-2.PCB	SHT 1 OF 1