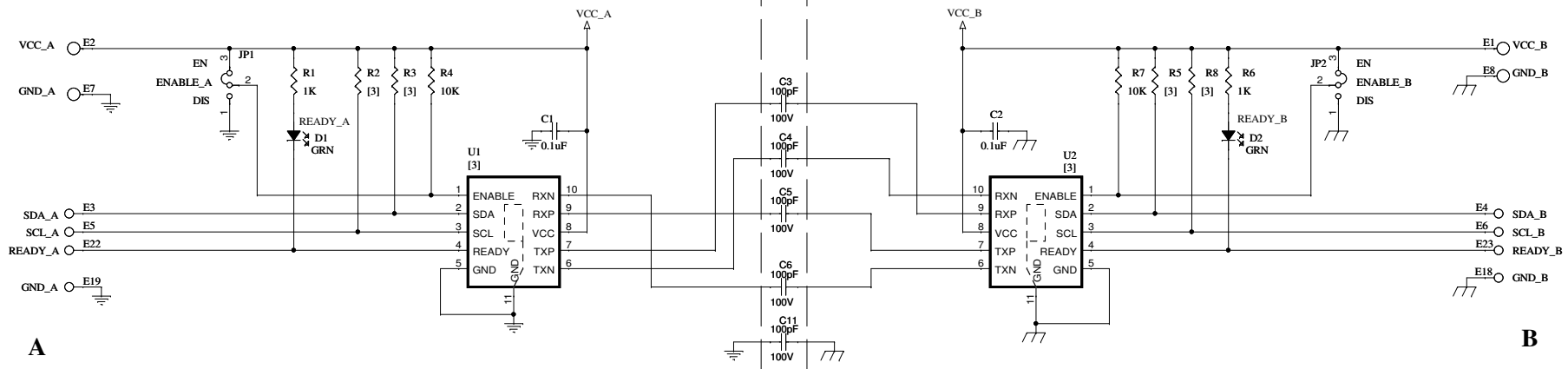


REVISION HISTORY				
EDD	REV	DESCRIPTION	DATE	APPROVED
	3	PRODUCTION	04/08/10	DILIAN R.

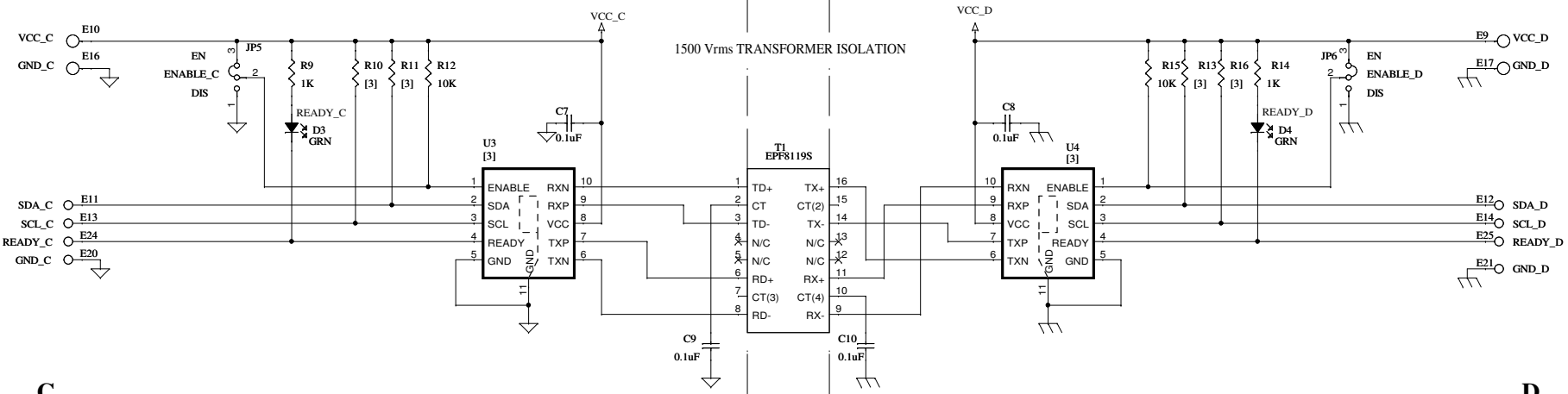
CAPACITOR ISOLATION



A

B

1500 Vrms TRANSFORMER ISOLATION



C

D

- NOTES: UNLESS OTHERWISE SPECIFIED,  
 1. ALL CAPACITOR AND RESISTOR CASE SIZE ARE 0603.  
 2. INSTALL SHUNTS AS SHOWN.

[3]	ASSY	U1-U4	R2, R3, R5, R8, R10, R11, R13, R16
	-A	LTC4310CD-1	10k
	-B	LTC4310CD-2	3.3k

<b>CUSTOMER NOTICE</b> LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.		<b>APPROVALS</b> PCB DES: MI APP ENG: DILIAN R.			1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 www.linear.com Fax: (408)434-0507 LTC Confidential-For Customer Use Only	
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.		SCALE = NONE			<b>TITLE: SCHEMATIC</b> <b>ISOLATED I2C THROUGH SPLITTER</b> SIZE: N/A IC NO: LTC4310CDD-1/-2 DEMO CIRCUIT 1079A DATE: Monday, August 02, 2010	
				REV. 1		
				SHEET 1 OF 1		