

SIZE	QTY	SYM	PLATED	TOL
0.9	28	+	YES	+/-0.0
1.4	16	X	YES	+/-0.0
3.17	4	□	NO	+/-0.0
1.6	4	◇	YES	+/-0.0
0.9	16	⊗	YES	+/-0.0
0.85	91	⊗	YES	+/-0.0
1.05	68	+ <sup>A</sup>	YES	+/-0.0
0.8	9	+ <sup>B</sup>	YES	+/-0.0
1.9	9	+ <sup>C</sup>	YES	+/-0.0
4.9	8	+ <sup>D</sup>	NO	+/-0.0
0.254	217	+ <sup>E</sup>	YES	+/-0.0
0.2032	11	+ <sup>F</sup>	YES	+/-0.0

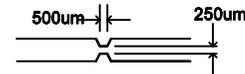
PCB FABRICATION # 600-DC2754 REV03

### REVISION HISTORY

ECO	REV	DESCRIPTION	APP. ENG.	DATE
	3	Production	E Benedict	4 OCT 18

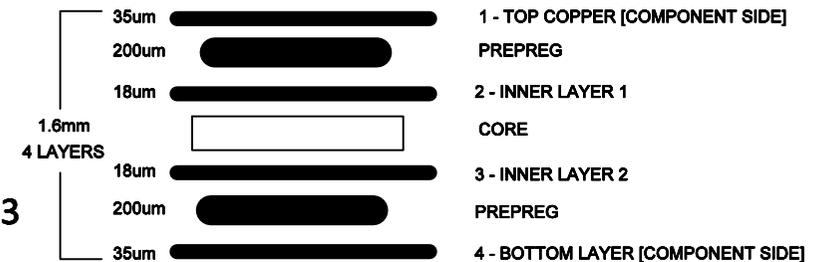
### NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-800.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HA or EQUIVALENT  
-FINISHED THICKNESS TO BE 1.6mm  
-TOTAL OF 4 LAYERS WITH 1 OZ. Cu ON THE EXTERNAL LAYERS AND 0.5 OZ. Cu ON THE INTERNAL LAYERS.  
-FLAMMABILITY RATING: 94 V-O MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0.00 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 25um THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE ±75um IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION (ENIG) BOTH SIDES.  
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:

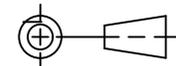


⑨ SCORE HERE, BUT DO NOT BREAK AT THIS LOCATION. BREAK BOARDS AS A SET.

### LAYER STRUCTURE



UNLESS OTHERWISE SPECIFIED  
DIMENSIONS ARE METRIC  
TOLERANCES:  
0.5mm = ±0.05mm  
0.5 - 6mm = ±0.1mm  
6 - 30mm = ±0.2mm  
30 - 120mm = ±0.3mm  
INTERPRET DIM AND TOL  
PER ASME Y14.5M-1994  
THIRD ANGLE PROJECTION



### APPROVALS

PCB DES.	EB
APP ENG.	EB
SCALE = NONE	



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TITLE: FABRICATION DRAWING  
DEMO CIRCUIT  
RUGGED I2C SLAVE DEVICE EXTENDER

SIZE	IC NO.	REV.
N/A	LTC4331 DEMO CIRCUIT DC2754A	3