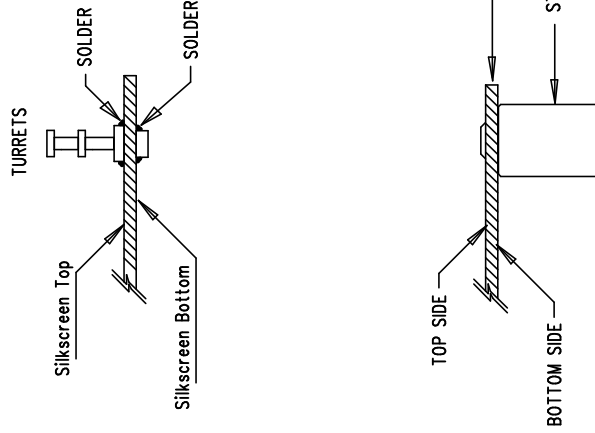
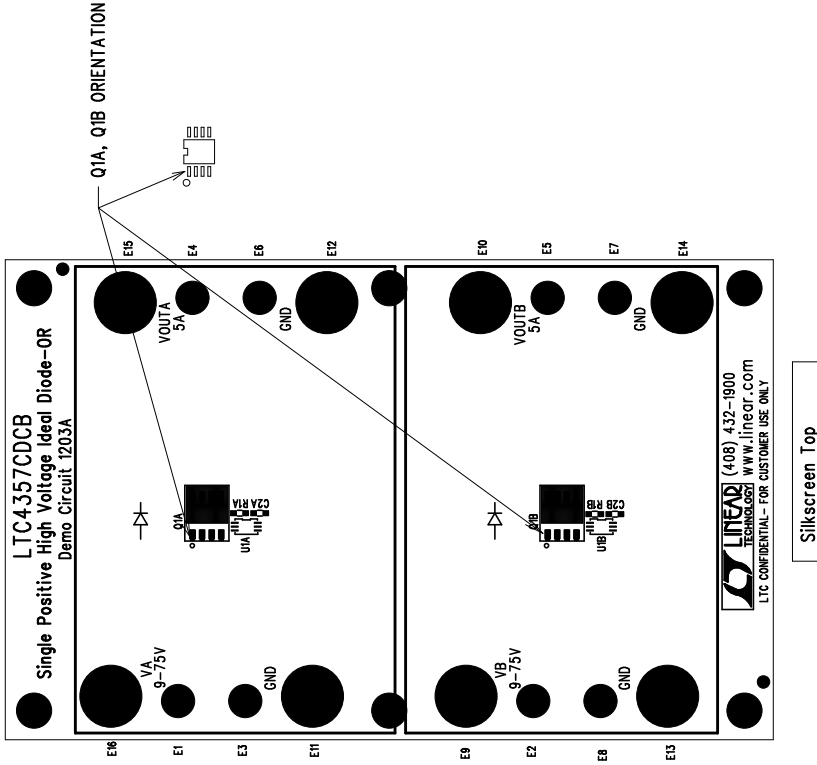


REVISIONS			
REV	DESCRIPTION	APPR	DATE



## NOTES : Unless Otherwise Specified

1. BREAK BOARDS FROM THE PANEL WHEN ASSEMBLY IS COMPLETE.
2. OPTIONAL COMPONENTS ARE UNSTUFFED AND SHOULD BE FREE OF SOLDER AFTER ASSEMBLY.

## ASSY DRAWING

APPROVALS		LINEAR TECHNOLOGY	
INIT	DATE	1630 McCarthy Blvd. Milpitas, CA 95035 PH: (408)432-1900	
PCB DESIGNER	J. WU	4/9/07	
ENGINEER	M. Lee	4/9/07	
CHECK			
SCALE = NONE		TITLE: LTC4357CDCB, Single Positive High Voltage Ideal Diode-OR	
		SIZE	REV. 1
		A	DC1203A
		SHT 1 of 1	