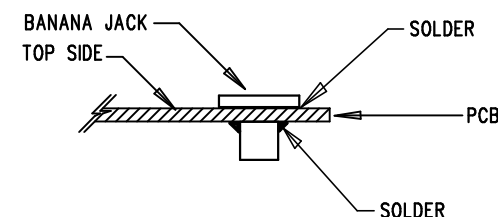


## REVISION HISTORY

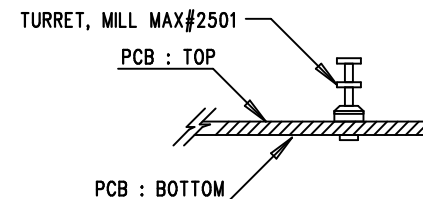
ECO	REV	DESCRIPTION	APPR	DATE
—	4	PRODUCTION	MITCHELL LEE	05-31-12

## NOTES: UNLESS OTHERWISE SPECIFIED

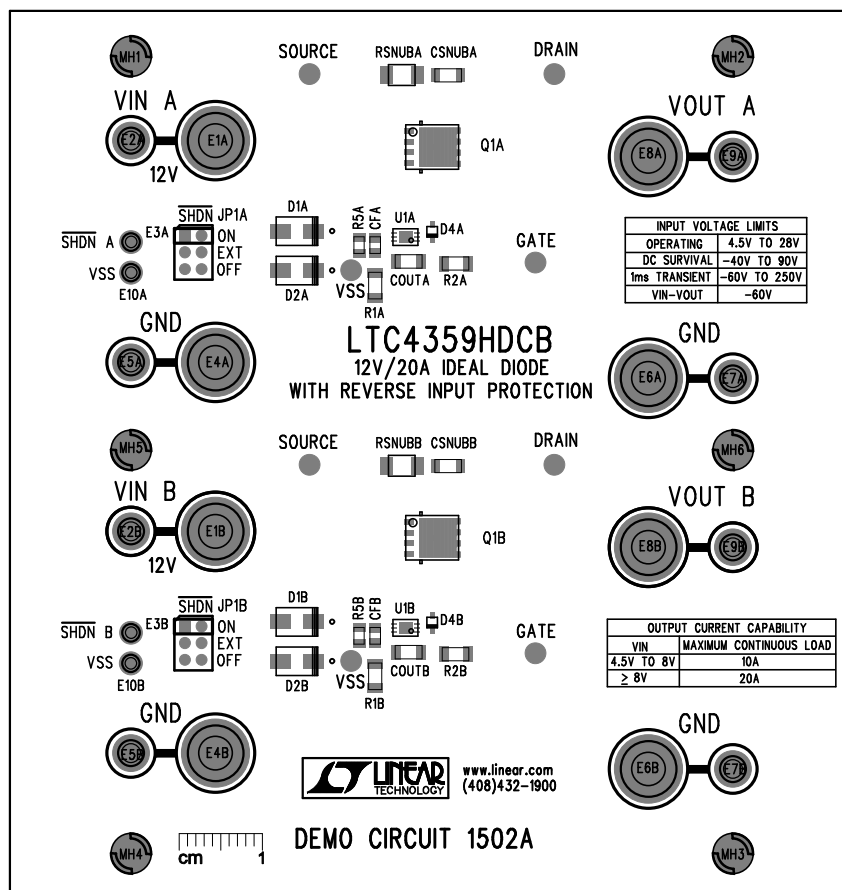
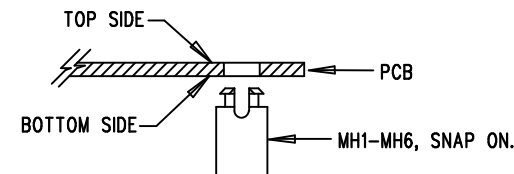
1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY PROCESS SHALL INCLUDE REFLOW SOLDER BOTH SIDES. MAXIMUM SOLDER TEMPERATURE SHALL BE 240 DEGREES CELSIUS.
3. OMITTED (OPTIONAL) PARTS ARE SPECIFIED ON THE BILL OF MATERIALS. LAND PATTERNS FOR THESE PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL AT THESE LOCATIONS.
4. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON ALL BOARD EDGES.
5. DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD.
6. INSTALL SHUNTS AT JP1A, JP1B IN POSITIONS AS SHOWN.
7. INSTALL BANANA JACKS E1A,E1B,E4A,E4B,E6A,E6B,E8A,E8B AS SHOWN BELOW:



8. INSTALL TURRETS E2A,E2B,E3A,E3B,E5A,E5B,E7A,E7B, E9A,E9B AS SHOWN BELOW:



9. INSTALL STANDOFFS AT 6 LOCATIONS AS SHOWN BELOW:



## APPROVALS

PCB DES. M. HAWKINS  
APP ENG. MITCHELL LEE



1630 MCCARTHY BLVD  
MILPITAS, CA 95035  
PH: (408)432-1900  
www.linear.com  
LTC CONFIDENTIAL-  
FOR CUSTOMER USE ONLY

TITLE: ASSEMBLY DRAWING, TOP  
12V/20A IDEAL DIODE  
WITH REVERSE INPUT PROTECTION

SIZE N/A IC NO. LTC4359HDCB  
DEMO CIRCUIT 1502A REV. 4

SCALE = NONE

FILENAME: DC1502A-4.PCB

SHT 1 OF 1