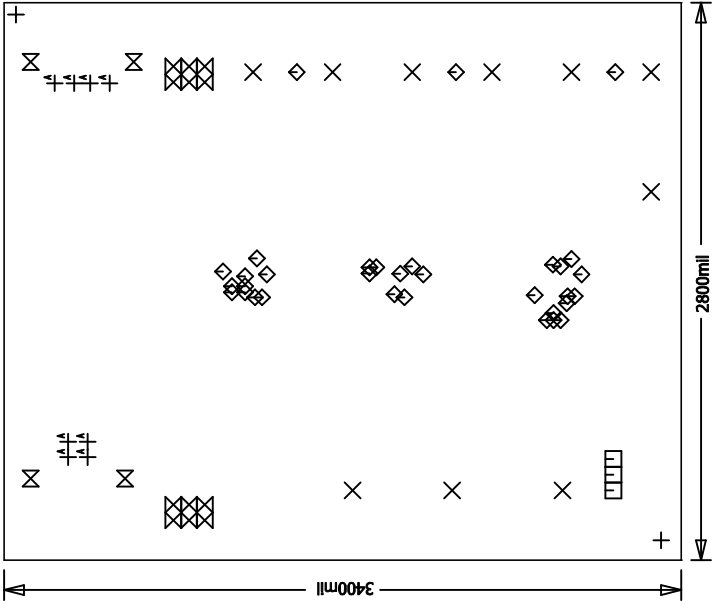


Linear Tech. Corp.
Demo Circuit 1505A
Component Side



ECO

REV

DESCRIPTION

APPR

DATE

—

3

PRODUCTION

DILIAN R.

04/30/10

—

4

Replaced all LED's, CLD1&2 and Q1.

DILIAN R.

Aug 26, 2015

NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600

2. MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE
OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM .

3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.

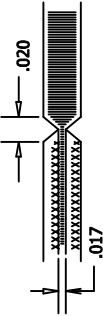
4. DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, 0.001" THICK MIN.
ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
HOLE LOCATION TOLERANCES ARE +/-0.003"
IN RELATION TO CENTER

5. FINISH: -SMOBC USING LPT BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.

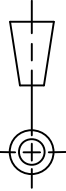
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.

7. PCBs ARE TO BE RoHS COMPLIANT .

8. SCORING FOR PANELIZED PCB:



UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES:
0.00" = +0.01"
0.000" = +0.005"
INTERPRET DIM AND TOL
PER ASME Y14.5M-1994
THIRD ANGLE PROJECTION



APPROVALS

PCB DES.

APP ENG.

REV

DILIAN R.


SCALE = NONE

FILENAME: DC1505A-4.PCB

SHT 1 OF 1

SIZE	QTY	SYM	PLATED	TOL
70	2	+	NO	+/-3mil
94	10	X	YES	+/-3mil
31	3	□	YES	+/-3mil
10	33	◇	YES	+/-3mil
34	12	⊗	YES	+/-3mil
100	4	⊗	YES	+/-3mil
40	8	⊕	YES	+/-3mil

1630 MCARDY BLVD
MILPITAS, CA 95035
PH: (408)432-1900
WWW.LINEAR.COM
LTC CONFIDENTIAL-
FOR CUSTOMER USE ONLY

 **LINEAR**
TECHNOLOGY

TITLE: FABRICATION DRAWING
OVERVOLTAGE PROTECTION CONTROLLER

SIZE

REV

IC NO. LTC4360CS08-1/LTC4360CS08-2

DEMO CIRCUIT 1505A

4