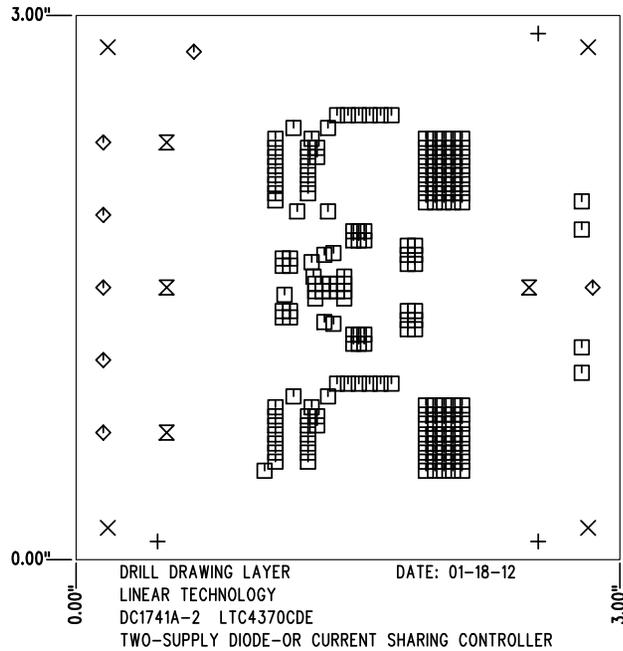


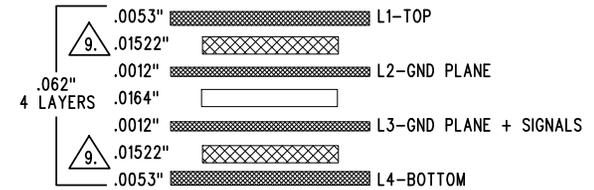
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	2	2ND PROTOTYPE	VLADIMIR O.	01-18-12

SHOWN FROM TOP SIDE



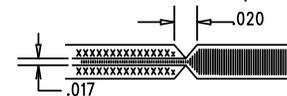
SIZE	QTY	SYM	PLATED	TOL
70	3	+	NO	+/-0.003"
190	4	X	NO	+/-0.003"
12	188	□	YES	+/-0.003"
64	7	◇	YES	+/-0.003"
215	4	⊗	YES	+/-0.003"

### LAYER STRUCTURE



### NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.  
-FINISHED THICKNESS TO BE 0.062" +/- .005"  
-TOTAL OF 4 LAYERS WITH 4 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR BURGUNDY (PMS 202).  
-GOLD IMMERSION BOTH SIDES.  
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



9. SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC CONSTANT DEVIATIONS. PLEASE CONSULT LTC.

UNLESS OTHERWISE SPECIFIED		APPROVALS		LINEAR TECHNOLOGY	
DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		PCB DES.	KIM T.	1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 WWW.LINEAR.COM LTC CONFIDENTIAL - FOR CUSTOMER USE ONLY	
		APP ENG.	VLADIMIR O.		
TITLE: FABRICATION DRAWING				SIZE	IC NO.
TWO-SUPPLY DIODE-OR CURRENT SHARING CONTROLLER				N/A	LTC4370CDE
SCALE = NONE				FILENAME:	REV
DC1741A-2.PCB				DC1741A-2.PCB	2
SHT 1 OF 1					