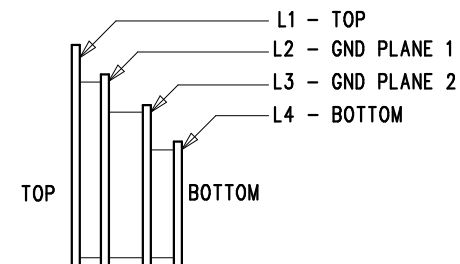


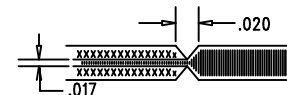
SIZE	QTY	SYM	PLATED	TOL
0.07	2	+	NO	+/-0.003
0.095	18	X	YES	+/-0.003
0.187	4	□	NO	+/-0.003
0.205	12	◇	YES	+/-0.003
0.01	395	⊗	YES	+/-0.003


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPR	DATE
-	2	2ND PROTOTYPE	JOSH Y.	11-09-10



NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB.



UNLESS OTHERWISE SPECIFIED		APPROVALS		 LINEAR TECHNOLOGY 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		PCB DES.	AK		
		APP ENG.	JOSH Y.	TITLE: FABRICATION DRAWING	
				DUAL LOW LOSS 36V POWERPATH CONTROLLERS	
				SIZE	IC NO. LTC4412HVIS6 & LTC4414EMS8
				N/A	DEMO CIRCUIT 1635A
		SCALE = NONE		FILENAME: DC1635A-2.PCB	REV 2
				SHT 1 OF 1	