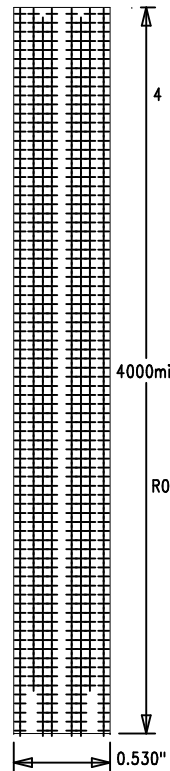
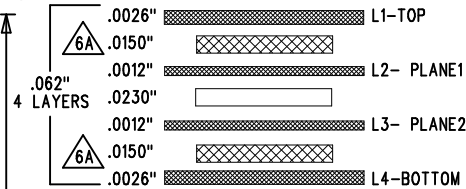


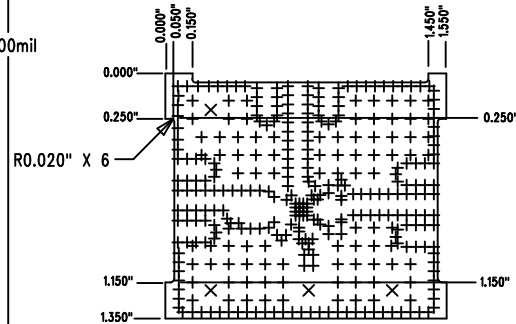
LTC DESIGNED
IMPEDANCE TEST
STRUCTURE COUPON



LAYER STRUCTURE



DO NOT ALTER ANY COPPER PLANES IN ALL LAYERS



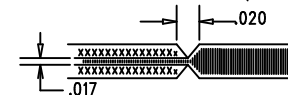
DRILL DRAWING LAYER

LINEAR TECHNOLOGY DATE: 04-08-2013
DC1983A-2 * LTC5510IUF
WIDEBAND ACTIVE MIXER HIGH FREQUENCY OUTPUT

NOTES: UNLESS OTHERWISE SPECIFIED

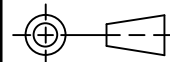
- FAB PER IPC-A-600.
- MATERIAL: -EPOXY FIBERGLASS, NELCO 4000-13 EP
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- CONTROLLED 50 OHM +/-5% IMPEDANCE FOR LAYER 1-2 TRACE WIDTH 30 MILS. MUST BE TESTED ON LTC DESIGNED IMPEDANCE TEST STRUCTURE COUPON.
EVERY PANEL MUST BE FABRICATE WITH ONE LTC DESIGNED IMPEDANCE TEST STRUCTURE COUPON
MARK PANEL NUMBER ON BOARDS AND COUPON

- △ SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC CONSTANT DEVIATIONS. PLEASE CONSULT LTC.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
 - PCBS ARE TO BE RoHS COMPLIANT.
 - SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



SIZE	QTY	SYM	PLATED	TOL
0.008	981	+	YES	+/-0.003
0.094	4	X	YES	+/-0.003

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES:
0.XX" = ±0.01"
0.XXX" = ±0.005"
INTERPRET DIM AND TOL
PER ASME Y14.5M-1994
THIRD ANGLE PROJECTION



APPROVALS

PCB DES.	AK
APP ENG.	SUNNY H.
SCALE = NONE	



1630 MCCARTHY BLVD
MILPITAS, CA 95035
PH: (408)432-1900
www.linear.com
LTC CONFIDENTIAL-
FOR CUSTOMER USE ONLY

TITLE: FABRICATION DRAWING
WIDEBAND ACTIVE MIXER
HIGH FREQUENCY OUTPUT

SIZE	IC NO.	REV
N/A	LTC5510IUF DEMO CIRCUIT 1983A	2
FILENAME:	DC1983A-2.PCB	SHT 1 OF 1