



SIZE	QTY	SYM	PLTD
10	93	Y	PLTD
95	4	Z	PLTD

.0028"	Microstrip
⑦ .017"	Prepreg
.0014"	Microstrip
.062"	Core Material
4 LAYERS	Microstrip
.0014"	Prepreg
⑦ .017"	Microstrip
.0028"	Microstrip

FAB DRAWING

APPROVALS		LINEAR TECHNOLOGY		1630 McCarthy Blvd. Milpitas, CA 95035 PH: (408)432-1900	
DRAWN	INIT	DATE			
CHECK					
DESIGN					
ENGR					
TITLE: LTC5532ES6			RF Power Detector 300MHz to 700MHz		
SIZE	A	DEMO	DC656A	REV.	
SCALE = NONE			SHT 1 of 1		

REVISIONS		
REV	DESCRIPTION	DATE

NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600
2. MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4
FINISHED THICKNESS TO BE .062 +/- .005 INCH
WITH 2 OZ. COPPER ON TWO OUTER LAYERS AND
1 OZ. COPPER ON TWO INTERNAL LAYERS.
FLAMABILITY RATING: 94 V-0 MINIMUM .
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
4. DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, .001 INCH THICK MIN. ALL
HOLE SIZES ARE SPECIFIED AFTER PLATING.
HOLE LOCATION TOLERANCES ARE +/- .003
INCH IN RELATION TO CENTER
5. FINISH: SMOBC USING WET MASK, OR LPI BOTH SIDES, GREEN
PREFERRABLE. SILKSCREEN COMPONENT SIDE WITH
WHITE NON-CONDUCTIVE INK.
6. CONTROLLED 50 OHM IMPEDANCE (AT 2.5 GHz FREQ.) FOR
LAYER 1-2 AND LAYER 4-3.
- ⑦ SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC
CONSTANT DEVIATIONS. PLEASE CONSULT LTC.
8. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
9. WHEN PANELIZED THIS BOARD, PLEASE MERGE TEST BOARD TOGETHER.
THE TEST BOARD NEED TO BE BUILT IN THE SAME PANEL. MIN. BUILD
ONE TEST BOARD EACH PANEL.