

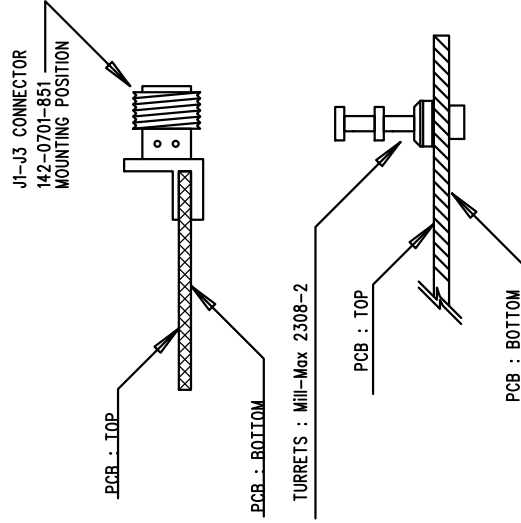
TOP ASSEMBLY
LINEAR TECHNOLOGY DATE: 11-24-15
DC2461A-2 * LTC5548
2-14 GHz PASSIVE MIXER

LTC DESIGNED
IMPEDANCE TEST
STRUCTURE COUPON

REVISION HISTORY			
ECO	REV	DESCRIPTION	APP. ENG. DATE
-	2	PRODUCTION	WESTON S.11-24-15

NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD. MAXIMUM SOLDER TEMPERATURE IS 240 DEGREES CELSIUS.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS. LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. NO SHUNT
5. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
6. DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD.
7. INSTALL J1-J3 CONNECTORS AS SHOWN BELOW:



APPROVALS	
PCB DES.	SM
APP ENG.	WESTON S.
TITLE: TOP ASSEMBLY DRAWING	
2-14 GHz PASSIVE MIXER	
SIZE	IC NO. LTC5548
N/A	REV. 2
FILENAME: LT5548.PCB	
SHT 1 OF 2	

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