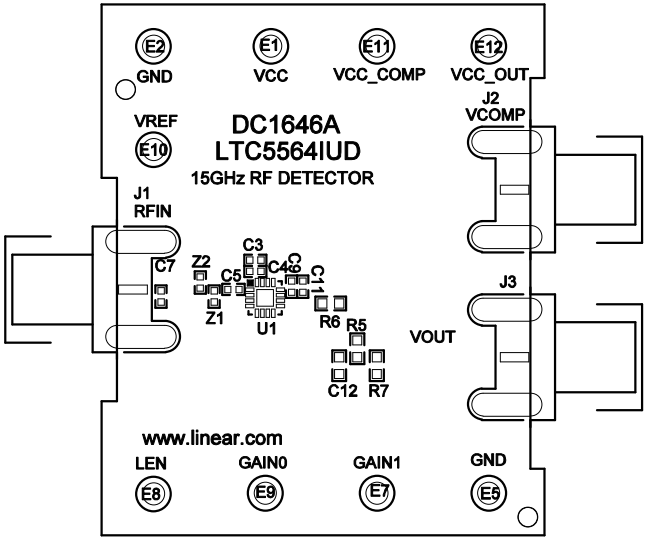
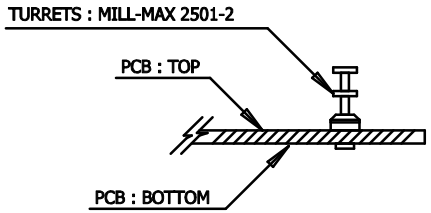
1646A_Rev3.pcb - Tue Sep 07 10:49:57 2010

REVISION HISTORY				
ECO	REV	DESCRIPTION	APPR	DATE
-	3	PRODUCTION	VLAD D.	08/11/10



NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILLS OF MATERIAL. LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
5. DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD.



TOP SILKSCREEN
LINEAR TECHNOLOGY DATE: 08/11/10
DC1646A-3 - LTC5564IUD
15GHz RF DETECTOR

APPROVALS	
PCB DES.	RB
APP ENG.	VLAD D.
SCALE = NONE	

LINEAR TECHNOLOGY
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LTC CONFIDENTIAL-
FOR CUSTOMER USE ONLY

TITLE: TOP ASSEMBLY DRAWING

15GHz RF DETECTOR

SIZE	IC NO.	REV.
N/A	LTC5564IUD DEMO CIRCUIT 1646A	3

FILENAME: 1646A-3.PCB