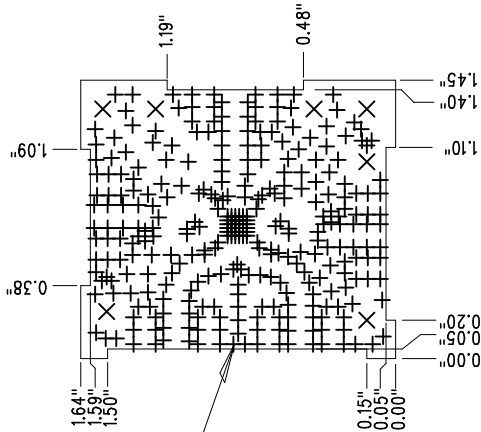


Do Not Alter Any Copper Planes In All Layers.



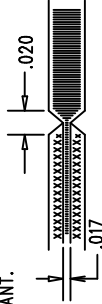
SIZE	QTY	SYM	PLATED	TOL
0.01	315	+	YES	+/-0.003
0.065	7	X	YES	+/-0.003

⑦ Dielectric Thickness
approx 15 mils, 2X.
LYR1 XXXXXXXXXXXX PREPREG XXXXXXXXXXXX
LYR2 XXXXXXXXXXXX PREPREG XXXXXXXXXXXX
LYR3 XXXXXXXXXXXX CORE XXXXXXXXXXXX
LYR4 XXXXXXXXXXXX PREPREG XXXXXXXXXXXX
LAYER STRUCTURE

REVISION HISTORY			
ECO	REV	DESCRIPTION	APP. ENG.
-	2	PRODUCTION	V.LAD. D.
-	2		8-31-10

NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -EPOXY FIBERGLASS, NELCO 4000-13
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS
AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003"
IN RELATION TO CENTER
5. FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
(LEAD FREE SOLDER CAN BE USED FOR PROTOTYPE)
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
6. CONTROLLED 50 OHM +/-5% IMPEDANCE FOR LAYER 1-2
AND LAYER 3-4 TRACE WIDTH 30MIL
- ⑦ SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC
CONSTANT DEVIATIONS. PLEASE CONSULT LTC.
8. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
9. PCBs ARE TO BE RoHS COMPLIANT.
10. SCORING FOR PANELIZED PCB:



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		APPROVALS	
	PCB DES.	JW	
	APP. ENG.	V.LAD. D.	
SCALE = NONE		TITLE: FABRICATION DRAWING DUAL HIGH DYNAMIC RANGE DOWN CONVERTING MIXER	
SIZE	IC NO.	LTC559XIUH	REV
N/A		DEMO CIRCUIT 1710A	2
FILENAME: 1710A-2.PCB		SHT 1 OF 1	

LINEAR TECHNOLOGY
1630 MCCARTHY BLVD
MILPITAS, CA 95035
PH: (408)432-1800
WWW.LINEAR.COM
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