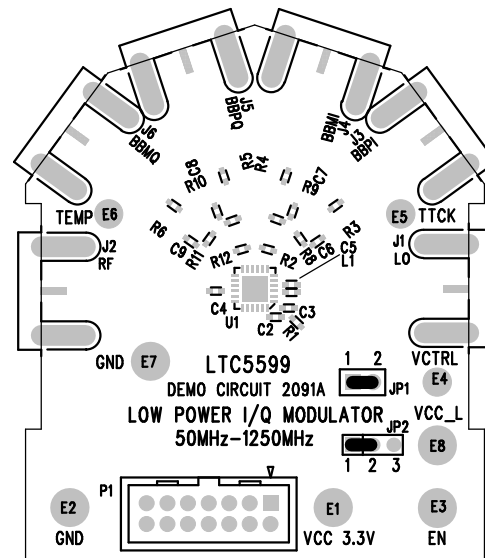


REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	3	3RD PROTOTYPE	WESTON S.	03-04-14

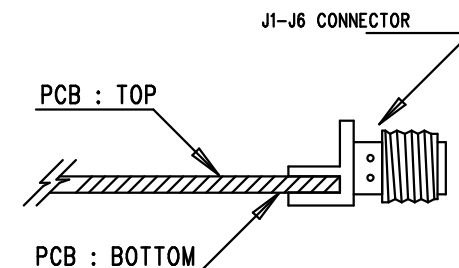



TOP SILKSCREEN

LINEAR TECHNOLOGY
DATE: 03-04-14
LOW POWER I/Q MODULATOR
50MHz - 1250MHz
DC2091A3-LTC5599IUF

NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD. MAXIMUM SOLDER TEMPERATURE IS 240 DEGREES CELSIUS.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS. LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. INSTALL SHUNTS AS SHOWN ON ASSY DRAWING.
5. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
6. DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD.



APPROVALS				1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
PCB DES.	AK	TITLE: TOP ASSEMBLY DRAWING LOW POWER I/Q MODULATOR 50MHz - 1250MHz			
APP ENG.	WESTON S.				
		SIZE	IC NO.	LTC5599IU	REV.
		N/A		DEMO CIRCUIT 2091A	3
SCALE = NONE		FILENAME: DC2091A-3.PCB			SHT 1 OF 1