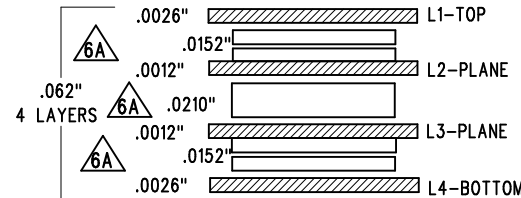


LAYERING STRUCTURE

REVISION HISTORY

ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	3	3RD PROTOTYPE	WESTON S.	03-04-14

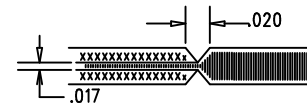


- NOTES :
1. FAB PER IPC-A-600.
 2. MATERIAL: -EPOXY FIBERGLASS, NELCO 4000-13
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
 3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
 4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
 5. FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.

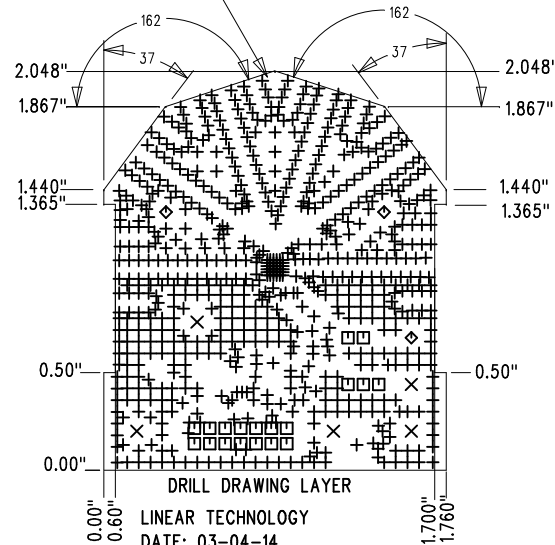
6. CONTROLLED 50 OHM +/-5% IMPEDANCE FOR LAYER 1-2
TRACE WIDTH 30 MILS. MUST BE TESTED ON LTC DESIGNED IMPEDANCE TEST STRUCTURE COUPON.
EVERY PANEL MUST BE FABRICATE WITH ONE LTC DESIGNED IMPEDANCE TEST STRUCTURE COUPON
MARK PANEL NUMBER ON BOARDS AND COUPON

6A SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC CONSTANT DEVIATIONS. PLEASE CONSULT LTC.

7. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
8. PCBs ARE TO BE RoHS COMPLIANT.
9. SCORING FOR PANELIZED PCB



DO NOT MODIFY COPPER POUR IN ALL LAYERS.
NO TABS ON SMA CONNECTOR AREA.
SMA CONNECTOR NEEDS A SHARP EDGE



DRILL DRAWING LAYER
LINEAR TECHNOLOGY
DATE: 03-04-14
LOW POWER I/Q MODULATOR
50MHz - 1250MHz
DC2091A3-LTC5599IUF

SIZE	QTY	SYM	PLATED	TOL
0.006	713	+	YES	+/-0.003
0.096	5	X	YES	+/-0.003
0.035	19	□	YES	+/-0.003
0.066	3	◇	YES	+/-0.003

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		APPROVALS PCB DES. AK APP ENG. WESTON S.		<div> <div> 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY </div> </div>
				TITLE: FABRICATION DRAWING LOW POWER I/Q MODULATOR 50MHz - 1250MHz
				SIZE N/A
				IC NO. LTC5599IUF DEMO CIRCUIT 2091A
				REV 3
		SCALE = NONE		FILENAME: DC2091A-3.PCB
				SHT 1 OF 1