

1. Notes:

System interface not shown, connect UART and other signals as required and per integration guidelines:  
the UART signals for the API interface  
UART\_TX, UART\_TX\_RTSn, UART\_TX\_CTSn,  
UART\_RX, UART\_RX\_RTSn, UART\_RX\_CTSn  
the generic GPIO DP4  
and the specific IO TIMEn


2. Associated Documents



Revision History:

Rev	Description	ECO	Author
01	Initial release	1186	Ric Peregrino
02	Update Oski symbol per datasheet	1311	Ric Peregrino
03	Add TIMEn pullup, RADIO_INHIBIT pull down	1388	Ric Peregrino
04	Adjust shield part properties, editorial change only	1441	Ric Peregrino



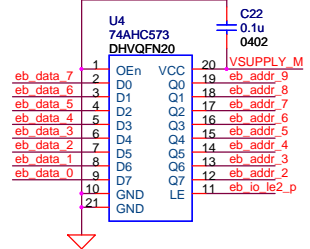
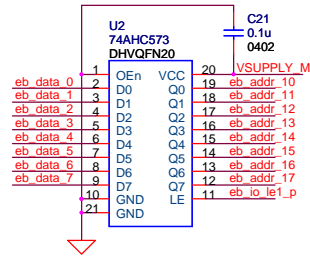
LTC CONFIDENTIAL - FOR CUSTOMER USE ONLY		CONTRACT NO.		 <b>A Linear Technology Company</b> 1630 McCarthy Blvd. Phone: (408)432-1900 Milpitas, CA 95035 Fax: (408)434-0507	
<b>CUSTOMER NOTICE</b>  LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.		APPROVALS			
		DRAWN:			
		CHECKED:			
		APPROVED:			
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND IS SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.		ENGINEER:		TITLE: <b>PCA SCH, ETERNA EXTERNAL MEMORY REFERENCE, ELWHA</b>	
		DESIGNER:			
		SIZE B	DWG NO. 710-0211		REV 04
		DATE: Friday, January 09, 2015			SHEET 1 OF 3



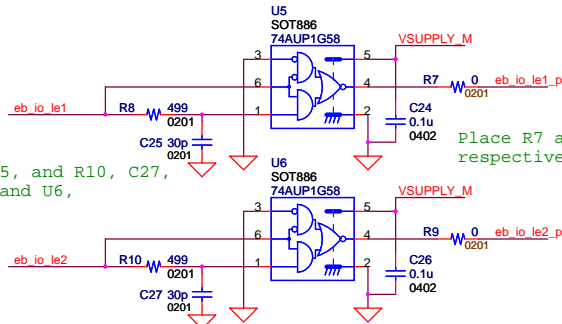
25 mil traces for VSUPPLY\_M

VSUPPLY\_M >>> VSUPPLY\_F

eb\_io\_cs0n >>> eb\_io\_cs0n  
 eb\_io\_le1 >>> eb\_io\_le1  
 eb\_io\_le2 >>> eb\_io\_le2  
 eb\_addr\_0 >>> eb\_addr\_0  
 eb\_addr\_1 >>> eb\_addr\_1  
 eb\_data\_0 >>> eb\_data\_0  
 eb\_data\_1 >>> eb\_data\_1  
 eb\_data\_2 >>> eb\_data\_2  
 eb\_data\_3 >>> eb\_data\_3  
 eb\_data\_4 >>> eb\_data\_4  
 eb\_data\_5 >>> eb\_data\_5  
 eb\_data\_6 >>> eb\_data\_6  
 eb\_data\_7 >>> eb\_data\_7  
 eb\_io\_oen >>> eb\_io\_oen  
 eb\_io\_wen >>> eb\_io\_wen



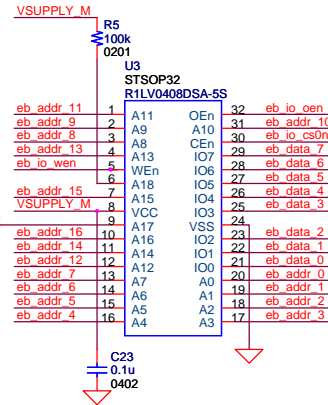
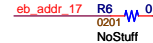
Pulse Generator Circuits




Place R8, C25, and R10, C27, close to U5 and U6, respectively

Place R7 and R9 close to U5 and U6, respectively

Stuff R6 for 256Kx8



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		CHECKED:					
		APPROVED:					
		ENGINEER:					
		DESIGNER:		SIZE B		DWG NO. 710-0211	
		DATE: Thursday, January 08, 2015		SHEET 3 OF 3			