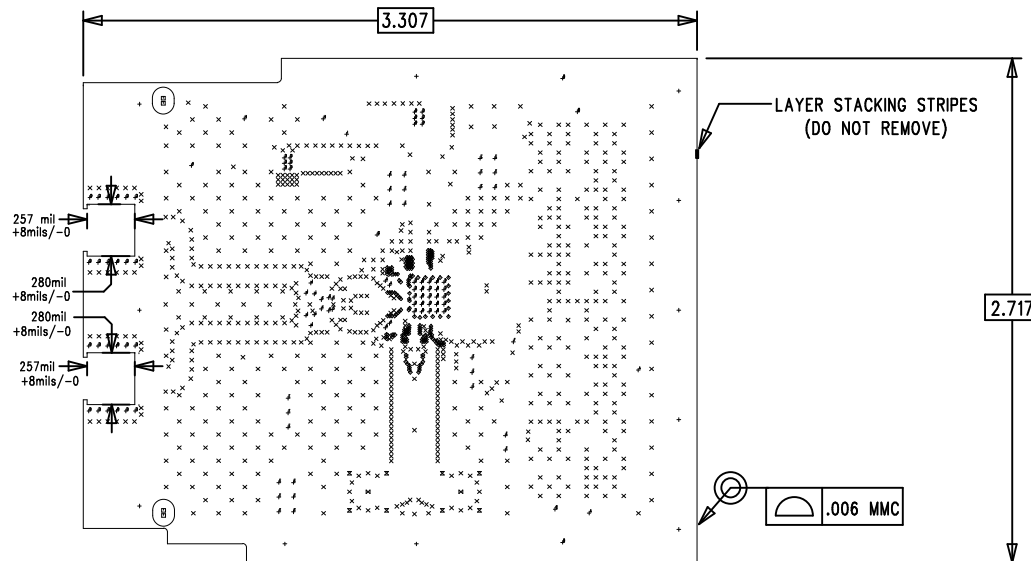


REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	1	PRODUCTION	C.MAYOTT	07-02-14

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL:
 - PCB'S SHALL BE RoHS COMPLIANT.
 - MATERIAL SHALL BE FR408HR OR EQUIVALENT.
 - FINISHED THICKNESS SHALL BE 0.062" \pm 10%.
 - TOTAL OF 6 LAYERS, FINISHED COPPER THICKNESS AS PER FIG.1, "LAYER STRUCTURE".
 - FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
- DRILLING:
 - DRILL HOLES PER SCHEDULE. HOLE PLATING SHALL BE 1.0 MILS THICK MINIMUM.
 - ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
 - HOLE LOCATION TOLERANCE FOR ALL HOLES SHALL BE 6 MILS DIAMETER TRUE POSITION.
- FINISH:
 - SMOBC USING LPI BOTH SIDES, COLOR GREEN.
 - ENIG FINISH BOTH SIDES IN ACCORDANCE WITH IPC-4552.
 - SILKSCREEN LEGENDS SHALL BE WHITE NON-CONDUCTIVE EPOXY INK.
- FABRICATOR SHALL NOT ADD LOGO OR DATE CODE TO ARTWORK. FABRICATOR MAY ADD SERIAL NUMBERS ON SECONDARY SIDE SILKSCREEN.
- BOARDS SHALL BE PANELIZED USING ROUTE AND RETAIN METHOD. PCB ARRAY SHALL HAVE TWO TOOLING RAILS ONLY, WITH THESE BEING ON OPPOSITE SIDES. NO TOOLING RAILS ARE ALLOWED ON SIDES ADJACENT TO PCB J1 AND J2.
- BOARD USES THREE DIFFERENT FIXED IMPEDANCE DESIGN GEOMETRIES AS FOLLOWS:
 - SURFACE MICROSTRIP PRIMARY SIDE, 20 MILS WIDE, IMPEDANCE 50 OHMS \pm 10% AT 2Ghz, REFERENCE PLANE LAYER 3.
 - SURFACE MICROSTRIP PRIMARY SIDE, 6 MILS WIDE, IMPEDANCE 50 OHMS \pm 10% AT 2Ghz, REFERENCE PLANE LAYER 2.
 - SURFACE MICROSTRIP SECONDARY SIDE, 20 MILS WIDE, IMPEDANCE 50 OHMS \pm 10% AT 2Ghz, REFERENCE PLANE LAYER 4.
- SPECIAL VIA REQUIREMENTS
 - ALL 12.0 MIL DIAMETER VIAS, QTY 61, SHALL BE IPC 4761 TYPE VII FILLED AND CAPPED VIAS. USE NON-CONDUCTIVE FILLER.
 - NOTE BLIND VIAS, 6.2 MIL DIA., QTY. 172, LAYER 1 TO LAYER 2.
- ELECTRICAL TEST STAMPS SHALL BE ON SECONDARY SIDE ONLY.



LINEAR TECHNOLOGY
DC2266A1
LTC2107 AND LTC6409
COMBO BOARD
DATE: 07-02-2014

FIG.1
LAYER STRUCTURE

1.5 OZ.		LAYER 1- PRIMARY SIDE
4.0 MILS		FR408HR
1.0 OZ.		LAYER 2- GND
6 MILS		FR408HR
1 OZ.		LAYER 3- PWR PLANE/GND
1 OZ.		FR408HR
1 OZ.		LAYER 4- PWR PLANE/GND
6 MILS		FR408HR
1.0 OZ.		LAYER 5- GND
4.0 MILS		FR408HR
1.5 OZ.		LAYER 6- SECONDARY SIDE

THICKNESS AS REQ'D \pm 10%

SIZE	QTY	SYM	PLATED	THR/PRTL	TOL
86.61	11	+	YES	THR	\pm 3 MILS
10	806	X	YES	THR	+ 3 MILS - HOLE SIZE
118.11 x 145.67	2	□	NO	THR	\pm 6 MILS
35	26	+	YES	THR	\pm 3 MILS
67	8	X	YES	THR	\pm 3 MILS
59	2	+	YES	THR	\pm 3 MILS
16	6	+	YES	THR	+ 3/- 0 MILS
135	2	+	YES	THR	\pm 3 MILS
50	2	+	NO	THR	+ 3/- 0 MILS
12	61	+	YES	THR	+ 3 MILS - HOLE SIZE
6.2	172	◇	YES	PI-2	+ 3 MILS - HOLE SIZE
20	1	+	YES	THR	+ 3 MILS - HOLE SIZE
65	1	+	YES	THR	\pm 3 MILS
95	3	+	YES	THR	\pm 3 MILS
40	1	+	YES	THR	\pm 3 MILS
8	6	+	YES	THR	\pm 3 MILS
6	6	+	YES	THR	\pm 3 MILS

UNLESS OTHERWISE SPECIFIED		APPROVALS		 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
DIMENSIONS ARE IN INCHES		PCB DES.	M.HAWKINS		
TOLERANCES:		APP ENG.	C.MAYOTT	TITLE: FABRICATION DRAWING LTC2107 AND LTC6409 COMBO BOARD	
0.XX" = \pm 0.01"					
0.XXX" = \pm 0.005"				SIZE IC NO. LTC2107, LTC6409 N/A DEMO CIRCUIT 2266A	
INTERPRET DIM AND TOL PER ASME Y14.5M-1994				REV 1	
THIRD ANGLE PROJECTION				FILENAME: DC2266A-1.PCB	
		SCALE = NONE		SHT 1 OF 1	