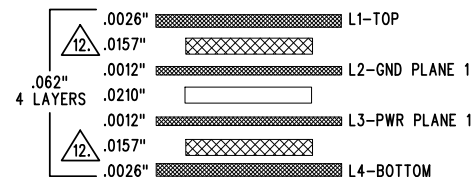


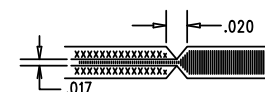
SIZE	QTY	SYM	PLATED	TOL
0.19	4	+	YES	+/-0.003"
0.01	544	X	YES	+/-0.003"
0.095	9	□	YES	+/-0.003"
0.035	39	◇	YES	+/-0.003"
0.015	13	⊗	YES	+/-0.003"
0.07	2	⊠	NO	+/-0.003"
0.055	15	⊕	YES	+/-0.003"
0.02	12	⊖	YES	+/-0.003"

LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED


- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS
AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003"
IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-FINISH SHALL BE ENIG.
(LEAD FREE SOLDER CAN BE USED FOR PROTOTYPE)
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



- CONTROLLED 50 OHM IMPEDANCE FOR LAYER 1 TRACE WIDTHS OF 20 MILS.

(10) SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC
CONSTANT DEVIATIONS. PLEASE CONSULT LTC.

REVISION HISTORY			
ECO	REV	DESCRIPTION	APPR DATE
	2	PRODUCTION FAB	ERJON Q. 10-14-11

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON ANGLE ±1° 0.XXX" = ±0.001" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994	APPROVALS		 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY
	PCB DES. NC	NAME	
THIRD ANGLE PROJECTION	APP ENG. ERJON Q.		TITLE: FABRICATION DRAWING: LTC6362 DRIVING 18-BIT SAR ADC
DO NOT SCALE DRAWING	SCALE: NONE	SIZE N/A	IC NO. LTC6362CMS8 DEMO CIRCUIT 1805A-A/H REV. 2
		FILENAME: DC1805A-2.PCB	SHT 1 of 1