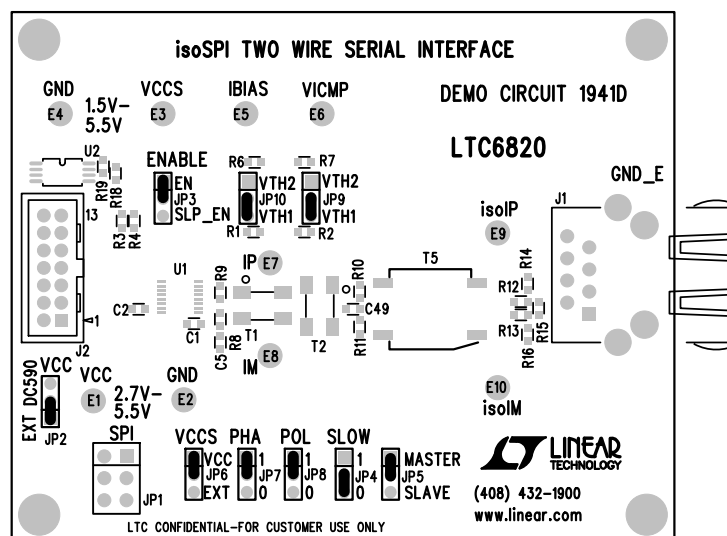
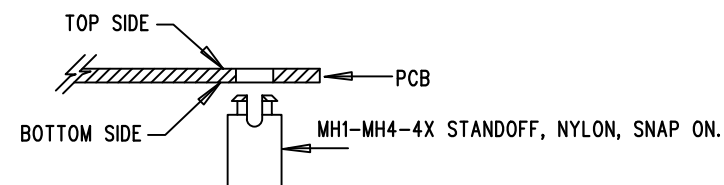


| REVISION HISTORY | | | | |
|------------------|-----|-------------|-----------|----------|
| ECO | REV | DESCRIPTION | APP. ENG. | DATE |
| - | 1 | PRODUCTION | CUYLER L. | 02-11-16 |


NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD. MAXIMUM SOLDER TEMPERATURE IS 240 DEGREES CELSIUS.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS. LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. INSTALL SHUNTS AS SHOWN ON ASSY DRAWING.
5. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
6. DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD.
7. INSTALL 4 STANDOFFS AT 4 LOCATIONS AS SHOWN BELOW:



TOP SILKSCREEN

LINEAR TECHNOLOGY
DC1941D-1- LTC6820
isoSPI TWO WIRE SERIAL INTERFACE
DATE: 02-11-1016

| APPROVALS | |  <div> 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL-FOR CUSTOMER USE ONLY </div> | | |
|--------------|-----------|--|--------------------------------------|-----------|
| PCB DES. | AK | | | |
| APP ENG. | CUYLER L. | | | |
| | | TITLE: TOP ASSEMBLY DRAWING | | |
| | | isoSPI TWO WIRE SERIAL INTERFACE | | |
| | | SIZE N/A | IC NO. LTC6820 DEMO CIRCUIT 1941D | REV. 1 |
| SCALE = NONE | | FILENAME: DC1941D-1.PCB | SHT 1 OF 1 | |