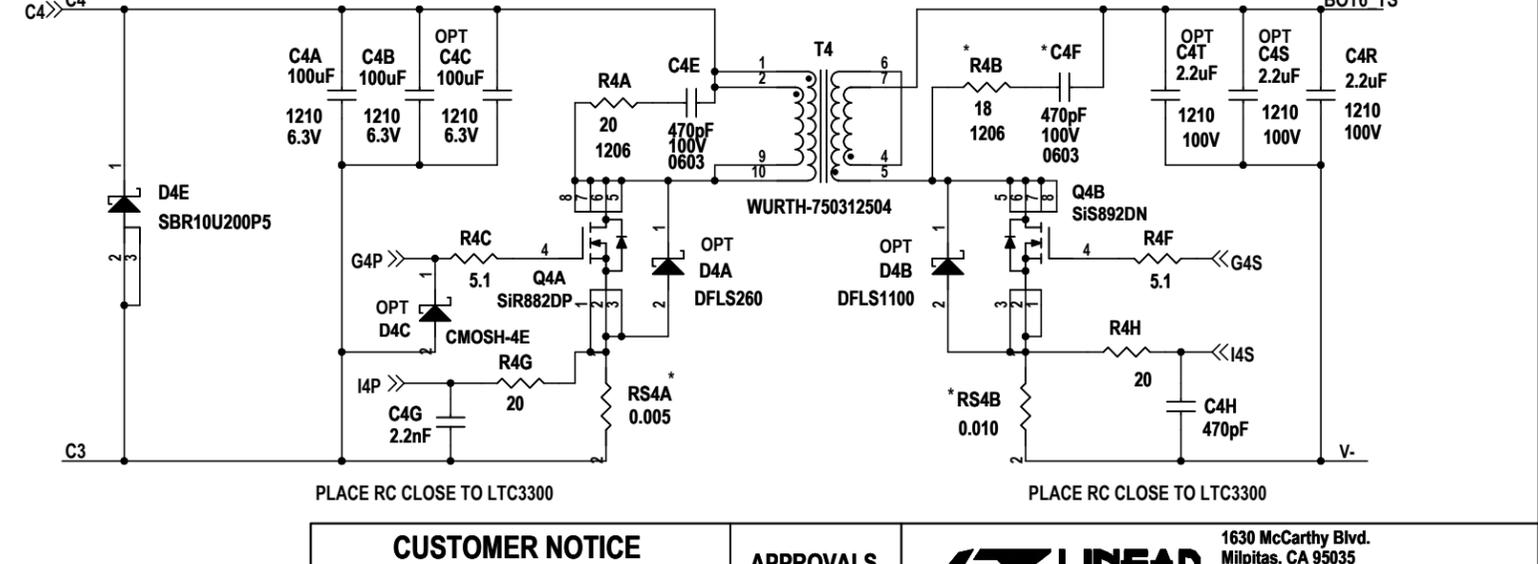
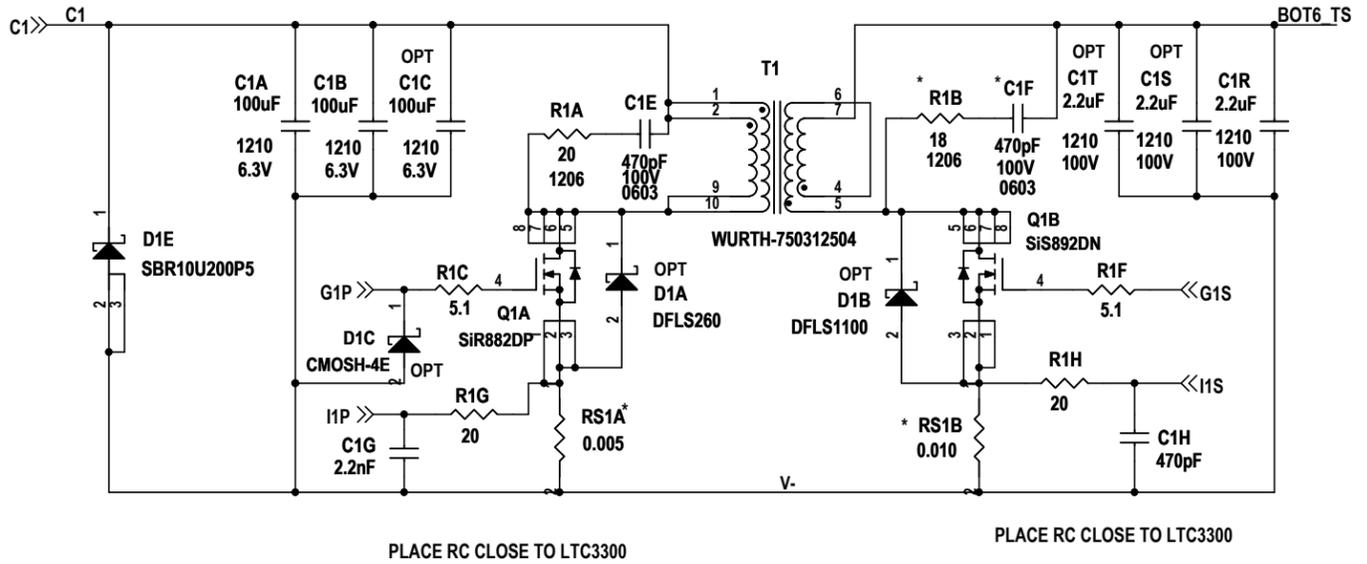
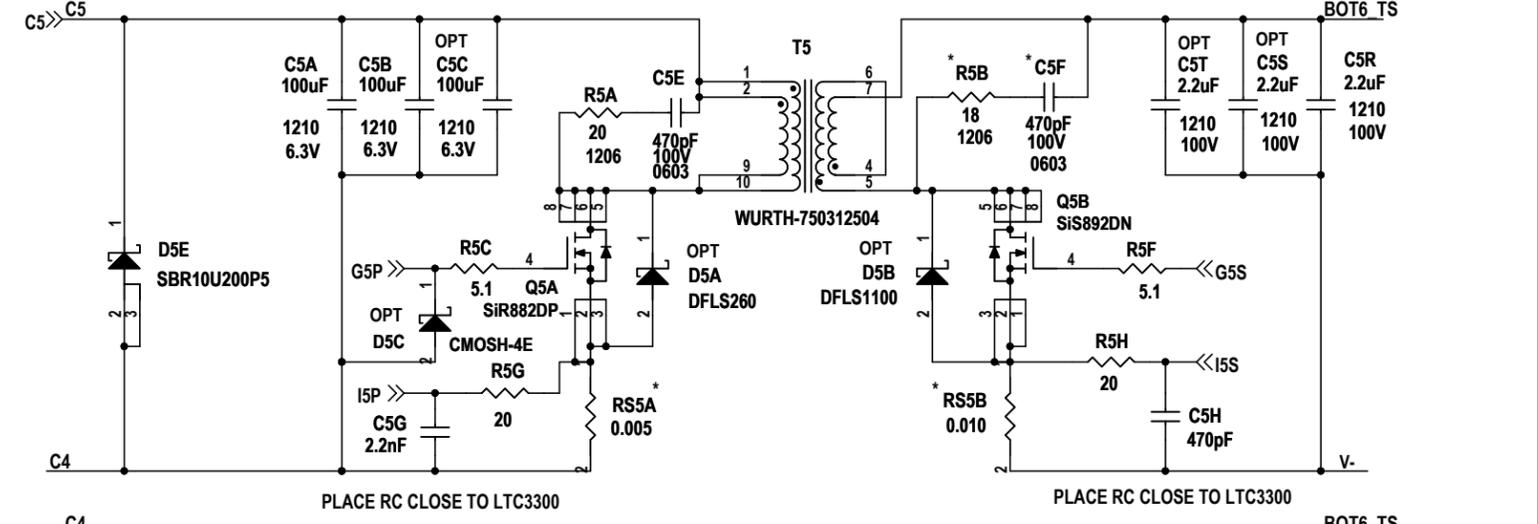
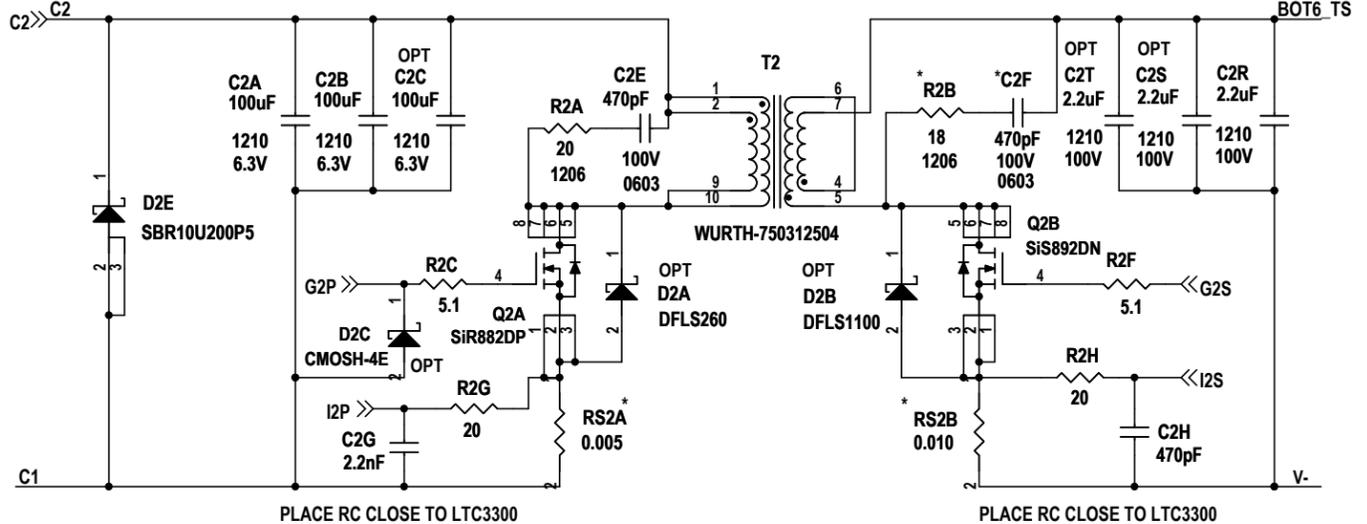
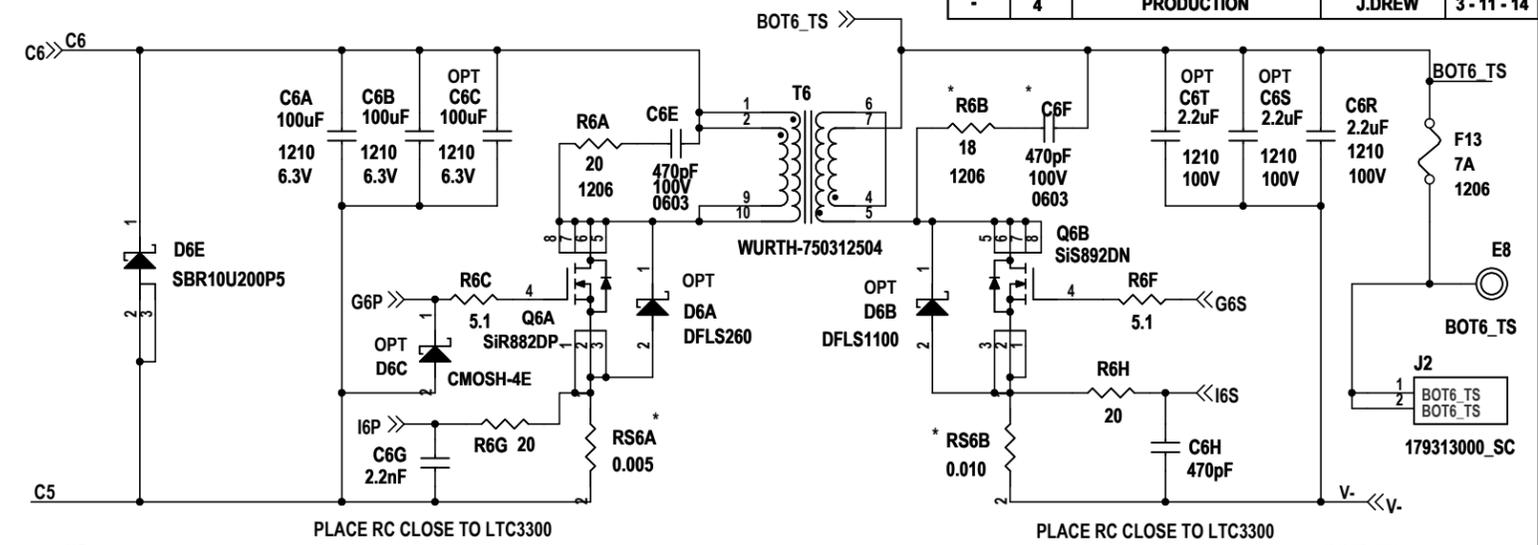
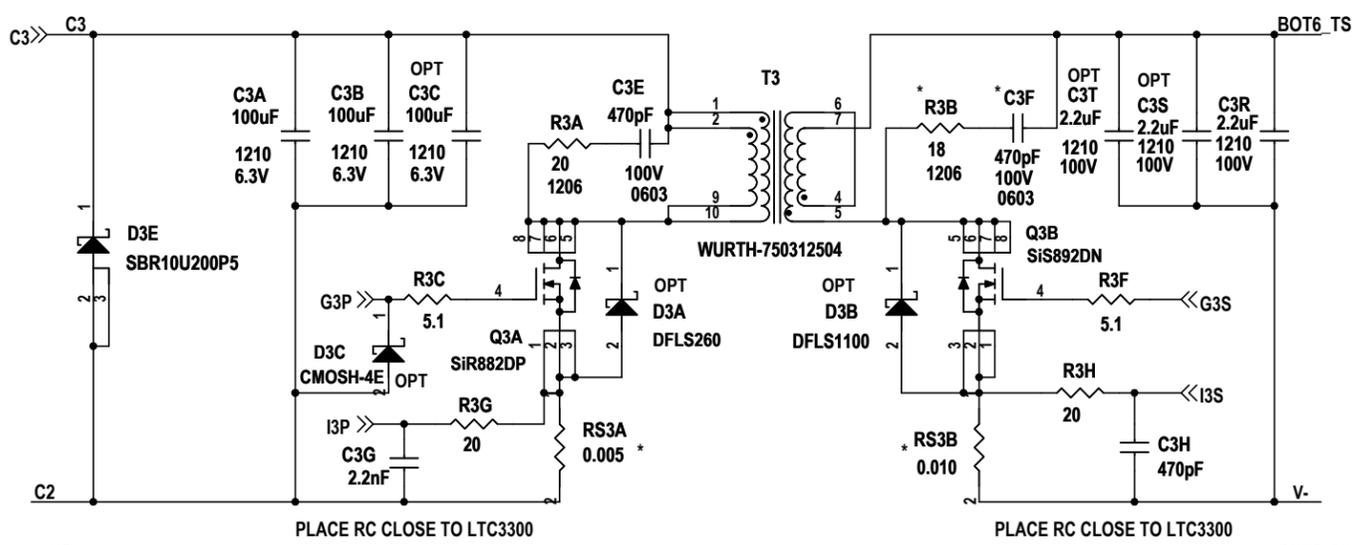


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	4	PRODUCTION	J.DREW	3-11-14



*	RS1A-RS12A	RS1B-RS12B	R1B-R12B	C1F-C12F
DC2100A - A / B	0.008	0.016	NP	NP
DC2100A - C / D	0.005	0.010	18	470pF

**CUSTOMER NOTICE**

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

**APPROVALS**

PCB DES.	NC
APP ENG.	J. DREW
SCALE	NONE

**LINEAR TECHNOLOGY**

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LTC Confidential-For Customer Use Only

TITLE: SCHEMATIC **HIGH EFFICIENCY BIDIRECTIONAL MULTICELL BATTERY BALANCER**

SIZE: N/A IC NO: **LTC3300ILXE-1 / LTC6804IG-2** REV: 4

DATE: 3-11-14 SHEET 1 OF 6