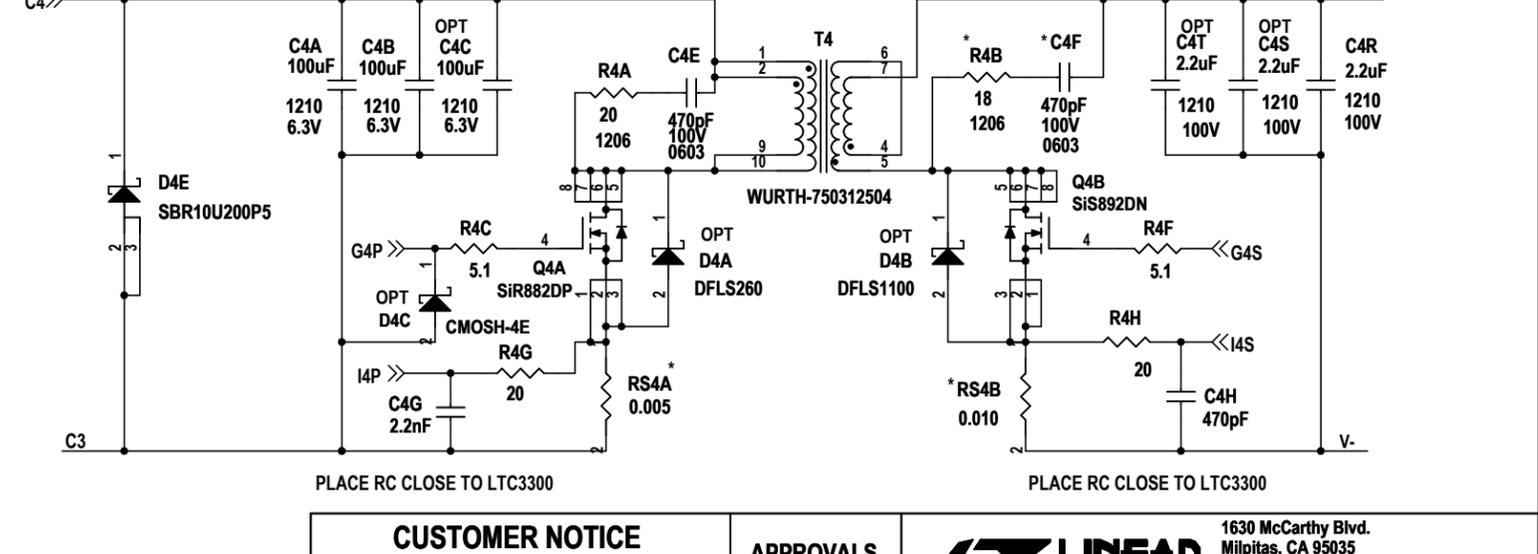
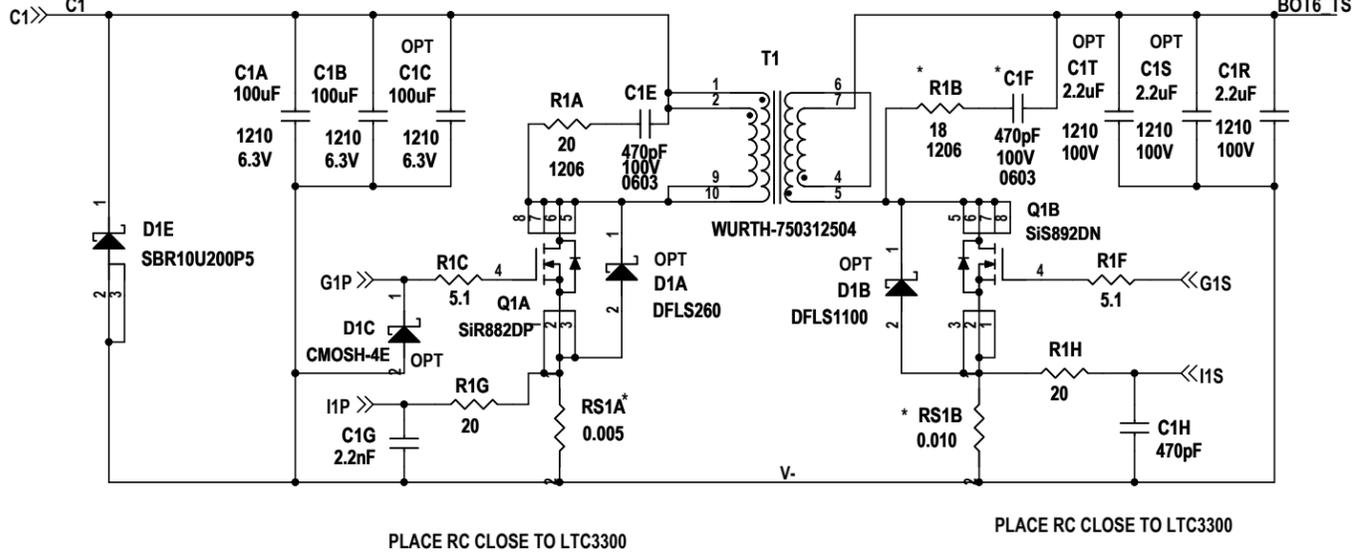
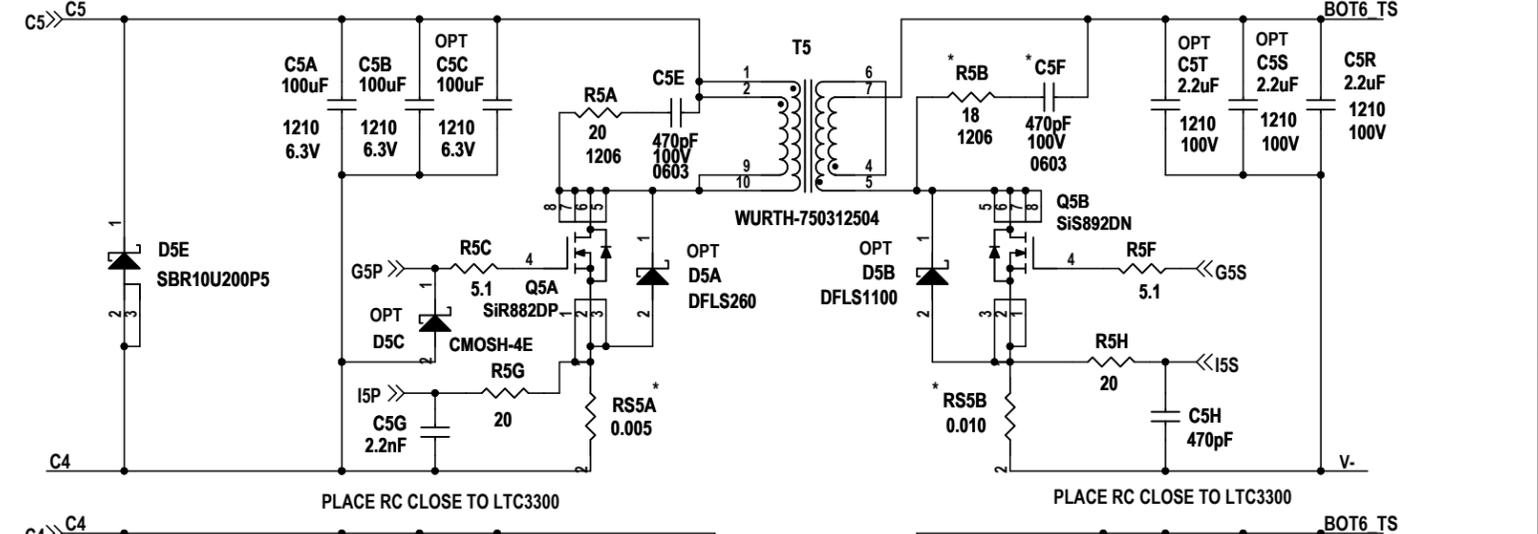
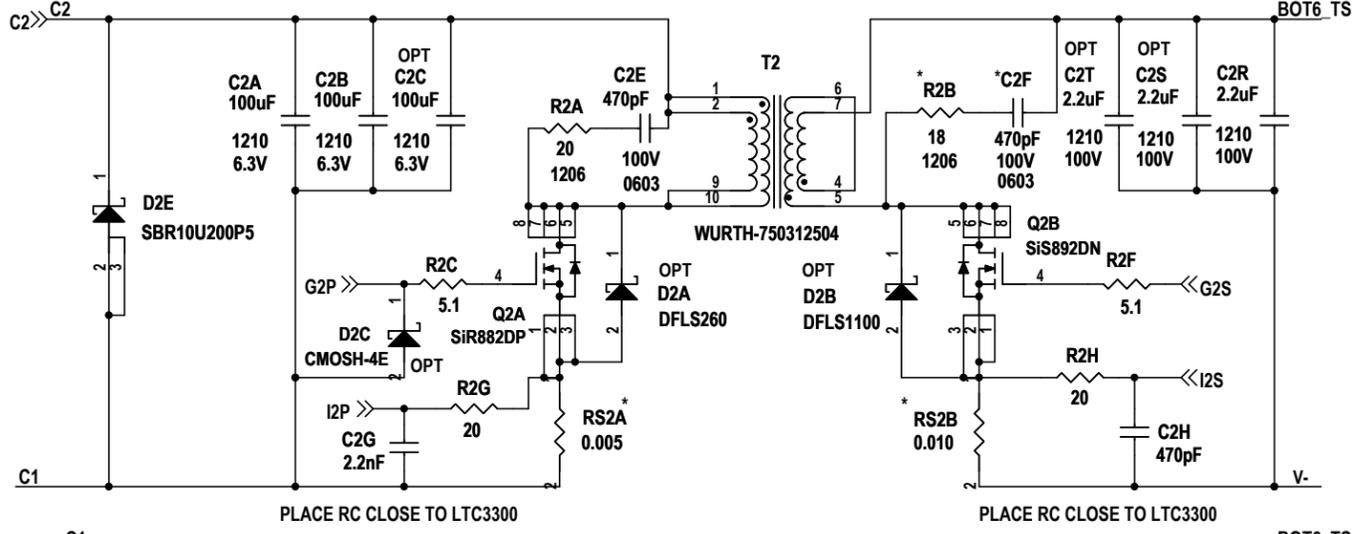
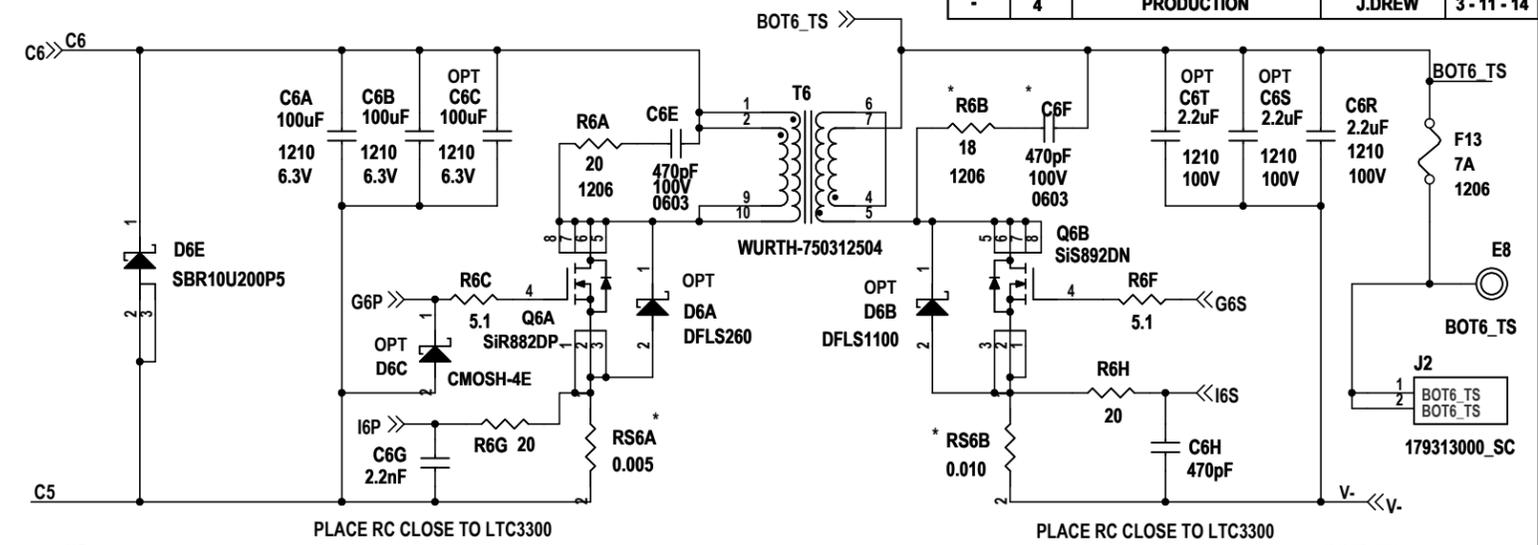
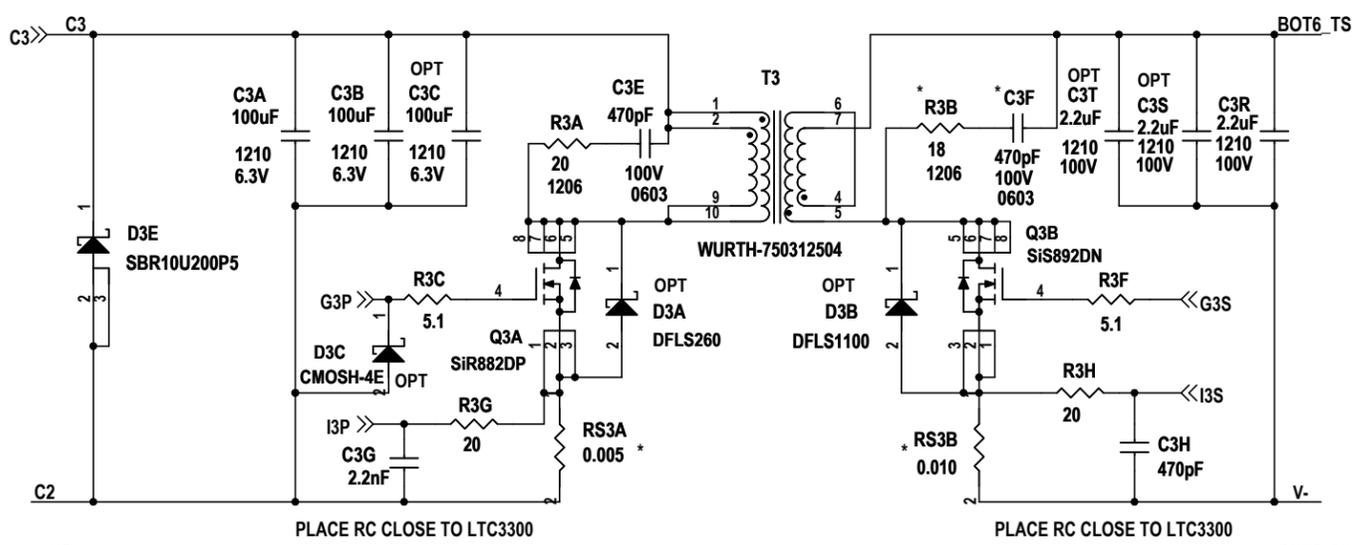


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	4	PRODUCTION	J.DREW	3-11-14



*	RS1A-RS12A	RS1B-RS12B	R1B-R12B	C1F-C12F
DC2100A - A / B	0.008	0.016	NP	NP
DC2100A - C / D	0.005	0.010	18	470pF

CUSTOMER NOTICE
 LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

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APPROVALS

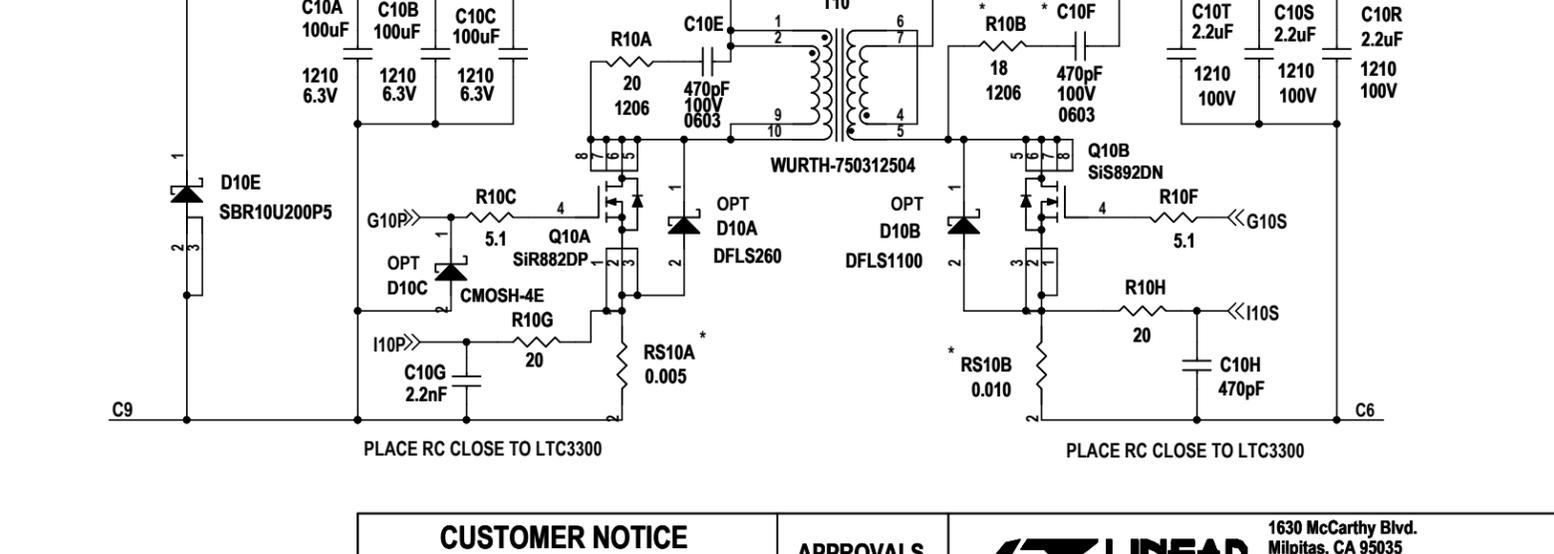
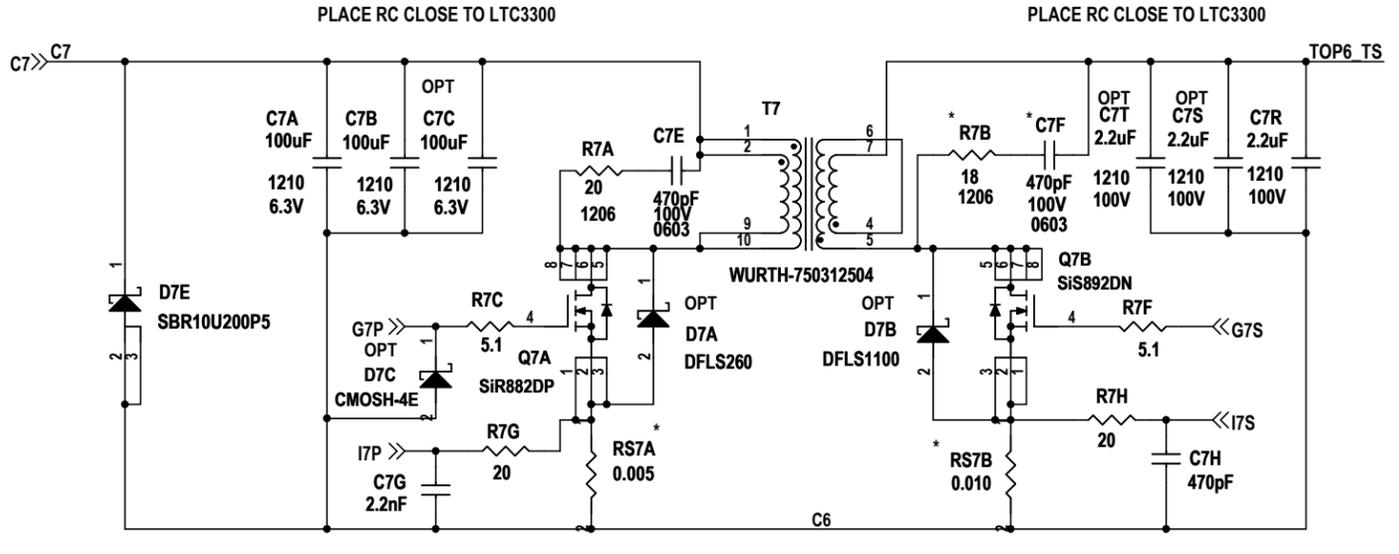
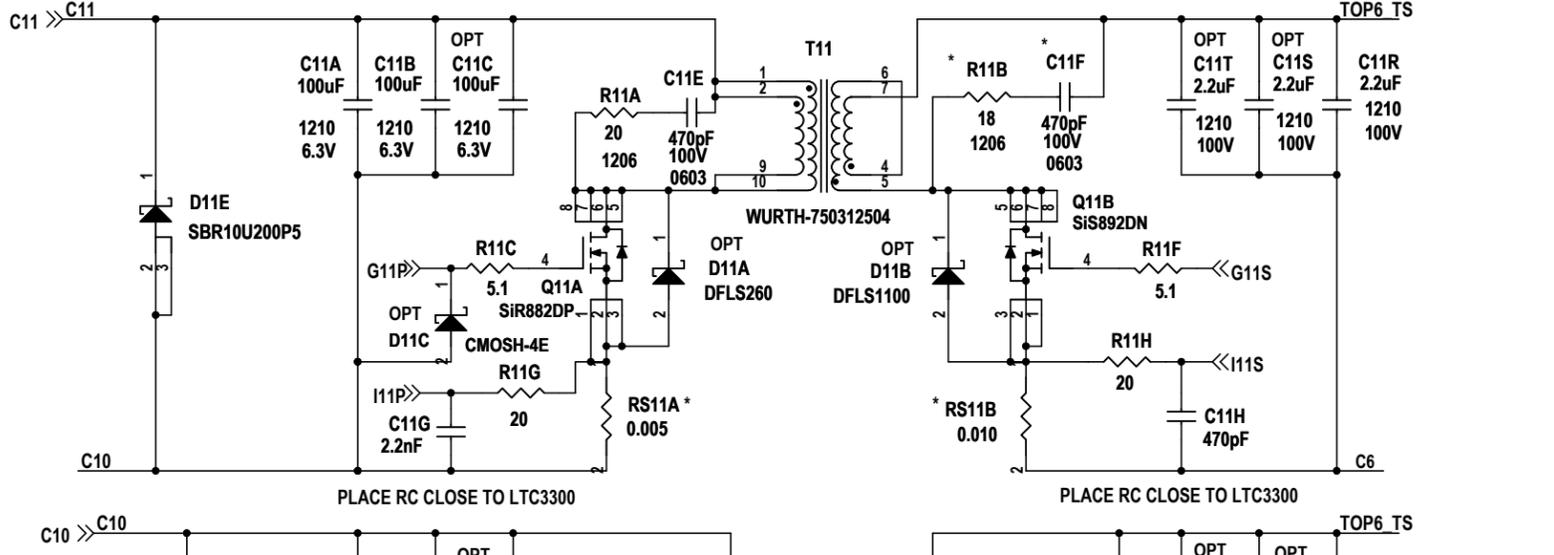
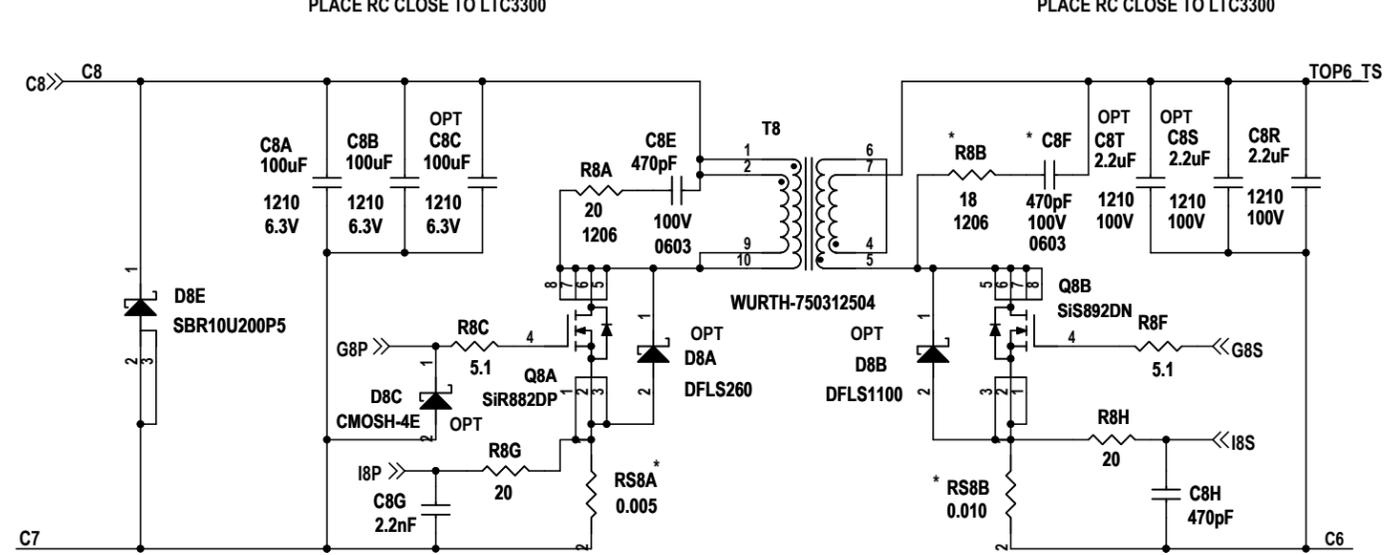
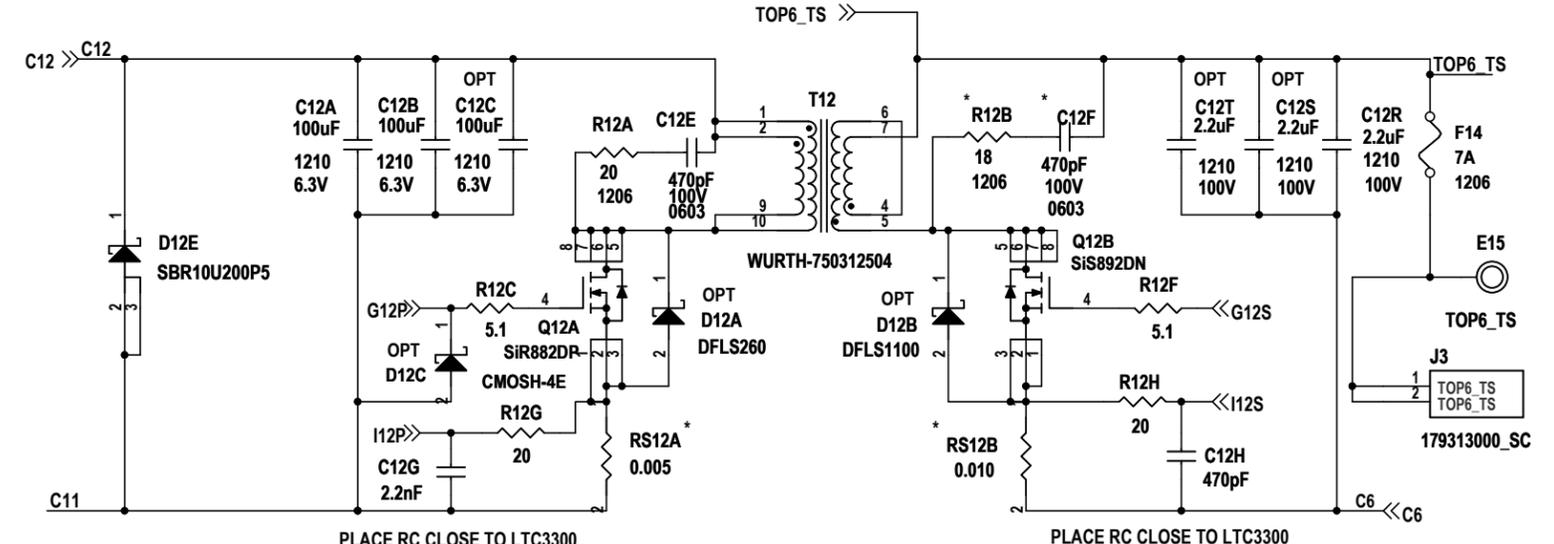
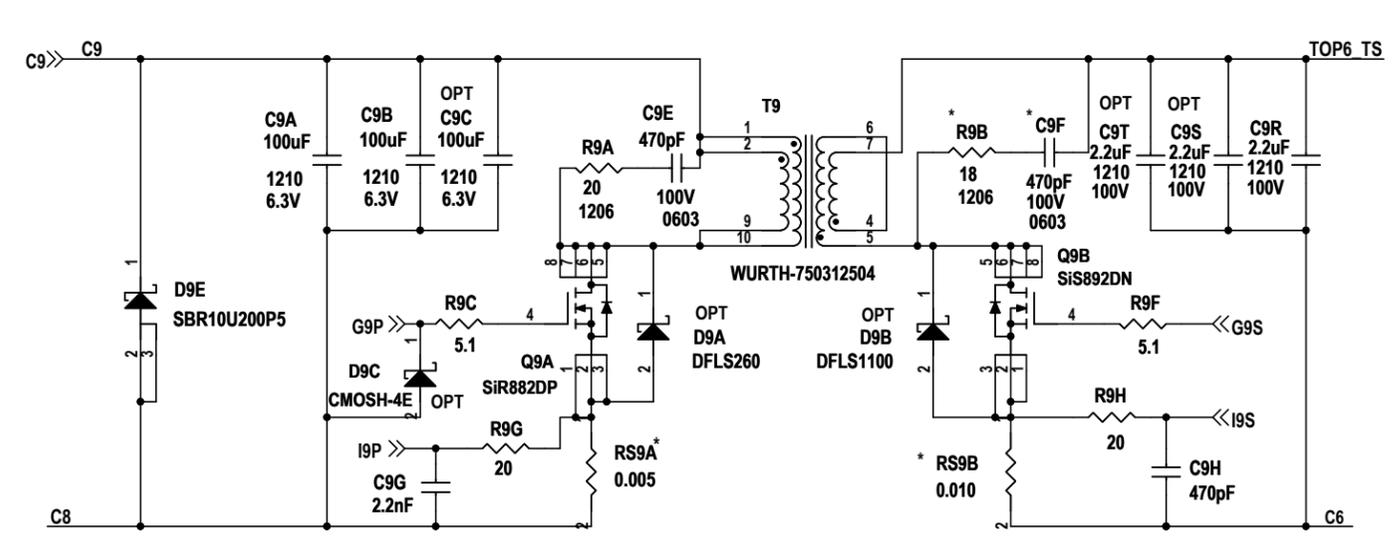
PCB DES.	NC
APP ENG.	J. DREW
SCALE	NONE

LINEAR TECHNOLOGY
 1630 McCarthy Blvd.
 Milpitas, CA 95035
 Phone: (408)432-1900 www.linear.com
 Fax: (408)434-0507
 LTC Confidential-For Customer Use Only

TITLE: SCHEMATIC **HIGH EFFICIENCY BIDIRECTIONAL MULTICELL BATTERY BALANCER**

SIZE: N/A IC NO: **LTC3300ILXE-1 / LTC6804IG-2** REV: 4
DEMO CIRCUIT 2100A

DATE: 3-11-14 SHEET 1 OF 6



*	RS1A-RS12A	RS1B-RS12B	R1B-R12B	C1F-C12F
DC2100A - A / B	0.008	0.016	NP	NP
DC2100A - C / D	0.005	0.010	18	470pF

CUSTOMER NOTICE

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APPROVALS

PCB DES.	NC
APP ENG.	J. DREW

SCALE = NONE

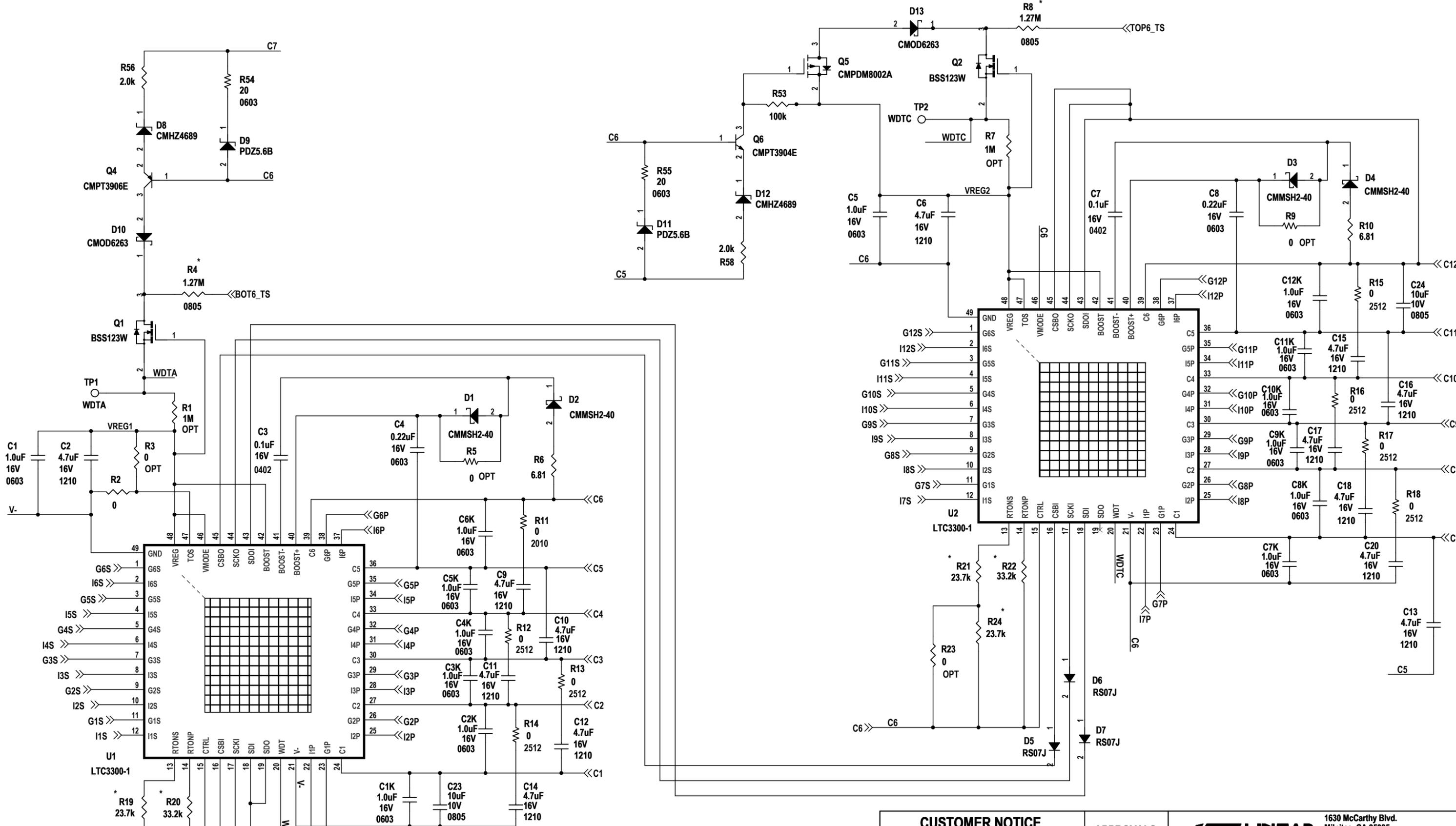
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SIZE N/A IC NO. **LTC3300ILXE-1 / LTC6804IG-2** REV. **4**

DATE: 3-11-14 SHEET 2 OF 6



*	R19	R20	R21	R22	R24	R4	R8
DC2100A - A / B	15.4k	20.5k	15.4k	20.5k	15.4k	845k	845k
DC2100A - C / D	23.7k	33.2k	23.7k	33.2k	23.7k	1.27M	1.27M

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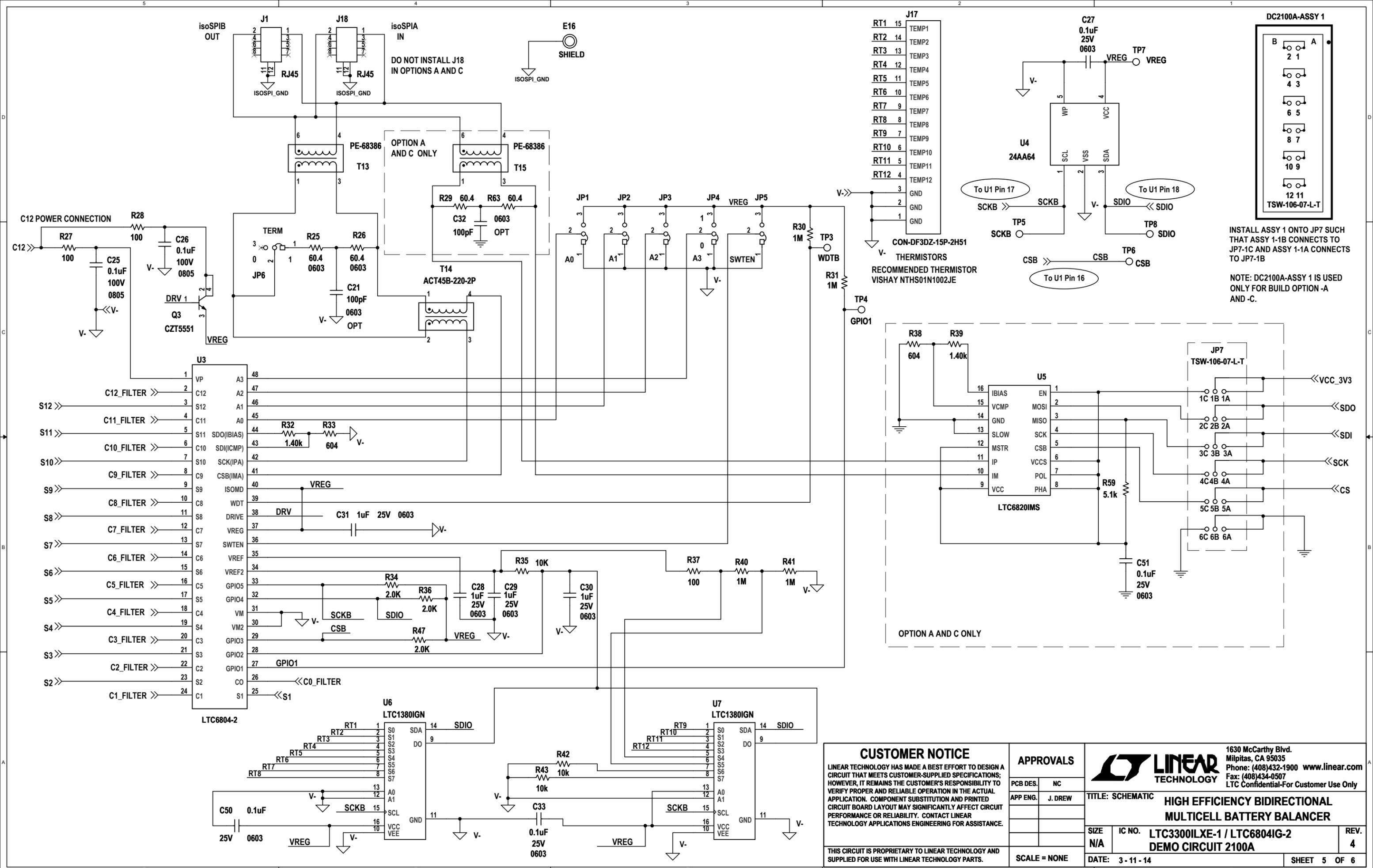
APPROVALS

PCB DES.	NC
APP ENG.	J. DREW
SCALE	NONE



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SIZE	N/A	IC NO.	LTC3300ILXE-1 / LTC6804IG-2
DATE:	3-11-14	REV.	4
SHEET 3 OF 6		REV. 4	



DO NOT INSTALL J18
IN OPTIONS A AND C

CUSTOMER NOTICE
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APPROVALS

PCB DES.	NC
APP ENG.	J. DREW

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TITLE: SCHEMATIC
HIGH EFFICIENCY BIDIRECTIONAL MULTICELL BATTERY BALANCER

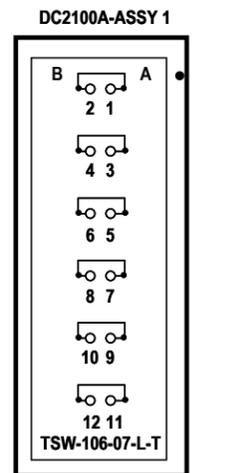
SIZE	IC NO.	REV.
N/A	LTC3300ILXE-1 / LTC6804IG-2	4

DATE: 3-11-14
SHEET 5 OF 6

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

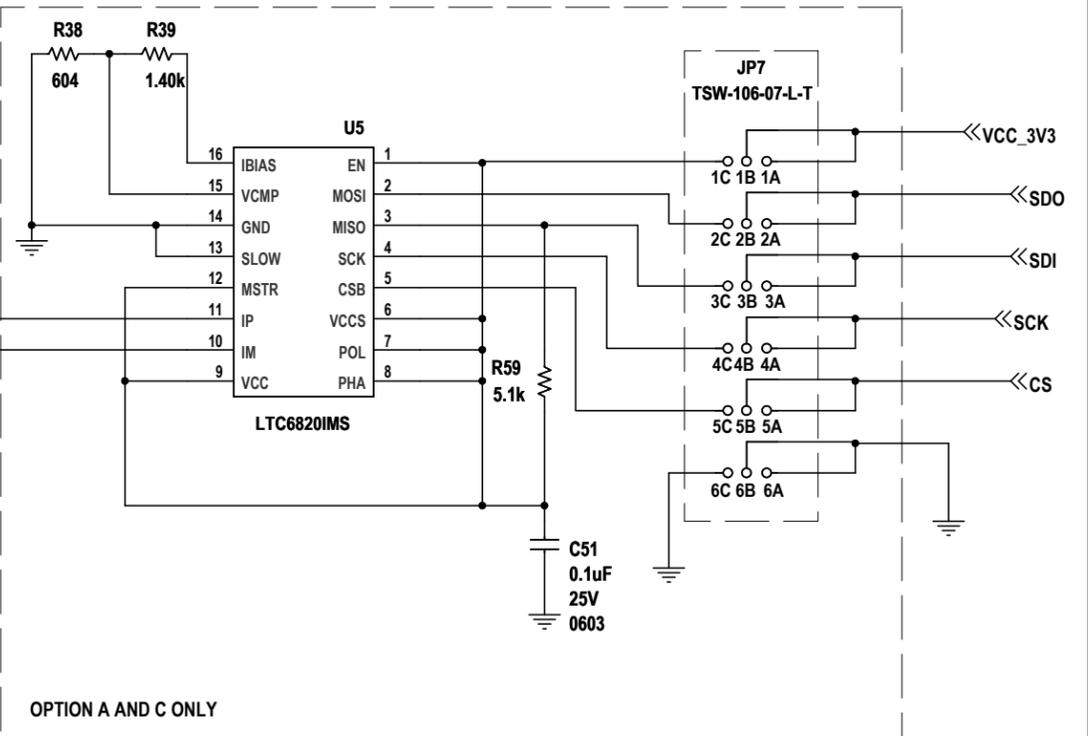
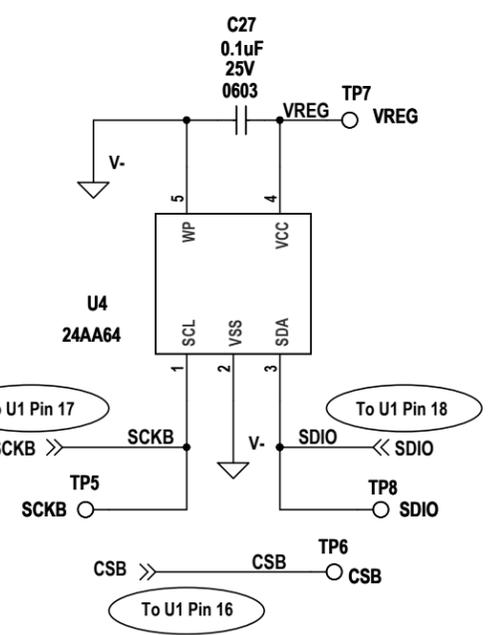
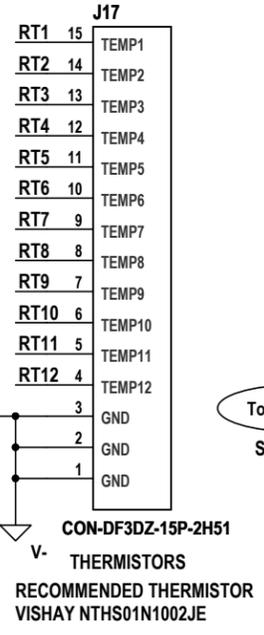
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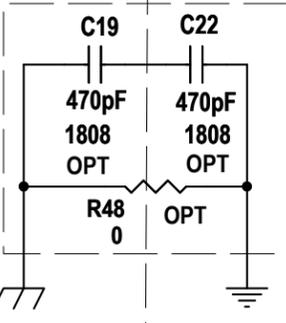
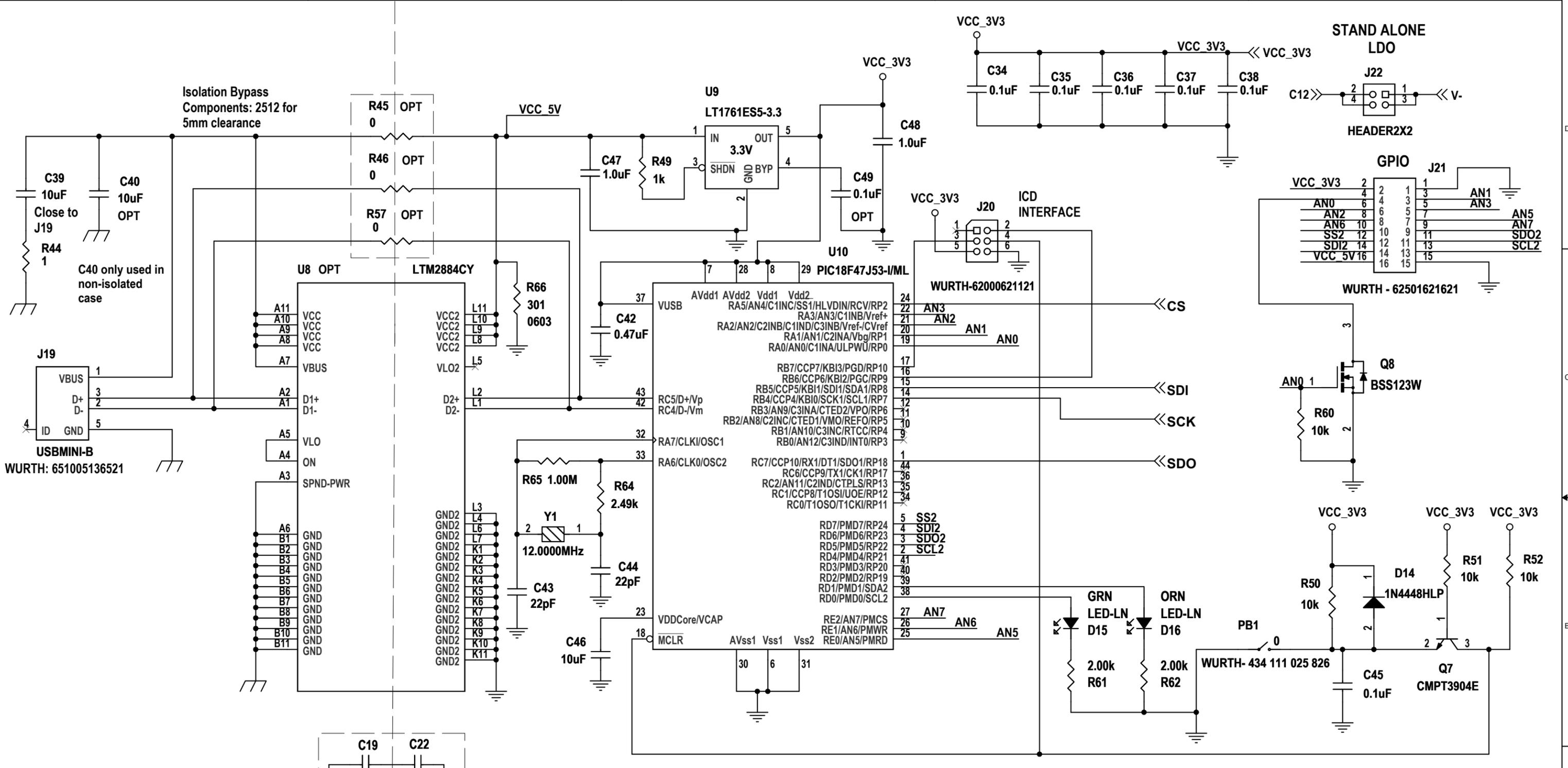
DATE: 3-11-14
SHEET 5 OF 6



INSTALL ASSY 1 ONTO JP7 SUCH THAT ASSY 1-1B CONNECTS TO JP7-1C AND ASSY 1-1A CONNECTS TO JP7-1B

NOTE: DC2100A-ASSY 1 IS USED ONLY FOR BUILD OPTION -A AND -C.





ASSEMBLY NOTE: ALL COMPONENTS ON PAGE 6 ARE FOR "BUILD OPTION A AND C ONLY".

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APPROVALS	
PCB DES.	NC
APP ENG.	J. DREW
SCALE = NONE	

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N/A	LTC3300ILXE-1 / LTC6804IG-2	4
DATE: 3-11-14		SHEET 6 OF 6

DEMO CIRCUIT 2100A