

REVISION HISTORY				
ECO	REV	DESCRIPTION	APPR	DATE
-	1	PRODUCTION	JIM D.	3-4-14

## NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.

-FINISHED THICKNESS TO BE 0.062" +/- .005"  
 -TOTAL OF 2 LAYERS WITH 1 OZ. CU ON PRIMARY AND SECONDARY  
 -FLAMMABILITY RATING: 94 V-0 MINIMUM.

3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
 0.00 ARE PRIMARY DATUMS.

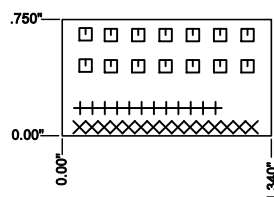
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.  
 -ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
 -HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER

5. FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
 -GOLD IMMERSION BOTH SIDES.  
 (LEAD FREE SOLDER CAN BE USED FOR PROTOTYPE)  
 -FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.

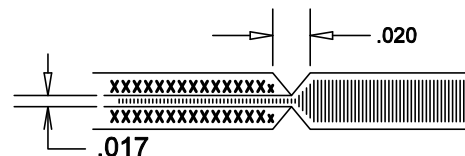
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
 PAD SIZE CAN BE MODIFIED TO MEET END FINISH.


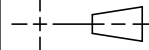
7. PCBs ARE TO BE RoHS COMPLIANT.

8. SCORING FOR PANELIZED PCB: "PRODUCTION FAB ONLY"



SIZE	QTY	SYM	PLATED	TOL
0.01	12	+	YES	+/- .003"
0.03543	15	X	YES	+/- .003"
0.063	14	□	YES	+/- .003"



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON ANGLE 21 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994	APPROVALS		 <b>LINEAR TECHNOLOGY</b> 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY		
	PCB DES.	NC			
	APP ENG.	JIM D.			
	TITLE: FABRICATION DRAWING:  DC2100A-THERMISTOR PCB-1				
THIRD ANGLE PROJECTION					
					
DO NOT SCALE DRAWING	SCALE: NONE		SIZE N/A	IC NO. LTC3300ILXE-1/LTC6804IG-2	REV. 1
			FILENAME: THERMISTOR PCB-1      SHT 1 of 1		