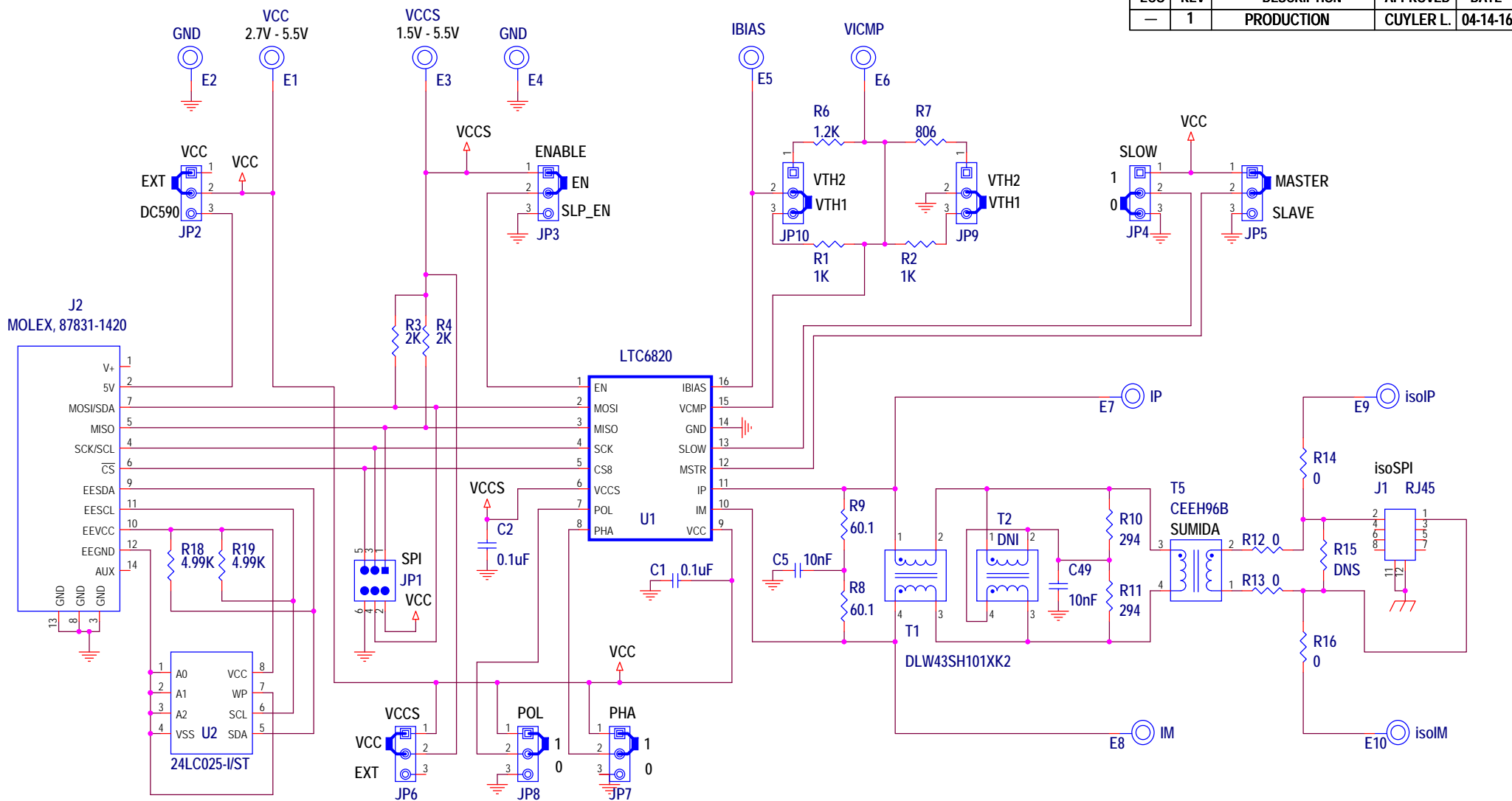


| REVISION HISTORY | | | | |
|------------------|-----|-------------|-----------|----------|
| ECO | REV | DESCRIPTION | APPROVED | DATE |
| — | 1 | PRODUCTION | CUYLER L. | 04-14-16 |



NOTES: UNLESS OTHERWISE SPECIFIED,
 1. ALL CAPACITORS AND RESISTORS ARE 0603.

| | | | | | | |
|--|--|---|--|--------------------------------|--|--------------|
| CUSTOMER NOTICE LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE. | | APPROVALS PCB DES: AK APP ENG: CUYLER L. | | | 1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 www.linear.com Fax: (408)434-0507 LTC Confidential-For Customer Use Only | |
| | | TITLE: SCHEMATIC isoSPI TWO WIRE SERIAL INTERFACE | | | SIZE: N/A IC NO.: LTC6820 DEMO CIRCUIT 1941D | REV. 1 |
| THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS. | | SCALE = NONE | | DATE: Thursday, April 14, 2016 | | SHEET 1 OF 1 |