

DRILL DRAWING LAYER

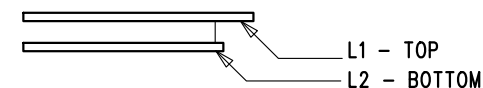
LINEAR TECHNOLOGY DATE: 06-15-15  
DC2260A-2- LTC6811IG-2  
isoSPI 12-CELL BATTERY-STACK MONITOR

SIZE	QTY	SYM	PLATED	TOL
0.056	16	$\oplus^D$	YES	+/-0.003
0.06496	4	$\otimes$	YES	+/-0.003
0.12795	4	$\square$	NO	+/-0.003
0.03819	16	$\diamond$	YES	+/-0.003
0.187	4	$\otimes$	NO	+/-0.003
0.035	61	$\otimes$	YES	+/-0.003
0.01	56	$\oplus^A$	YES	+/-0.003
0.063	8	$\oplus^B$	YES	+/-0.003
0.07	2	$\oplus^C$	NO	+/-0.003
0.02	24	$\oplus$	YES	+/-0.003

## REVISION HISTORY

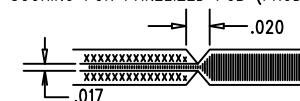
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	1	2ND PROTOTYPE	JON M.	06-15-15

## LAYER STRUCTURE



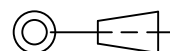
## NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.  
-FINISHED THICKNESS TO BE 0.062" +/- .005"  
-TOTAL OF 2 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN. 0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



## UNLESS OTHERWISE SPECIFIED

DIMENSIONS ARE IN INCHES  
TOLERANCES:  
0.XX" =  $\pm 0.01$ "  
0.XXX" =  $\pm 0.005$ "  
INTERPRET DIM AND TOL  
PER ASME Y14.5M-1994  
THIRD ANGLE PROJECTION



## APPROVALS

PCB DES.	AK
APP ENG.	JON M.

SCALE = NONE



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FOR CUSTOMER USE ONLY

TITLE: FABRICATION DRAWING

isoSPI 12-CELL BATTERY-STACK MONITOR

SIZE	IC NO.	REV
N/A	LTC6811IG-2 DEMO CIRCUIT 2260A	2

FILENAME: DC2260A-2.PCB

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