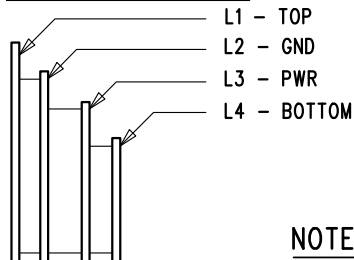
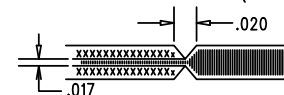


LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

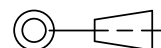
- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN. 0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



SIZE	QTY	SYM	PLATED	TOL
0.187	4	+	NO	+/-0.003
0.063	12	X	YES	+/-0.003
0.06496	4	□	YES	+/-0.003
0.12795	4	◇	NO	+/-0.003
0.03819	16	⊗	YES	+/-0.003
0.07	2	⊗	NO	+/-0.003
0.01	235	⊕ ^A	YES	+/-0.003
0.035	26	⊕ ^B	YES	+/-0.003
0.02	134	⊕ ^C	YES	+/-0.003
0.056	19	⊕ ^D	YES	+/-0.003

UNLESS OTHERWISE SPECIFIED

DIMENSIONS ARE IN INCHES
TOLERANCES:
0.XX" = ±0.01"
0.XXX" = ±0.005"
INTERPRET DIM AND TOL
PER ASME Y14.5M-1994
THIRD ANGLE PROJECTION



APPROVALS

PCB DES.	AK
APP ENG.	CUYLER L.
SCALE = NONE	



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TITLE: FABRICATION DRAWING

isoSPI MULTI CELL BATTERY-STACK MONITOR

SIZE	IC NO.	LTC6813/12	REV
N/A		DEMO CIRCUIT 2350A	3

FILENAME: DC2350A-3.PCB SHT 1 OF 1