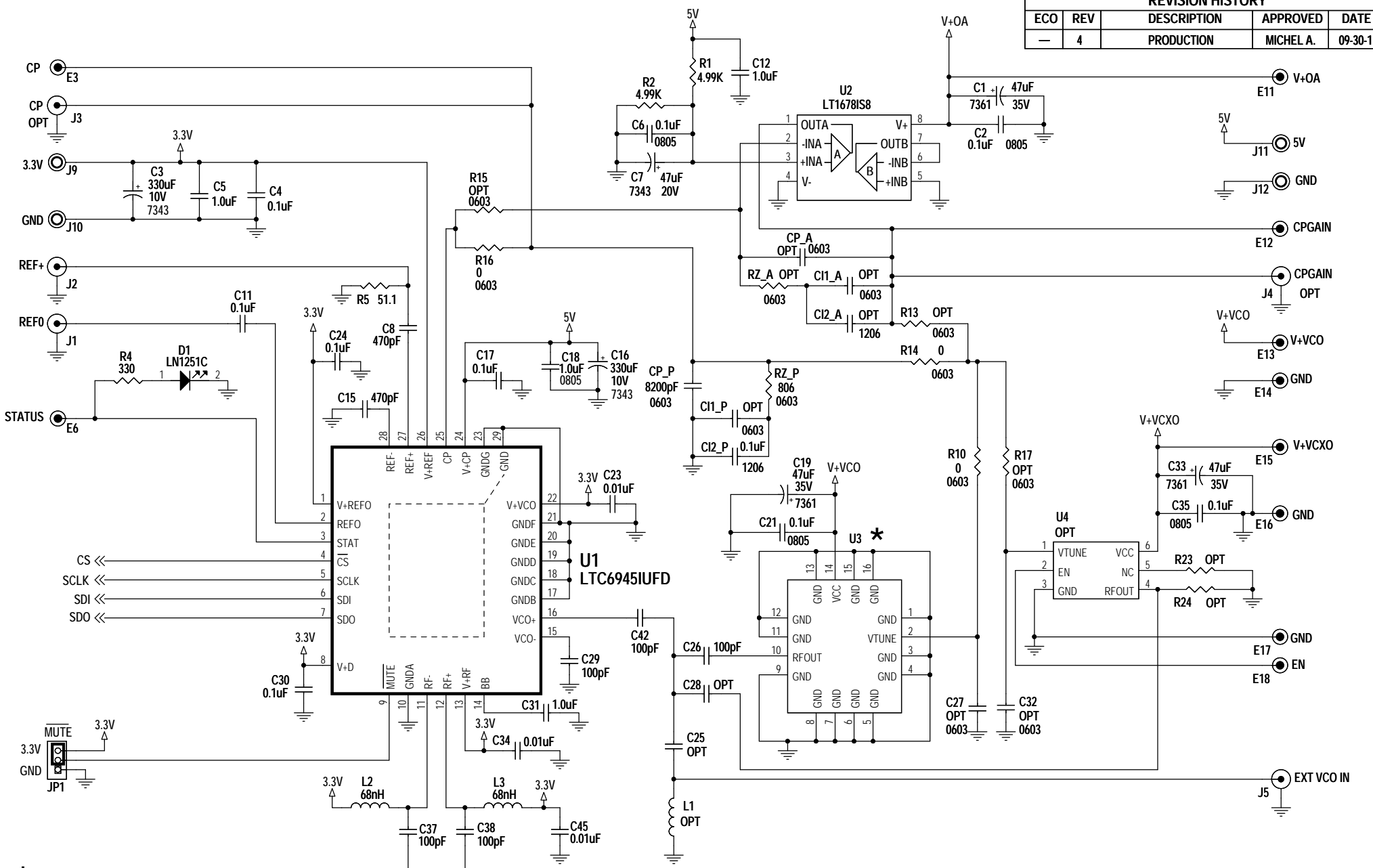


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	4	PRODUCTION	MICHEL A.	09-30-11



ASSY	U3	VCO
-A	CVC055CL-0902-0928	902 - 928 MHz

NOTE: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE IN OHMS, 0402
 ALL CAPACITORS ARE IN MICROFARADS, 0402.

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

APPROVALS	
PCB DES.	KIM T.
APP ENG.	MICHEL A.
SCALE = NONE	

LINEAR TECHNOLOGY

1630 McCarthy Blvd.
 Milpitas, CA 95035
 Phone: (408)432-1900 www.linear.com
 Fax: (408)434-0507
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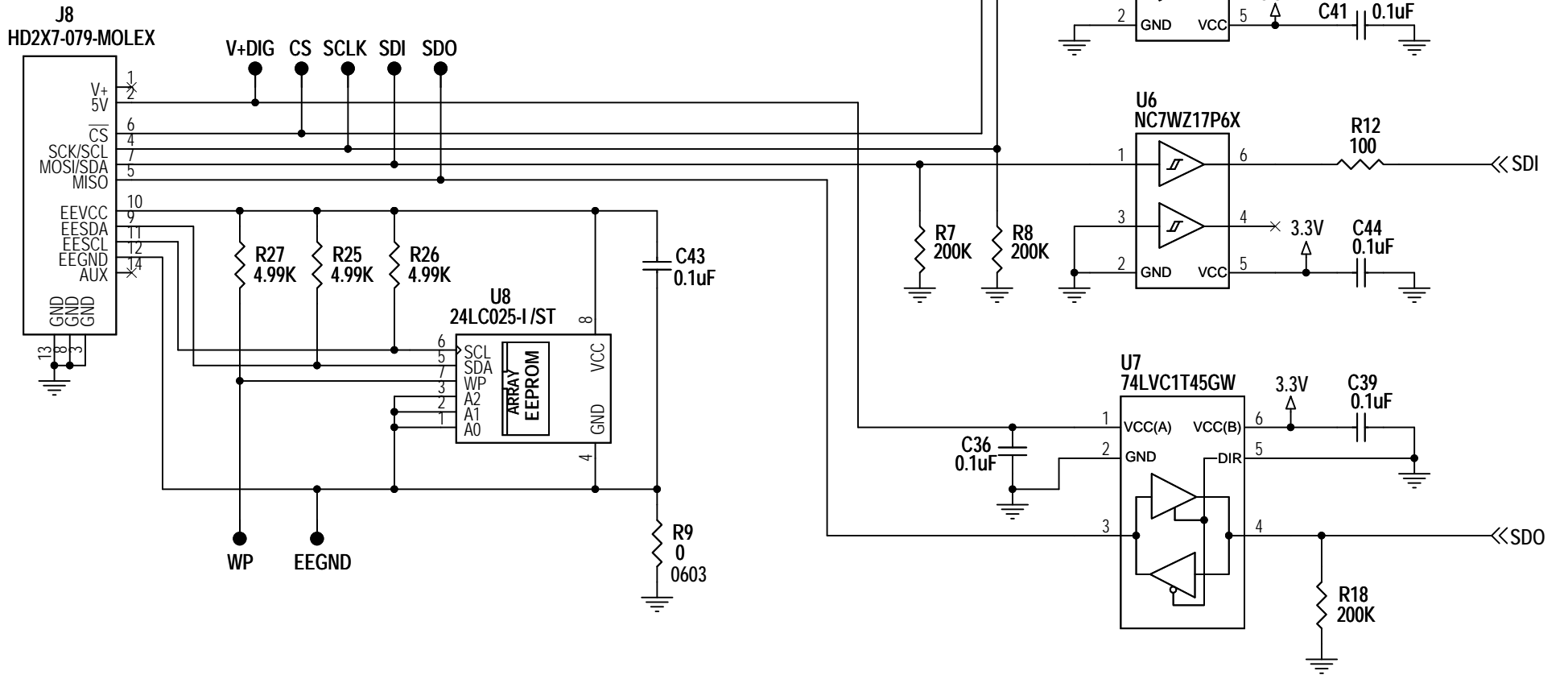
TITLE: SCHEMATIC

ULTRALOW NOISE AND SPURIOUS INTEGER-N SYNTHESIZER

SIZE	IC NO.	LTC6945IUFD	REV. 4
N/A			
DATE: 09/30/2011, 11:05 AM		SHEET 1 OF 2	

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

DC590B SPI INTERFACE



CUSTOMER NOTICE

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THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS

PCB DES.	KIM T.
APP ENG.	MICHEL A.

SCALE = NONE



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TITLE: SCHEMATIC

ULTRALOW NOISE AND SPURIOUS INTEGER-N SYNTHESIZER

SIZE	IC NO.	LTC6945IUFD	REV.
N/A		DEMO CIRCUIT 1649A	4

DATE: 09/30/2011, 11:05 AM

SHEET 2 OF 2