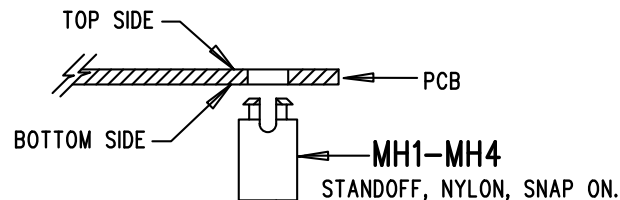
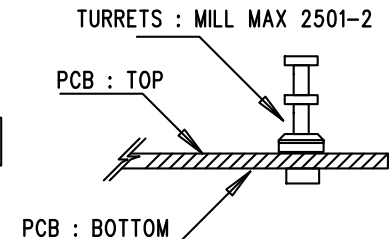
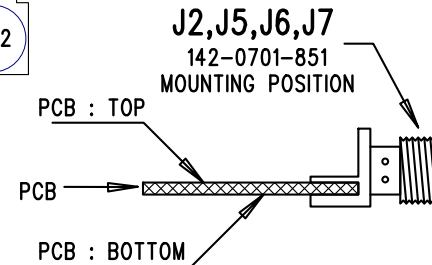


REVISION HISTORY			
ECO	REV	DESCRIPTION	APP. ENG. DATE
-	2	PRODUCTION	MICHEL A. 09-19-12

NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD. MAXIMUM SOLDER TEMPERATURE IS 240 DEGREES CELSIUS.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS. LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. INSTALL SHUNTS AS SHOWN ON ASSY DRAWING.
5. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
6. DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD.
7. FOR D1, INSTALL AS SHOWN BELOW:



8. MARK EACH ASSEMBLY TYPE AND VCO RANGE WITH BLACK PERMANENT MARKER AS SHOWN IN TABLE BELOW:

ASSY	VCO
-A	2328-2536 MHz

APPROVALS

PCB DES.	KIM T.
APP ENG.	MICHEL A.



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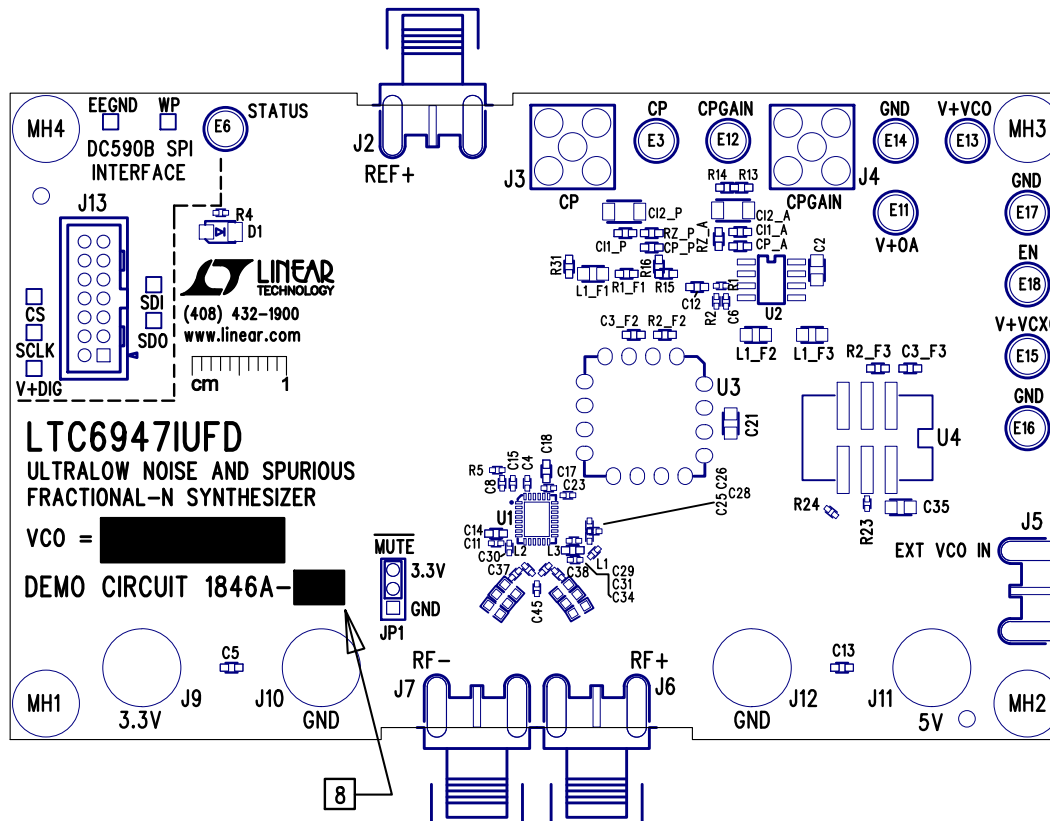
TITLE: TOP ASSEMBLY DRAWING
ULTRALOW NOISE AND SPURIOUS
FRACTIONAL-N SYNTHESIZER

SIZE	IC NO.	REV.
N/A	LTC6947IUFD DEMO CIRCUIT 1846A	2

SCALE = NONE

FILENAME: DC1846A-2.PCB

SHT 1 OF 2



LTC6947IUFD
ULTRALOW NOISE AND SPURIOUS
FRACTIONAL-N SYNTHESIZER

VCO = XXXXXXXXXX
DEMO CIRCUIT 1846A-XXXXXX