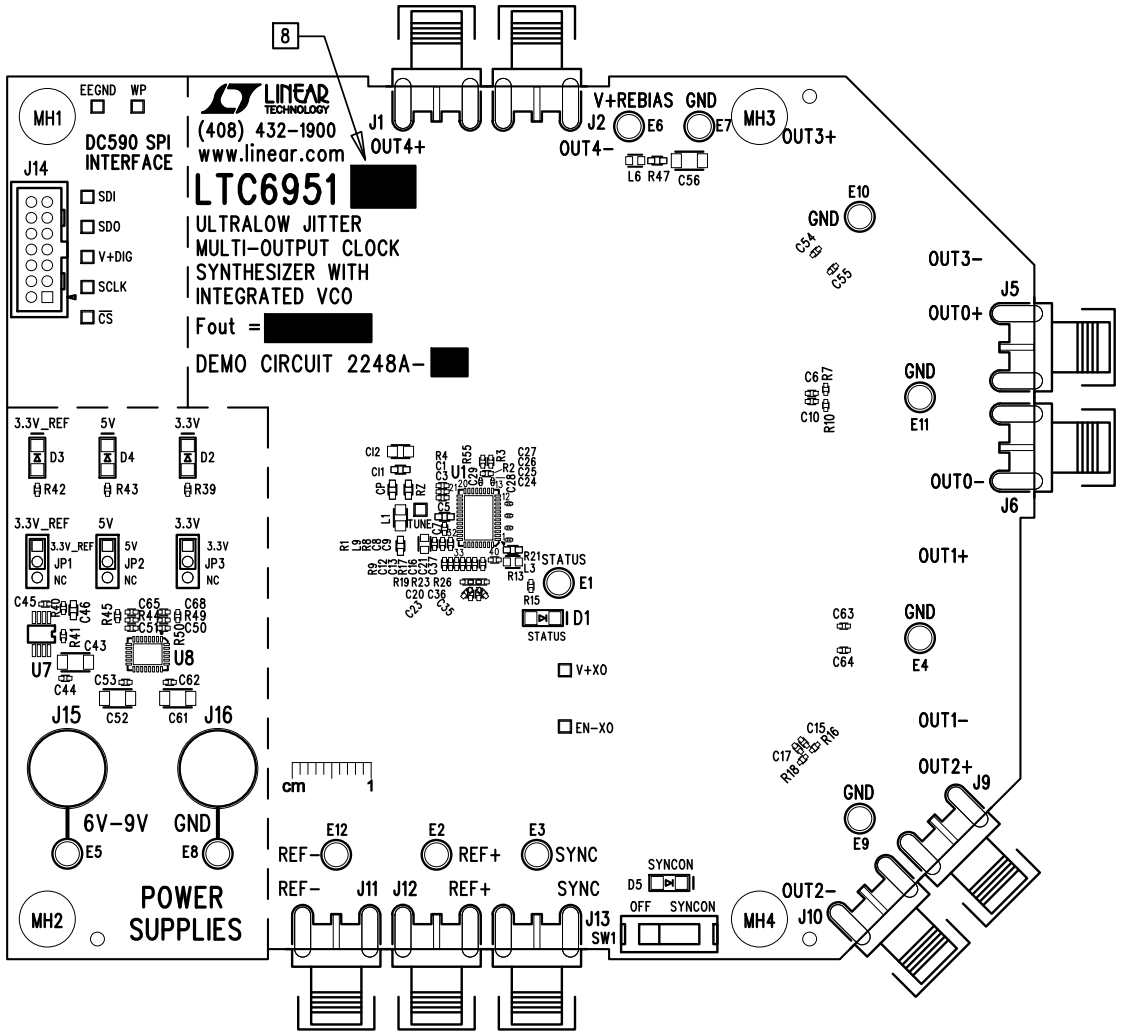
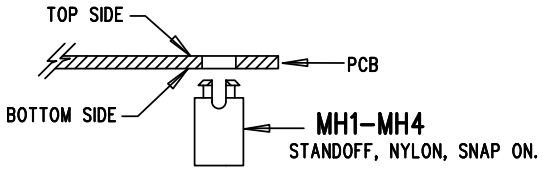
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
–	2	PRODUCTION	CHRIS P.	06-09-15



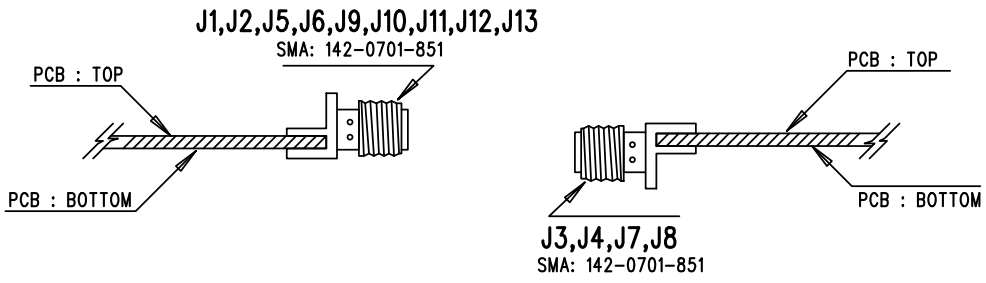
NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD. MAXIMUM SOLDER TEMPERATURE IS 240 DEGREES CELSIUS.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS. LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. INSTALL SHUNTS AS SHOWN.
5. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
6. DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD.
7. INSTALL 4 STANDOFFS AT 4 LOCATIONS AS SHOWN BELOW:



8. MARK EACH SUFFIX, Fout, AND ASSEMBLY TYPE IN THE WHITE BLOCK AREAS WITH BLACK PERMANENT MARKER AS SHOWN IN TABLE BELOW:

ASSY	U1	SUFFIX	Fout =
A	LTC6951		2.5GHz
B	LTC6951-1	-1	2.7GHz



APPROVALS	
PCB DES.	KIM T.
APP ENG.	CHRIS P.
SCALE = NONE	

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TITLE: TOP ASSEMBLY DRAWING
ULTRALOW JITTER MULTI-OUTPUT CLOCK SYNTHESIZER WITH INTEGRATED VCO

SIZE	IC NO.	REV.
N/A	LTC6951IUHF FAMILY DEMO CIRCUIT 2248A	2

FILENAME: DC2248A-2.PCB | SHT 1 OF 2