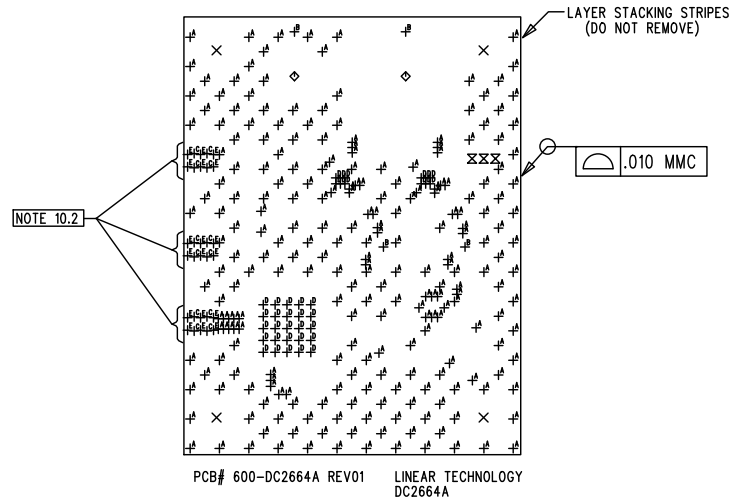


NOTES: UNLESS OTHERWISE SPECIFIED

| REVISION HISTORY | | | | |
|------------------|-----|---------------|------|--------|
| ECO | REV | DESCRIPTION | ENG. | DATE |
| - | 02 | ADDED NOTE 11 | M.H. | 6-1-17 |

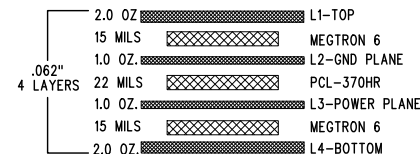



DRILL CHART

| SIZE | QTY | SYM | PLATED | TOL |
|------|-----|----------------|--------|------------|
| 188 | 4 | ✕ | NO | +/- 3 MILS |
| 207 | 2 | ◇ | YES | +/- 3 MILS |
| 35 | 3 | ⊗ | YES | +/- 3 MILS |
| 12 | 243 | ⊕ ^A | YES | +/- 3 MILS |
| 65 | 4 | ⊕ ^B | YES | +/- 3 MILS |
| 45 | 12 | ⊕ ^C | YES | +/- 3 MILS |
| 10 | 37 | ⊕ ^D | YES | +/- 3 MILS |
| 16 | 18 | ⊕ ^E | YES | +/- 3 MILS |

- FAB PER IPC-A-600.
- MATERIAL/STACKUP DETAILS
 - HYBRID CONSTRUCTION USING MEGTRON 6 AND PCL-370 HR MATERIALS.
 - TOTAL OF 4 LAYERS. FINISHED BOARD THICKNESS SHALL BE 62 MILS +/- 10%.
 - COPPER PLATING AND DIELECTRIC THICKNESSES AS PER FIG.1, "STACKUP DETAILS". STACKUP SHALL BE IN ACCORDANCE WITH GORILLA CIRCUITS DOCUMENT #JC7314811a.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
- DRILLING DETAILS
 - DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
 - ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
- FINISH PLATING AND SILKSCREEN
 - PLATING SHALL BE SMOBC, ENIG FINISH WITH GREEN SOLDERMASK.
 - SOLDERMASK IS SUPPLIED TO FABRICATOR AS ZERO-CLEARANCE DATA.
 - SILKSCREEN LEGENDS SHALL BE WHITE NON-CONDUCTIVE EPOXY INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- BOARDS SHALL BE RoHS COMPLIANT.
- BOARDS SHALL BE PANELIZED USING ROUTE AND RETAIN METHOD. THERE SHALL BE TWO RETAINING TABS ON EACH BOARD EDGE.
- ANY ELECTRICAL TEST/QA STAMPS SHALL BE ON BOTTOM SIDE.
- FIXED IMPEDANCE REQUIREMENTS
 - 20 MIL WIDE TRACES ON LAYER 1 ASSOCIATED WITH J3 AND C5 SHALL HAVE AND IMPEDANCE OF 50 OHMS +/-10% AT A 7GHz TEST FREQUENCY.
- NOTE GROUND PLANE EXTENDS TO BOARD EDGE ON TOP AND BOTTOM LAYERS FOR J1,J2 AND J3 SMA CONNECTORS.
- ALL 10 MIL DIAMETER VIAS, QTY 37, SHALL BE PLUGGED ON BOTTOM SIDE.

FIG. 1 STACKUP DETAILS



| | | | | |
|--|--------------|------------|---|-----------------|
| UNLESS OTHERWISE SPECIFIED | APPROVALS | |  LINEAR TECHNOLOGY 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY | |
| | ENGR. | M. HAWKINS | | |
| | APP ENGR. | C. PEARSON | TITLE: FABRICATION DRAWING VCO RIDER BOARD LOOP FILTER, 0.5" X 0.5" | |
| | | | | |
| | | | | |
| DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION | SCALE = NONE | | SIZE IC NO. LTC6952 N/A DEMO CIRCUIT 2664A | DWG. REV. 02 |
| | SHT 1 OF 1 | | | |