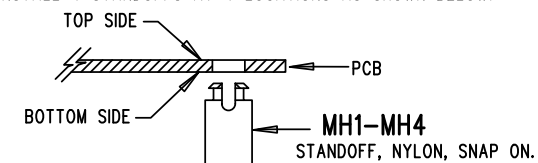


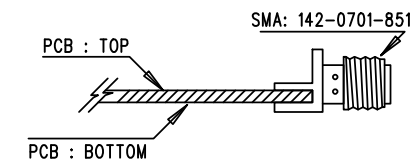
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	3	PRODUCTION	CHRIS P.	07-06-17

NOTES: UNLESS OTHERWISE SPECIFIED

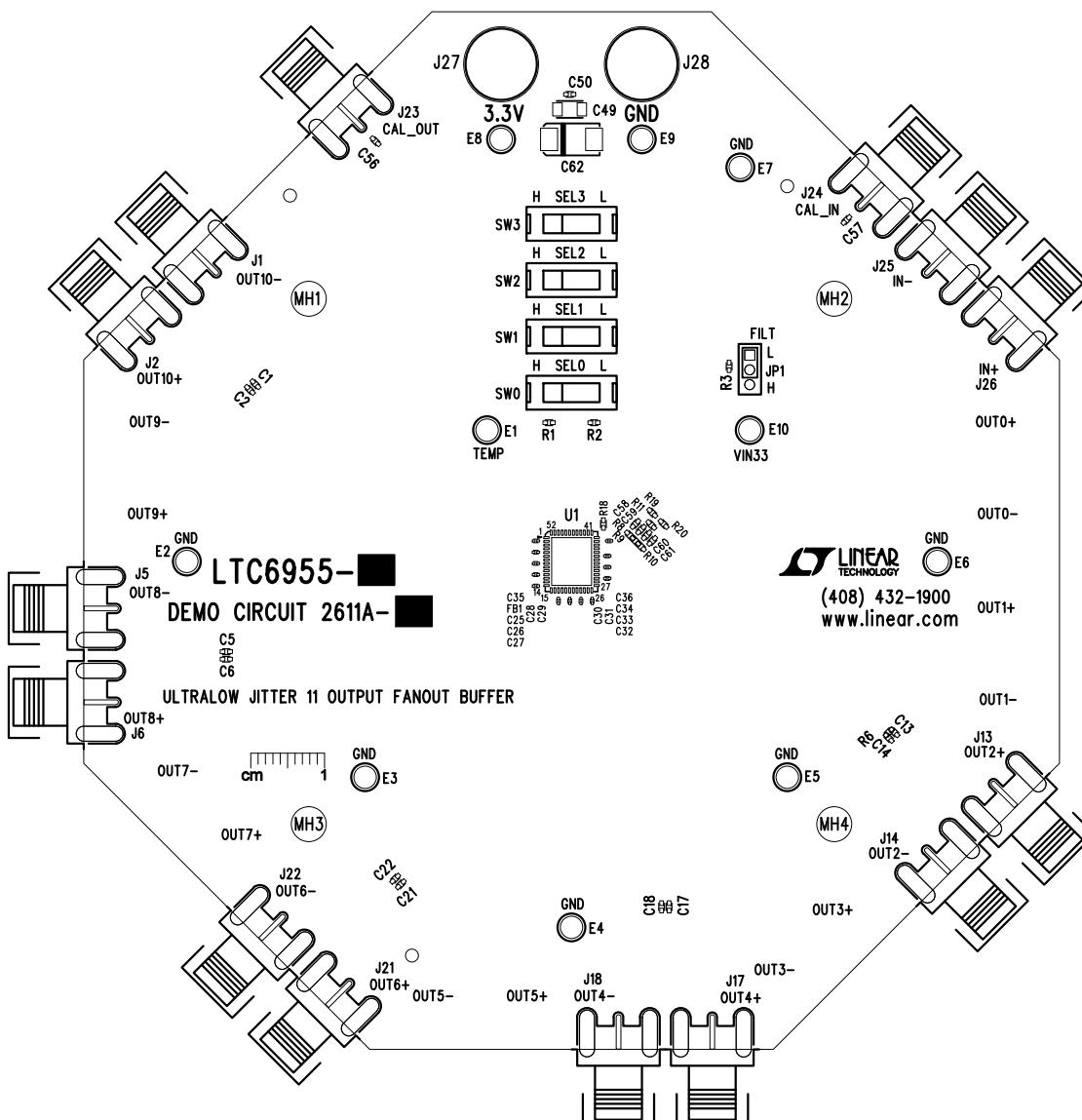
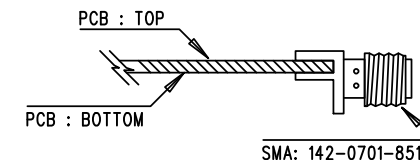
1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD. MAXIMUM SOLDER TEMPERATURE IS 240 DEGREES CELSIUS.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS. LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. INSTALL SHUNTS AS SHOWN.
5. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
6. DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD.
7. INSTALL 4 STANDOFFS AT 4 LOCATIONS AS SHOWN BELOW:



8. INSTALL J1,J2,J5,J6,J17,J18,J21-J26 ON THE TOP SIDE AS SHOWN BELOW:



9. INSTALL J9-J12 ON THE BOTTOM SIDE AS SHOWN BELOW:



APPROVALS		LINEAR TECHNOLOGY		
PCB DES.	KIM T.	1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408) 432-1900 www.linear.com LTC CONFIDENTIAL - FOR CUSTOMER USE ONLY		
APP ENG.	CHRIS P.			
		TITLE: TOP ASSEMBLY DRAWING		
		ULTRALOW JITTER 11 OUTPUT FANOUT BUFFER		
		SIZE	IC NO.	REV.
		N/A	LTC6955IUKG DEMOCIRCUIT 2611A	3
SCALE = NONE		FILENAME:	DC2611A-3.PCB	SHT 1 OF 2