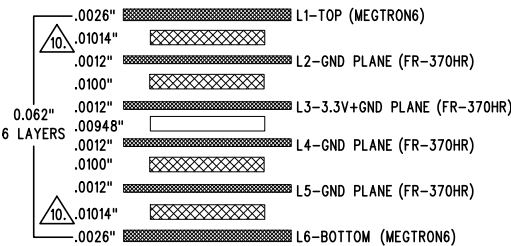


REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	3	PRODUCTION	CHRIS P.	07-06-17

SHOWN FROM TOP SIDE

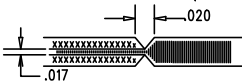
SIZE	QTY	SYM	PLATED	TOL
0.012	2184	+	YES	+/-0.003"
0.07	3	×	NO	+/-0.003"
0.064	10	□	YES	+/-0.003"
0.19	4	◇	NO	+/-0.003"
0.006	24	⊗	YES	+/-0.003"
0.035	15	⊗	YES	+/-0.003"
0.207	2	⊕	YES	+/-0.003"

LAYER STRUCTURE

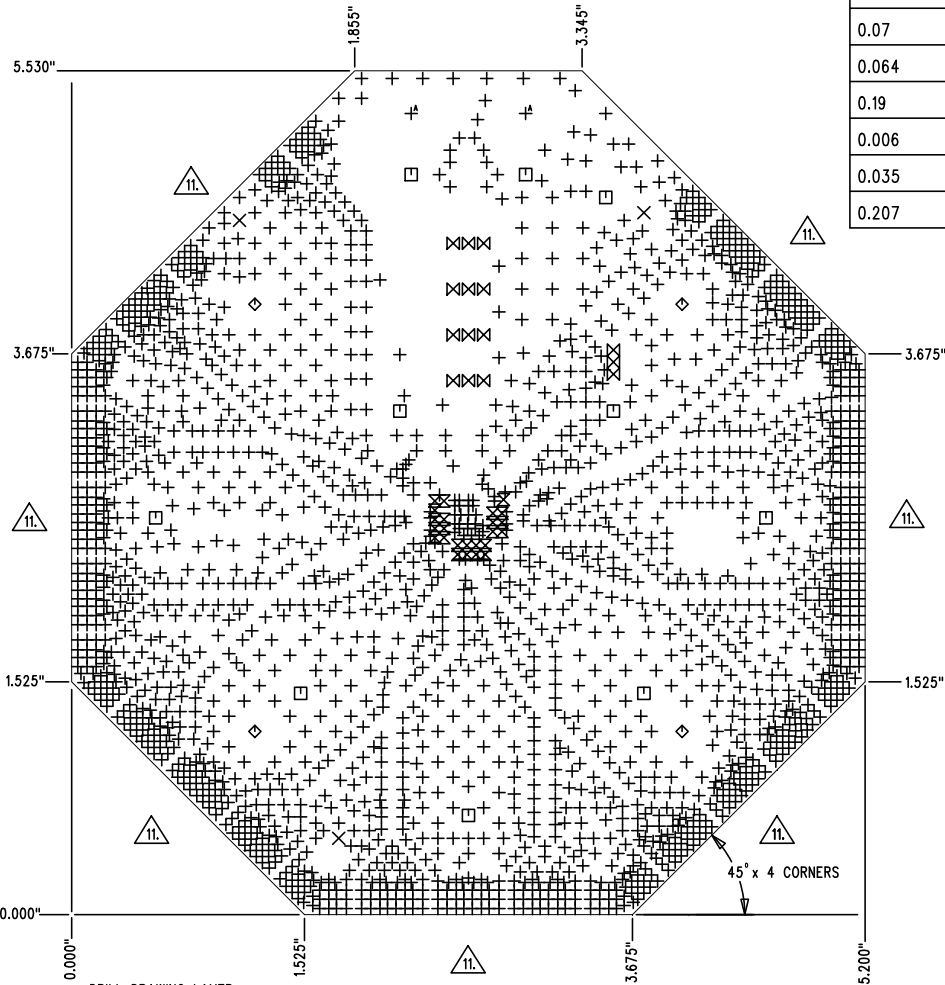


NOTES: UNLESS OTHERWISE SPECIFIED


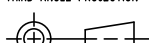
- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, MEGTRON 6 FOR OUTER LAYERS, AND ISOLAR FR-370HR FOR INNER LAYERS.  
-FINISHED THICKNESS TO BE 0.062" +/- .005"  
-TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
-FOR SILKScreens: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



- CONTROLLED 50 OHM +/-10% IMPEDANCE FOR LAYERS 1-2  
LAYER 6-5, USING 0.02" TRACE.
- SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC CONSTANT DEVIATIONS. PLEASE CONSULT LTC.
- INNER AND OUTER LAYER COPPER SHALL BE EXPOSED ALONG BOARD EDGES. DO NOT MODIFY INNER LAYER COPPER BACKOFF OUTLINE (SMA CONNECTOR).



DRILL DRAWING LAYER  
 LINEAR TECHNOLOGY DC2611A-3 LTC6955IUKG DATE: 07-06-17  
 ULTRALOW JITTER 11 OUTPUT FANOUT BUFFER

UNLESS OTHERWISE SPECIFIED		APPROVALS		 <b>LINEAR TECHNOLOGY</b> 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408) 432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY		
DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX" = ±0.01" 0.XXX" = ±0.005"  INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		PCB DES.	KIM T.			TITLE: FABRICATION DRAWING
		APP ENG.	CHRIS P.	ULTRALOW JITTER 11 OUTPUT FANOUT BUFFER		
				SIZE	IC NO.	
				N/A	LTC6955IUKG	REV
					DEMO CIRCUIT 2611A	3
				SCALE = NONE	FILENAME: DC2611A-3.PCB	SHT 1 OF 1