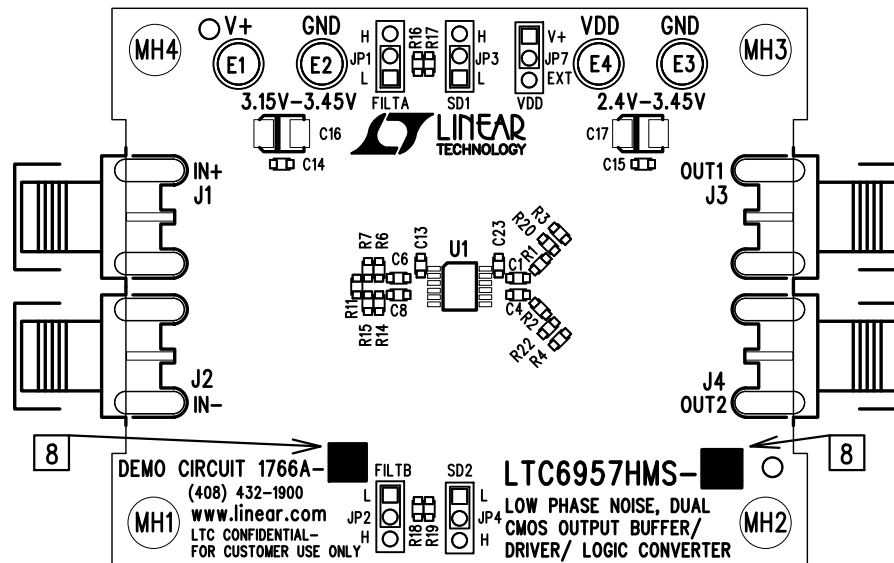
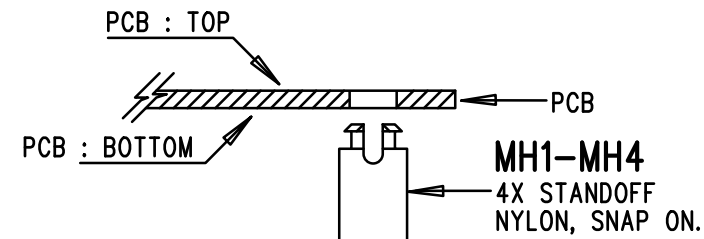
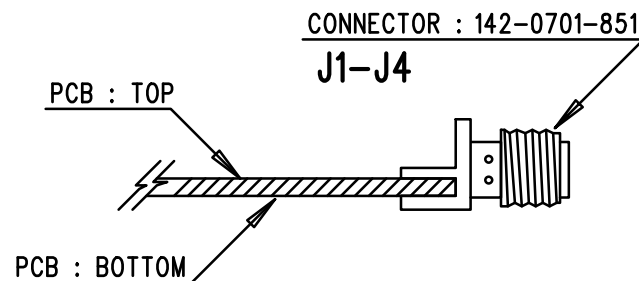


REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	3	PRODUCTION	MICHEL A.	12-17-12



NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD. MAXIMUM SOLDER TEMPERATURE IS 240 DEGREES CELSIUS.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS. LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. INSTALL SHUNTS AS SHOWN ON ASSY DRAWING.
5. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
6. DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD.
7. INSTALL 4 STANDOFFS AND SMA CONNECTORS AS SHOWN BELOW:



8. MARK EACH LTC PART NUMBER AND ASSEMBLY TYPE WITH BLACK PERMANENT MARKER AS SHOWN IN TABLE BELOW:

ASSY		U1
-A	IN-PHASE CMOS	LTC6957HMS-3
-B	COMPLEMENTARY CMOS	LTC6957HMS-4

APPROVALS

PCB DES.	KIM T.
APP ENG.	MICHEL A.
SCALE = NONE	



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TITLE: TOP ASSEMBLY DRAWING LOW PHASE NOISE, DUAL CMOS OUTPUT BUFFER/ DRIVER/ LOGIC CONVERTER		
SIZE N/A	IC NO. LTC6957HMS-3/-4 DEMO CIRCUIT 1766A	REV. 3
FILENAME: DC1766A-3.PCB		SHT 1 OF 1