

REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	3	PRODUCTION	YT	12-20-21

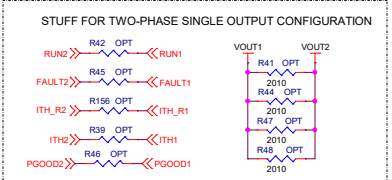
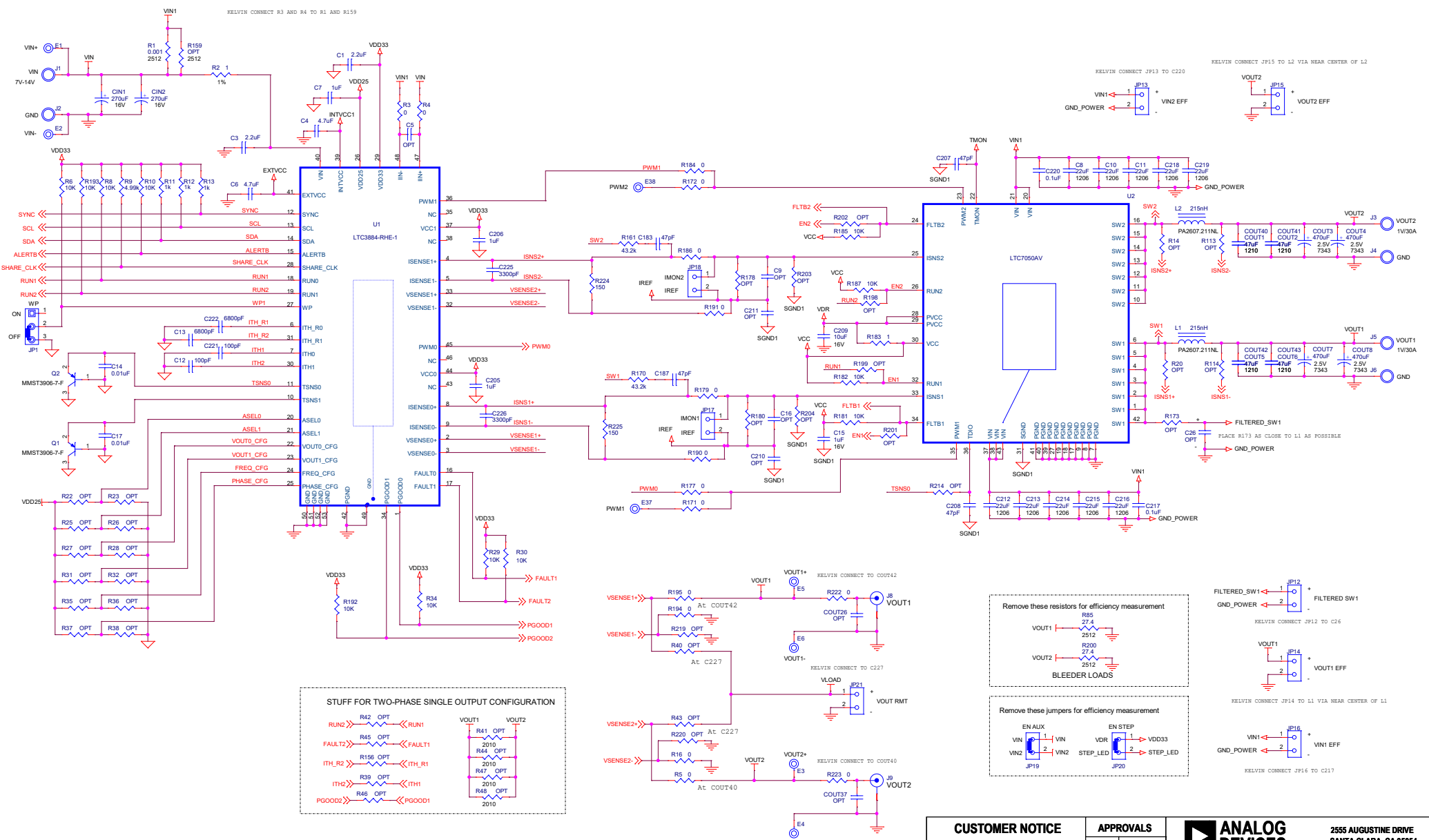


TABLE TO DETERMINE IDEAL Vsense LINES

# of Outputs	# of Phases per Output	Load Step on	R194	R195	R5	R16	R40	R219	R43	R220
2	1	None	0 Ohms	0 Ohms	0 Ohms	OPT	OPT			
2	1	VOUT1	OPT	0 Ohms	0 Ohms	0 Ohms	OPT			
2	1	VOUT2	0 Ohms	OPT	OPT	OPT	0 Ohms			
1	2	Any	OPT	OPT	0 Ohms	0 Ohms	0 Ohms			

NOTES:
1. L1 and L2 planes can accommodate FP0707R1-R110-R.

CUSTOMER NOTICE

ANALOG DEVICES HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS, HOWEVER IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT ANALOG DEVICES APPLICATIONS ENGINEERING FOR ASSISTANCE.

APPROVALS

PCB DES.	LT
APP ENG.	YT

ANALOG DEVICES
AHEAD OF WHAT'S POSSIBLE™

2555 AUGUSTINE DRIVE
SANTA CLARA, CA 95054
www.analog.com

TITLE: SCHEMATIC

HIGH EFFICIENCY SYNCHRONOUS BUCK CONVERTER WITH PSM

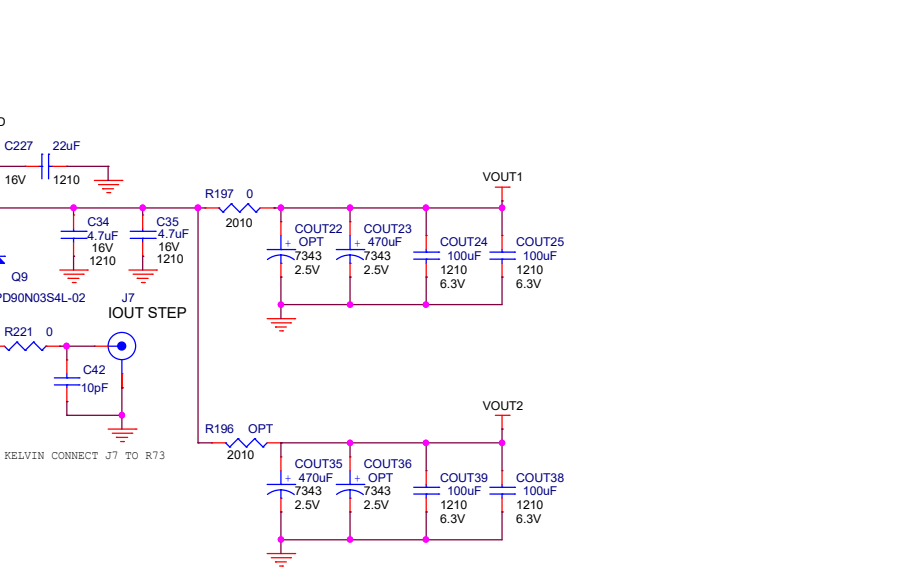
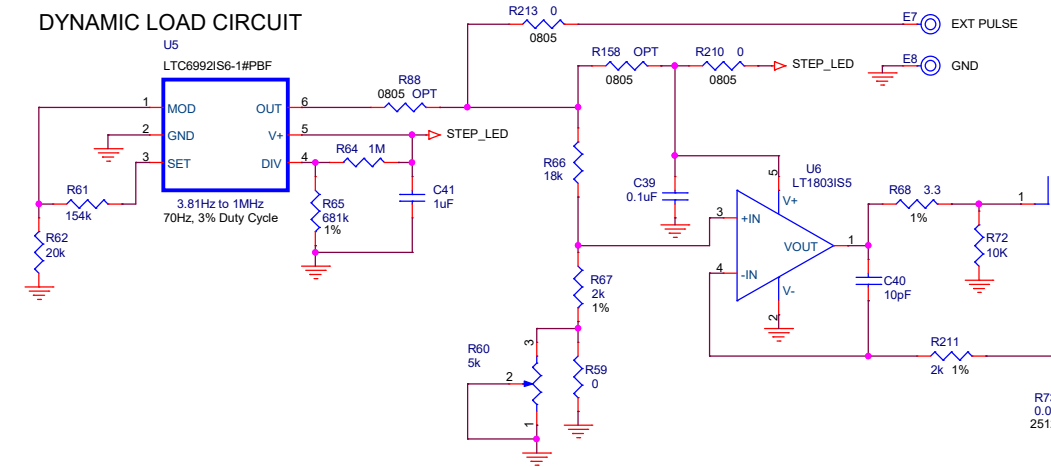
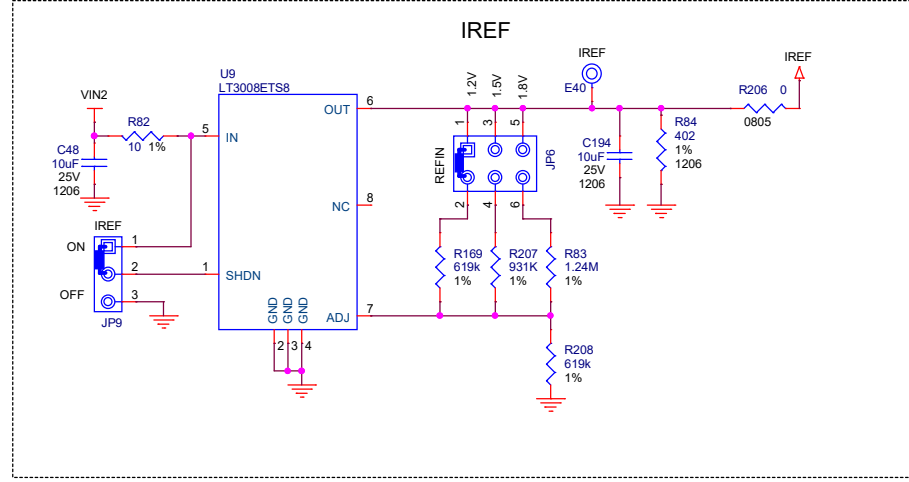
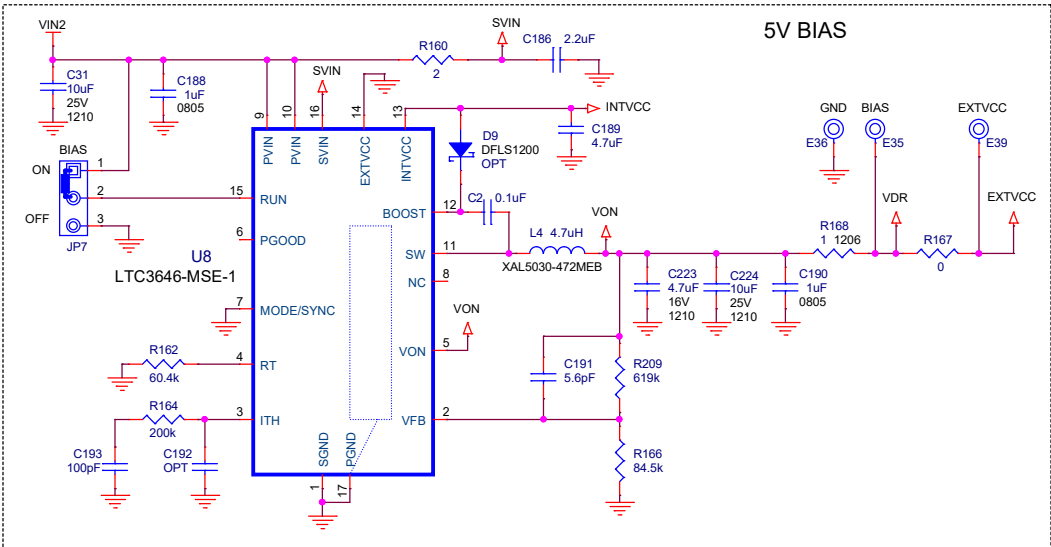
IC NO. LTC7050X / LTC3884R-E1
SKU NO. DC2881(A,B,C)

SIC NO. AND REVISION:
710-DC2881(A,B,C)_REV03


SIZE: N/A DATE: Monday, December 20, 2021 SHEET 1 OF 3

* **VERSION** **U2**

-A	LTC7050
-B	LTC7050-1
-C	LTC7050P



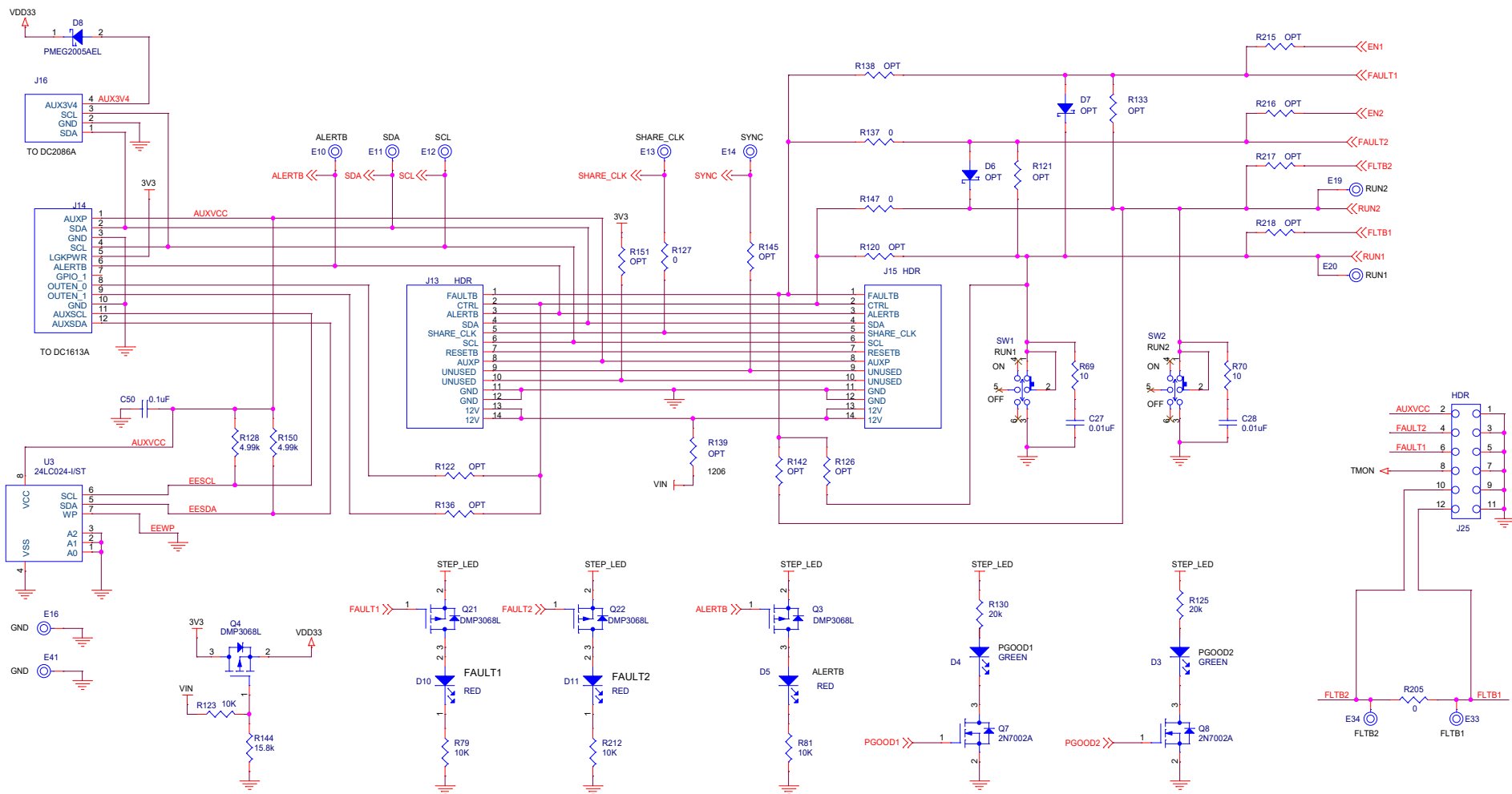
Stuffing options for dynamic load circuit:
 1) External Pulse:stuff R213,R210,R59=0, R67=2k;
 DNP:R88,R158
 2) On Board Pulse:stuff R88,R158=0, R67=301, R59=
 2.3k; DNP:R210

APPROVALS		 AHEAD OF WHAT'S POSSIBLE™	2555 AUGUSTINE DRIVE SANTA CLARA, CA 95054 www.analog.com
PCB DES.	LT		
APP ENG.	Y.T	TITLE: SCHEMATIC	SCHEMATIC NO. AND REVISION: 710-DC2881A(A,B,C)_REV03
		HIGH EFFICIENCY SYNCHRONOUS BUCK CONVERTER WITH PSM	
THIS CIRCUIT IS PROPRIETARY TO ANALOG DEVICES AND SUPPLIED FOR USE WITH ANALOG DEVICES PARTS.		IC NO. LTC7050X / LTC3884RHE-1	SIZE: N/A DATE: Monday, December 20, 2021 SHEET 2 OF 3
		SKU NO. DC2881A(A,B,C)	

CUSTOMER NOTICE

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ALL PARTS ON THIS PAGE ARE FOR DEMO BOARD ONLY, NOT NEEDED IN CUSTOMER DESIGN



HARDWARE

STNCL1	TOOL, STENCIL, 700-DC2881A	REV03	MP25	STANDOFF,NYLON,SNAP-ON,0.625 (5/8"),15.9mm
PCB1	PCB, DC2881A	REV03	MP26	STANDOFF,NYLON,SNAP-ON,0.625 (5/8"),15.9mm
LB1	LABEL		MP27	STANDOFF,NYLON,SNAP-ON,0.625 (5/8"),15.9mm
			MP28	STANDOFF,NYLON,SNAP-ON,0.625 (5/8"),15.9mm

<Variant Name>

CUSTOMER NOTICE		APPROVALS			2555 AUGUSTINE DRIVE SANTA CLARA, CA 95054 www.analog.com
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SIZE:	N/A	DATE:	Monday, December 20, 2021	SHEET 3 OF 3	