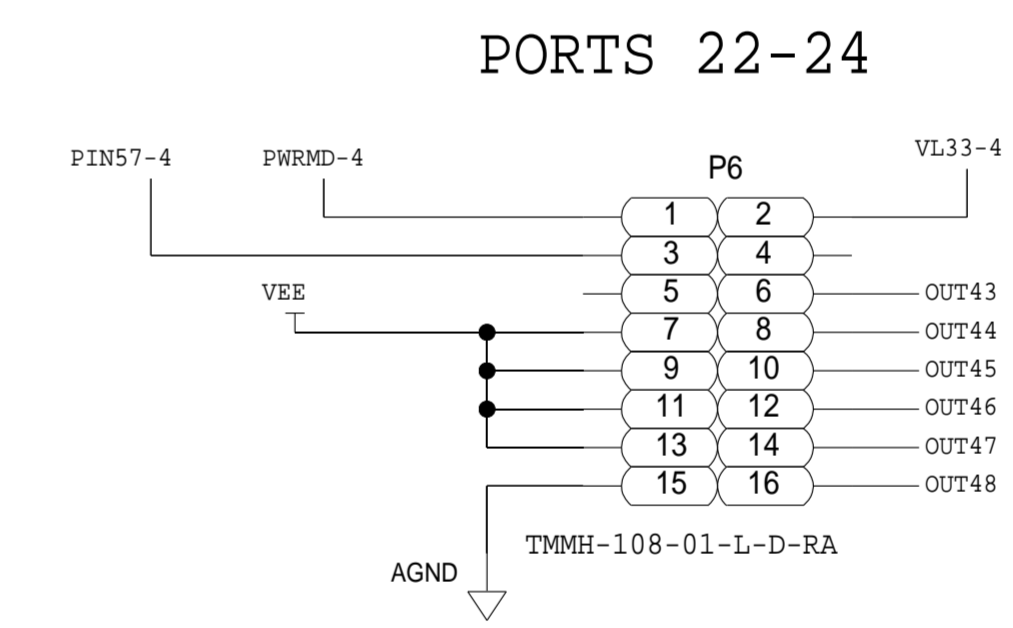
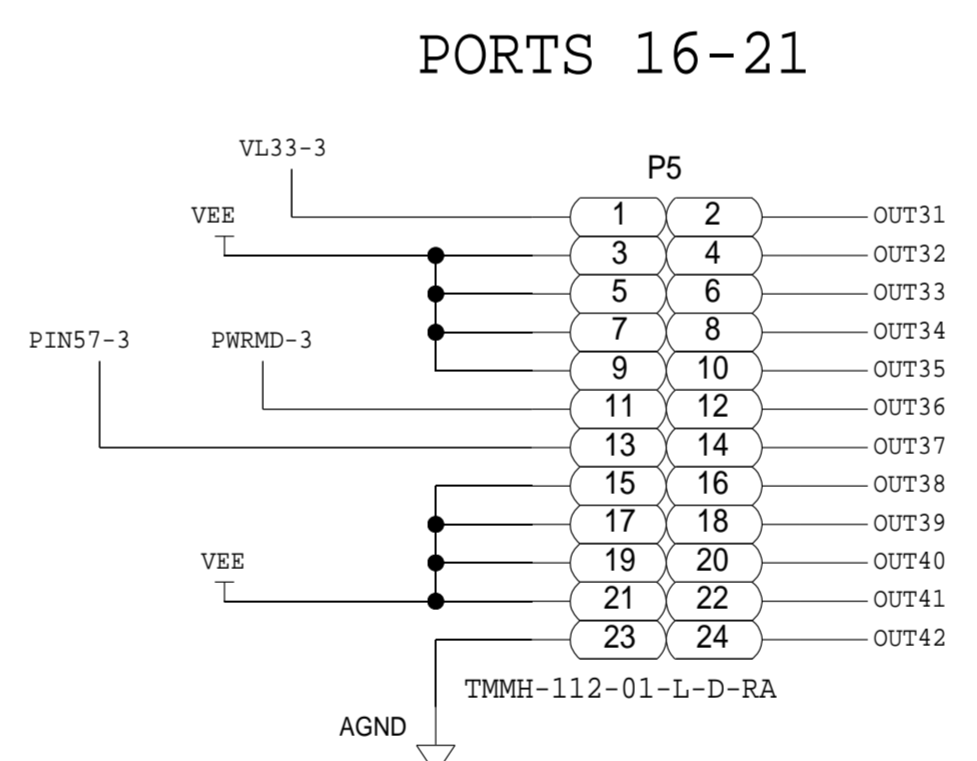
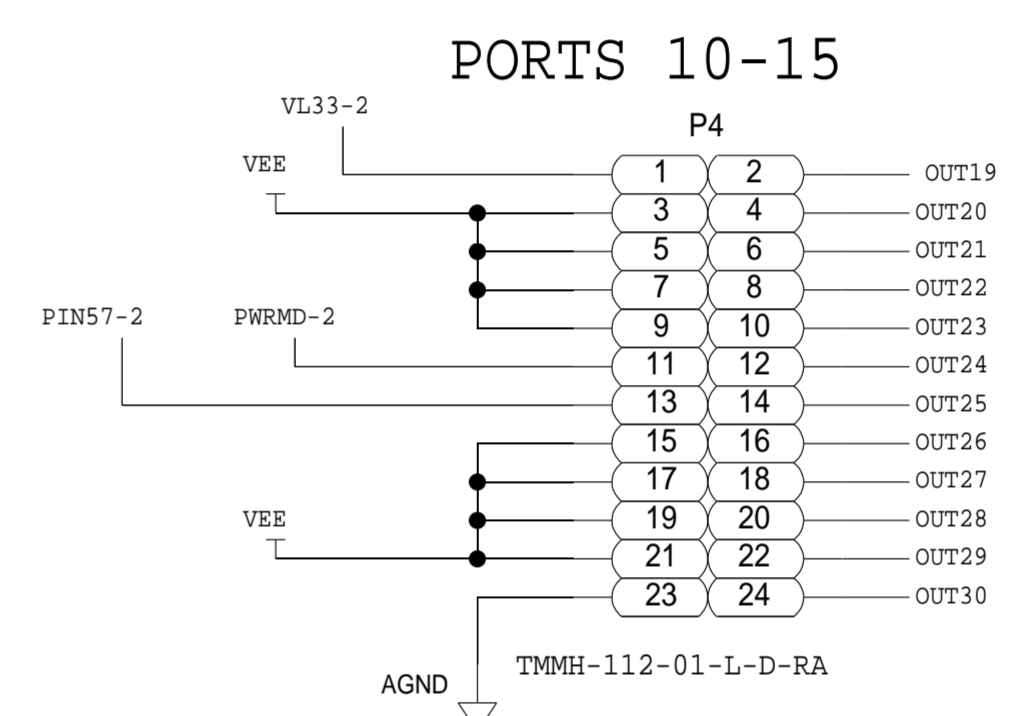
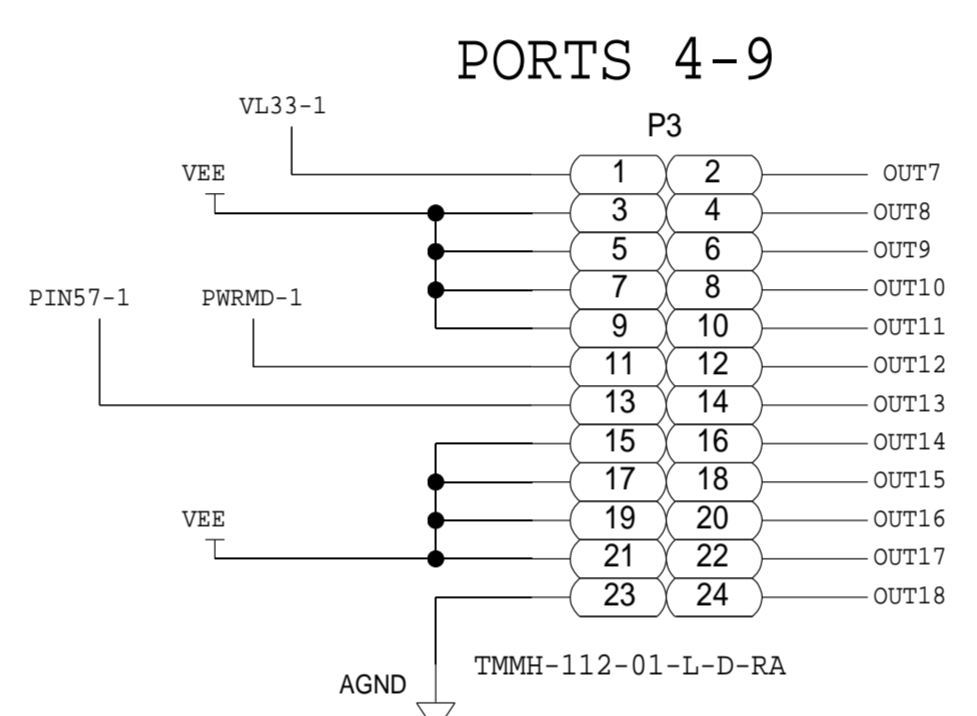
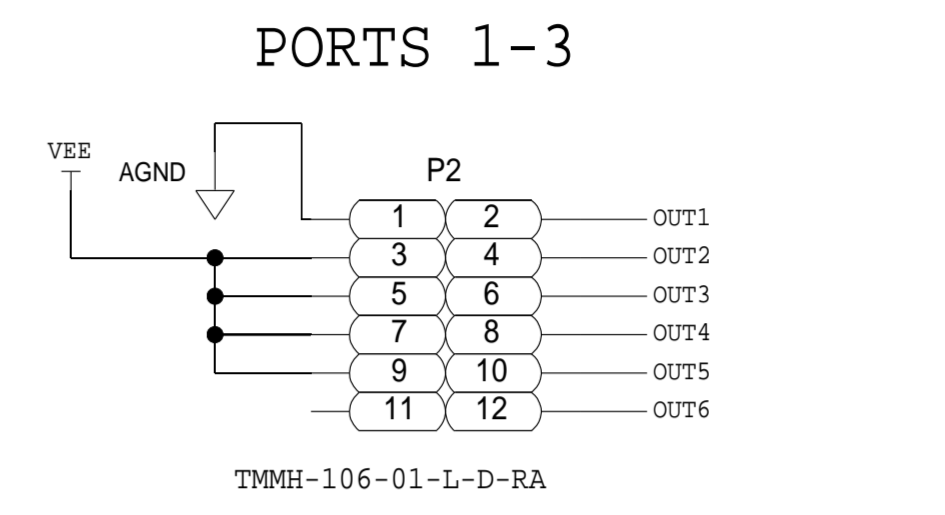
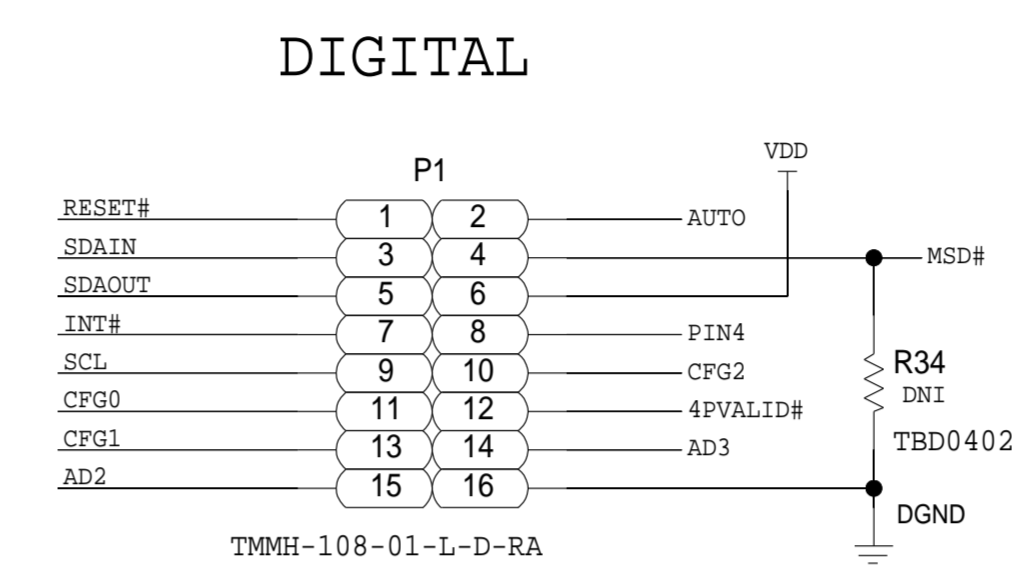
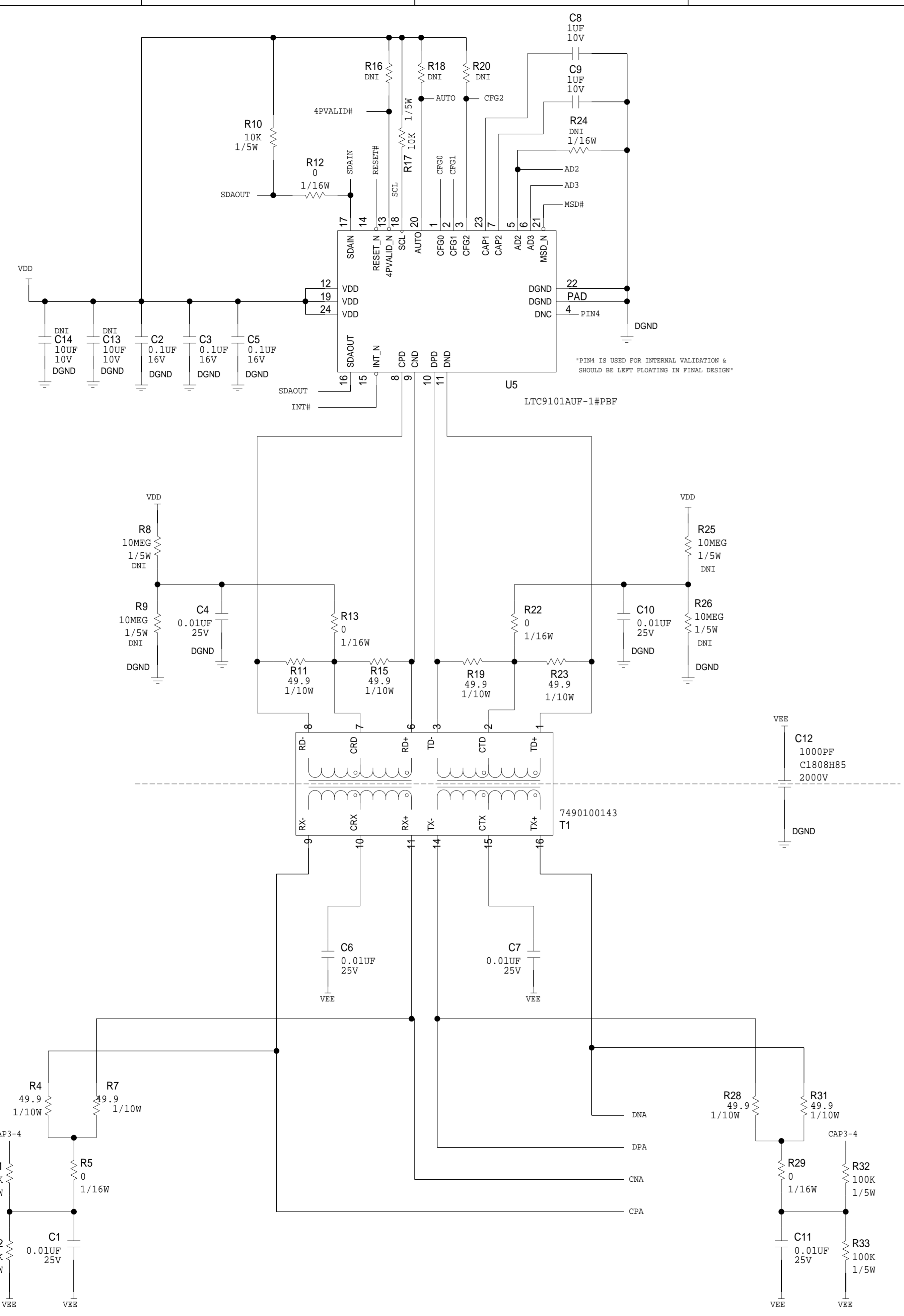




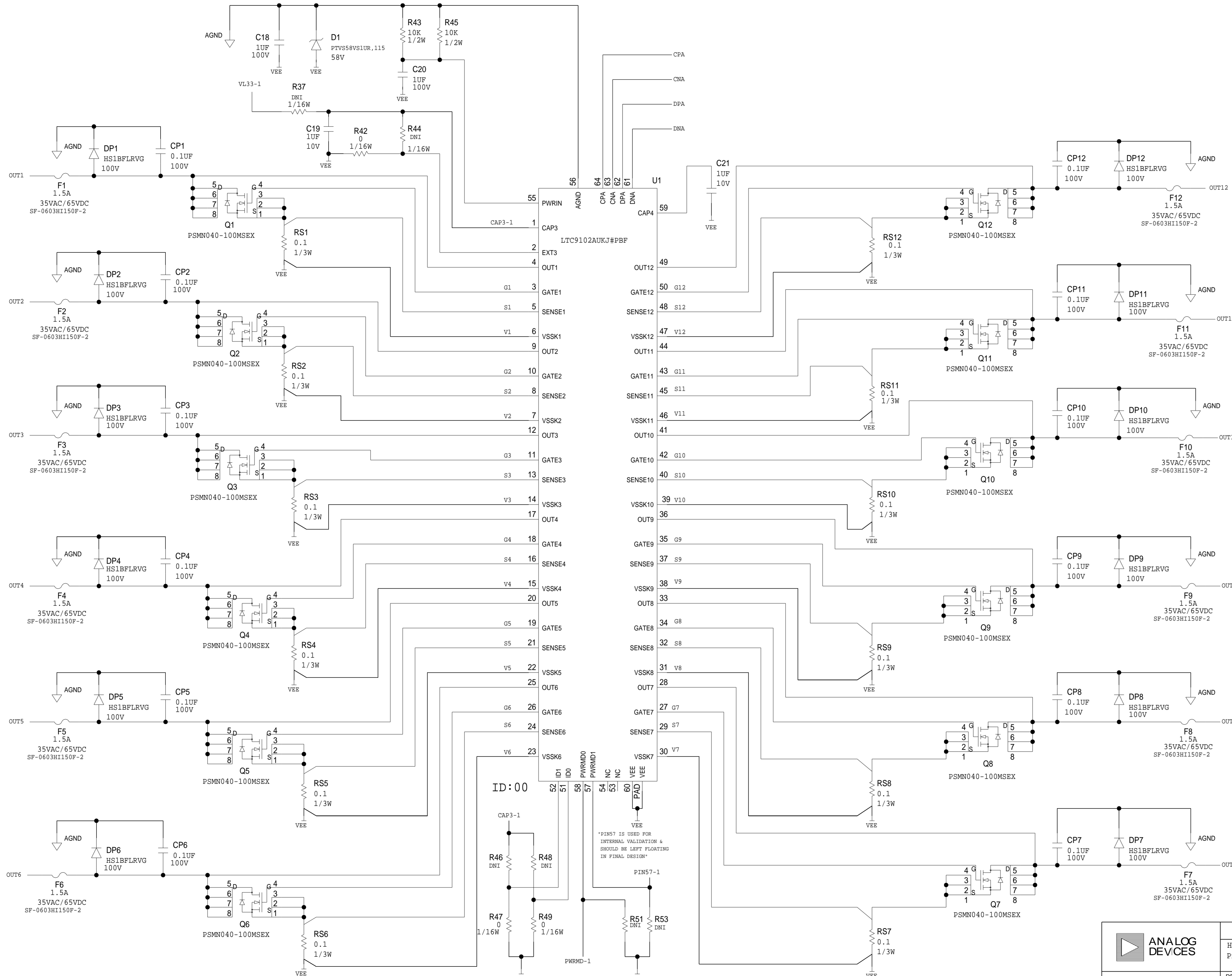
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



SCHEMATIC			
		HW TYPE : Customer Evaluation Z Product(s) : LTC9101-1/LTC9102 : ADI DEMO BOARD	
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_059470	REV D	
PTD ENGINEER D. REYES	SIZE D	SCALE 1:1	SHEET 2 OF 6

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REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



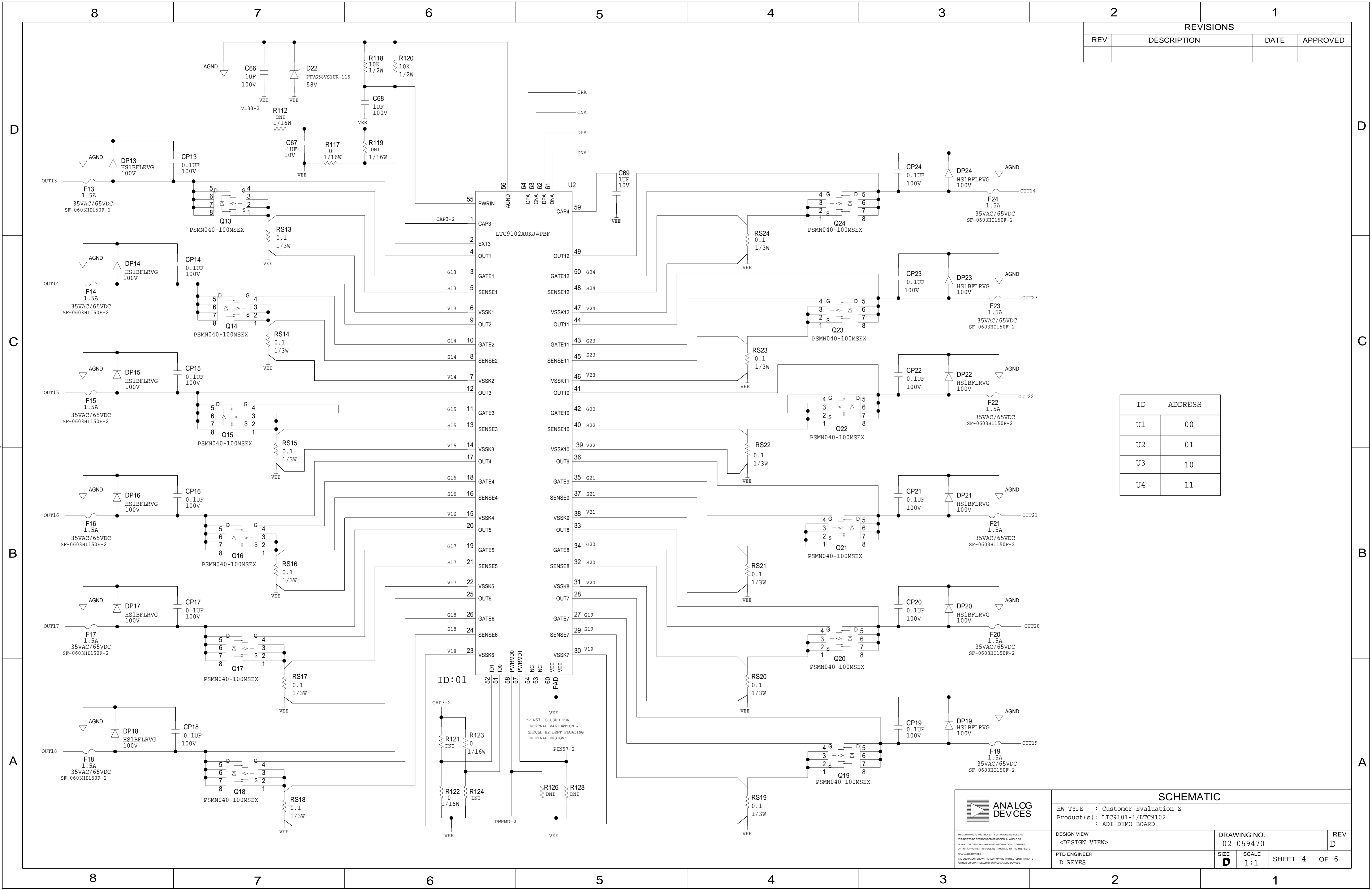
ID	ADDRESS
U1	00
U2	01
U3	10
U4	11

	<b>SCHEMATIC</b>		
	HW TYPE : Customer Evaluation Z Product(s) : LTC9101-1/LTC9102 : ADI DEMO BOARD		
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_059470	REV D	
PTD ENGINEER D. REYES	SIZE D	SCALE 1:1	SHEET 3 OF 6

\*PINS7 IS USED FOR INTERNAL VALIDATION & SHOULD BE LEFT FLOATING IN FINAL DESIGN\*

ID: 00

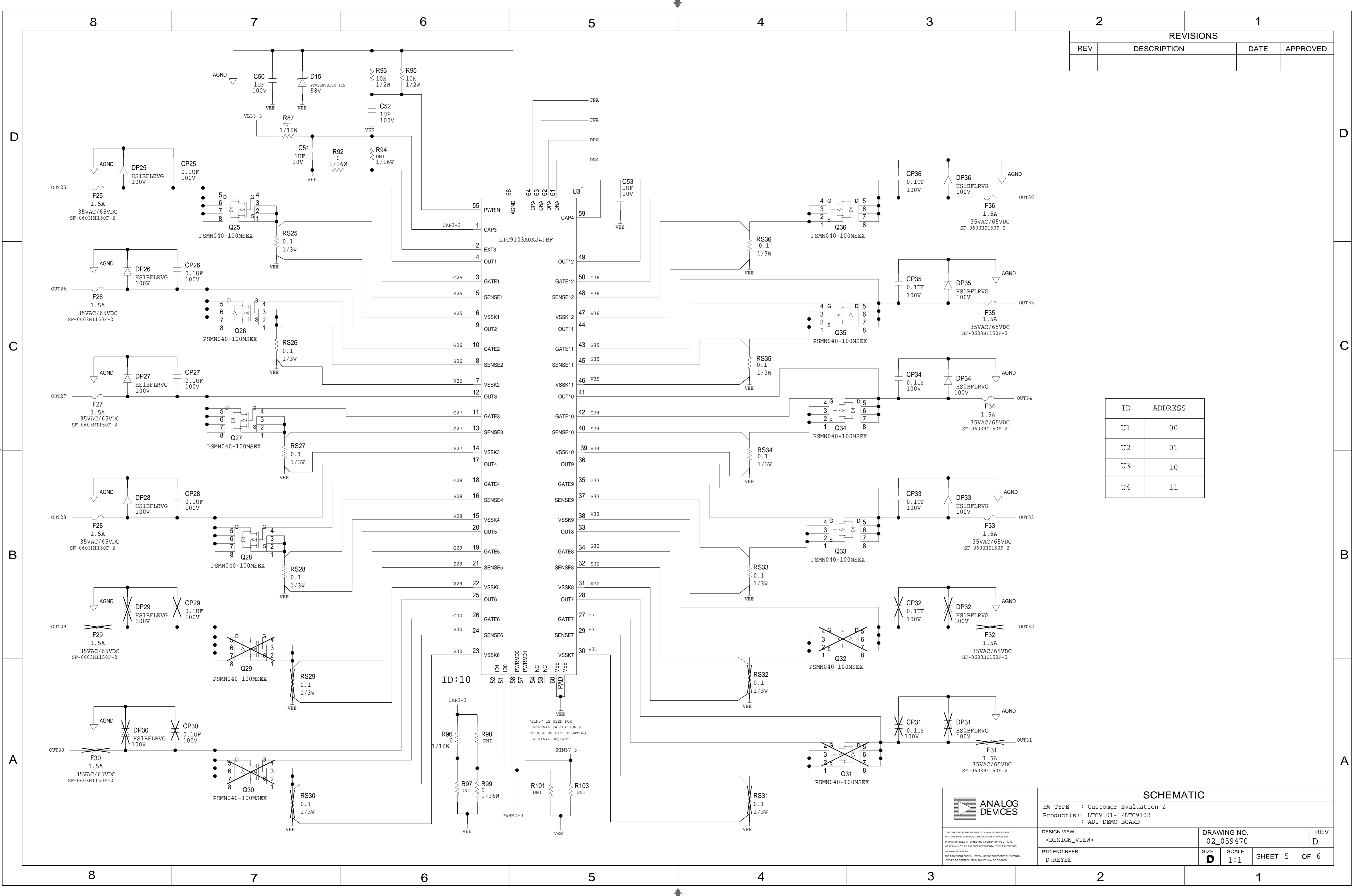
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



ID	ADDRESS
U1	00
U2	01
U3	10
U4	11

	<b>SCHEMATIC</b>		
	HW TYPE : Customer Evaluation Z Product(s) : LTC9101-1/LTC9102 : ADI DEMO BOARD		
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_059470	REV D	
PTD ENGINEER D. REYES	SIZE D	SCALE 1:1	SHEET 4 OF 6

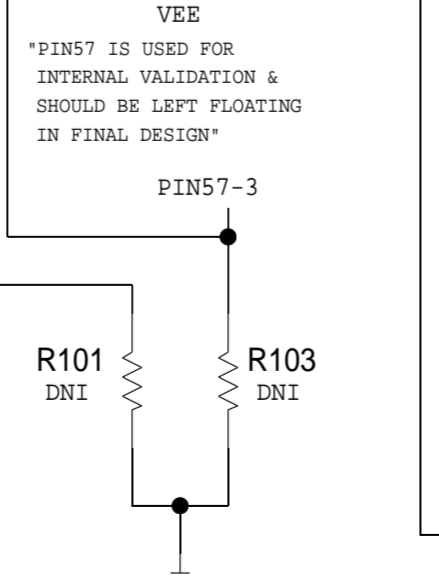
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REV	DESCRIPTION	DATE	APPROVED



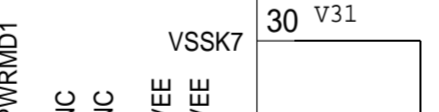
ID	ADDRESS
U1	00
U2	01
U3	10
U4	11

	<b>SCHEMATIC</b>		
	HW TYPE : Customer Evaluation Z Product(s) : LTC9101-1/LTC9102 : ADI DEMO BOARD		
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_059470	REV D	
PTD ENGINEER D. REYES	SIZE D	SCALE 1:1	SHEET 5 OF 6

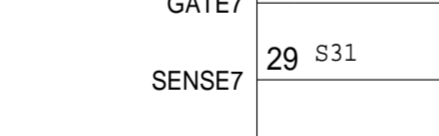
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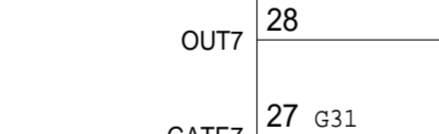
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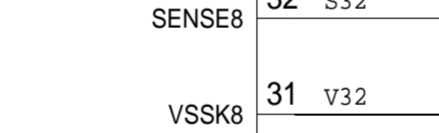
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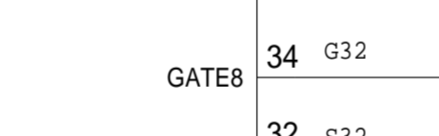
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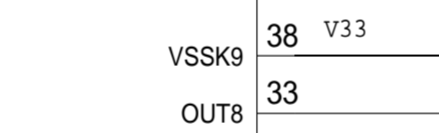
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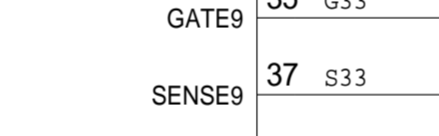
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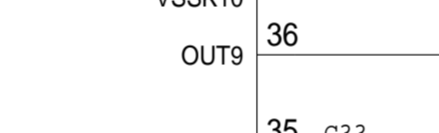
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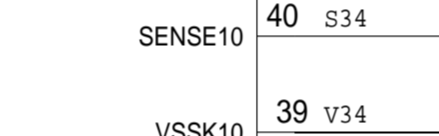
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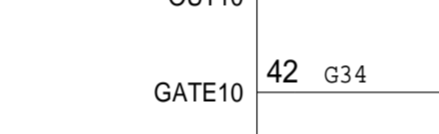
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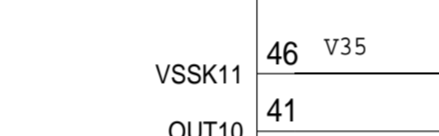
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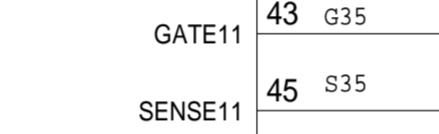
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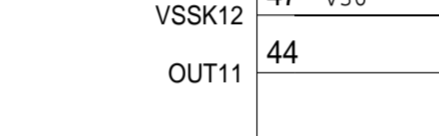
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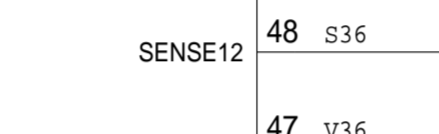
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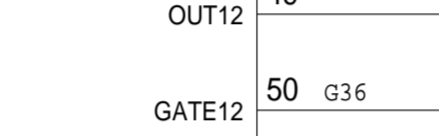
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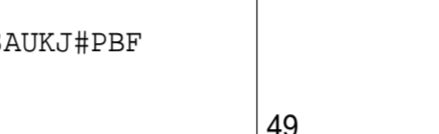
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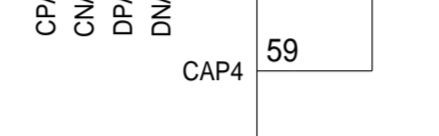
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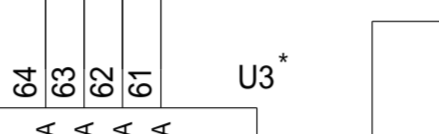
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VEE



VEE



VEE



VEE



VEE



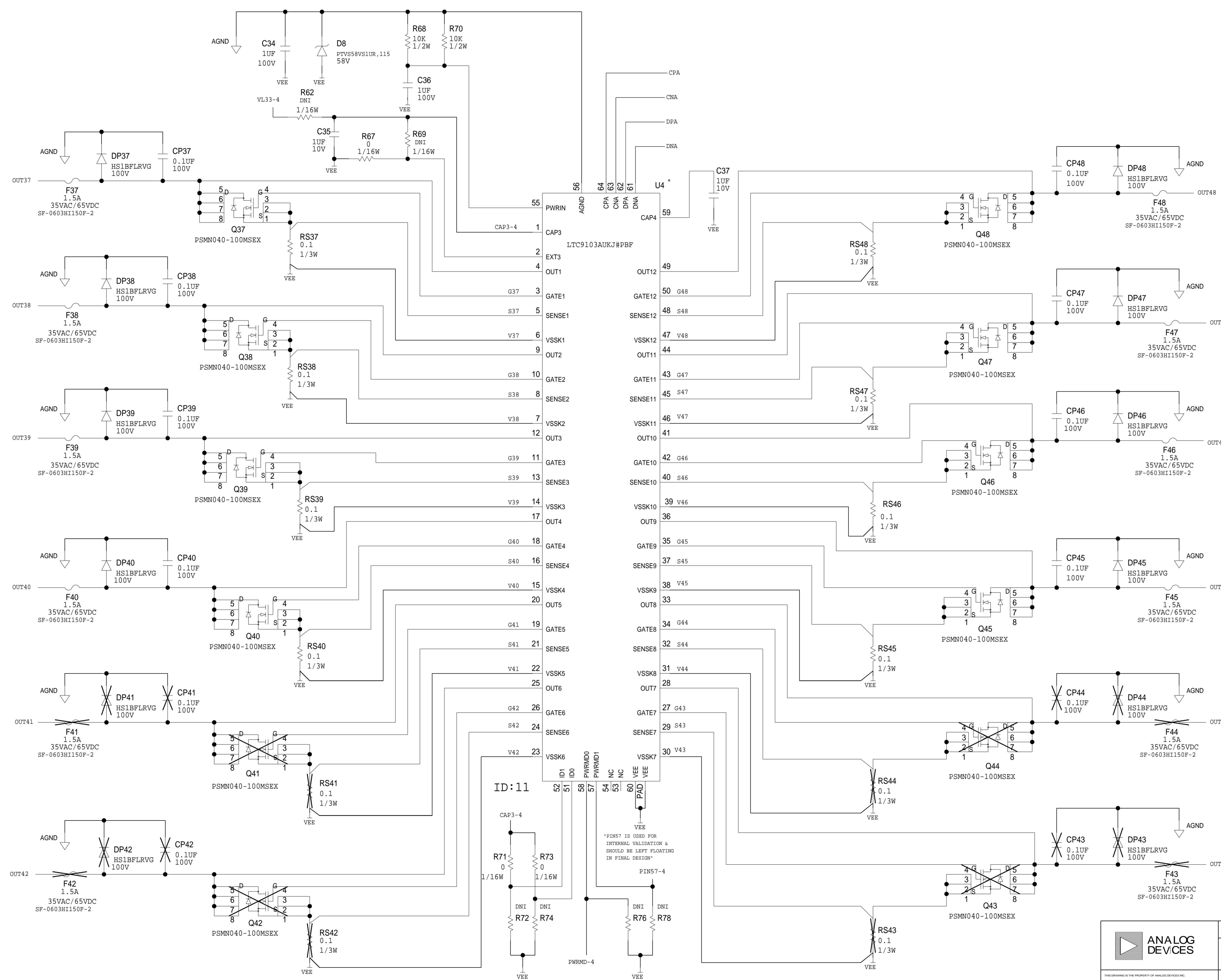
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VEE



REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



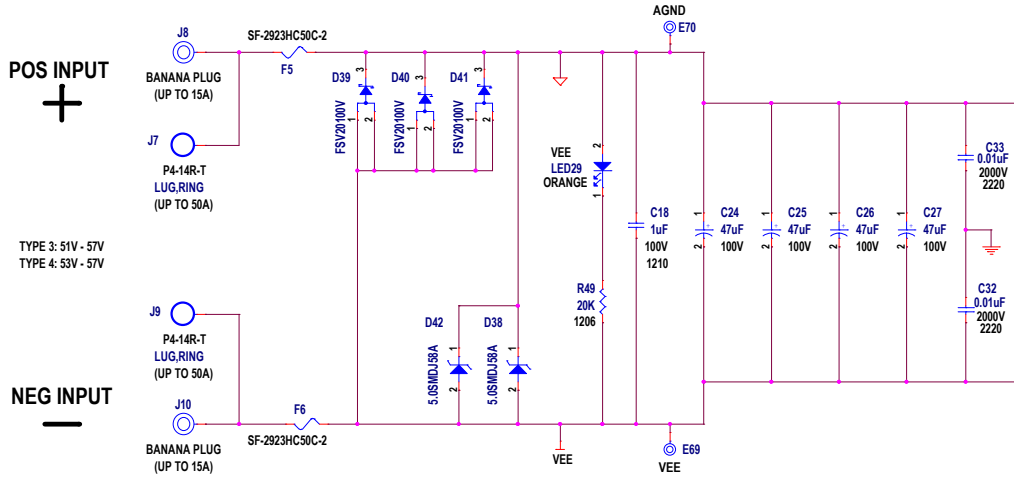
ID	ADDRESS
U1	00
U2	01
U3	10
U4	11

	<b>SCHEMATIC</b>		
	HW TYPE : Customer Evaluation Z Product(s) : LTC9101-1/LTC9102 : ADI DEMO BOARD		
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_059470	REV D	
PTD ENGINEER D. REYES	SIZE D	SCALE 1:1	SHEET 6 OF 6

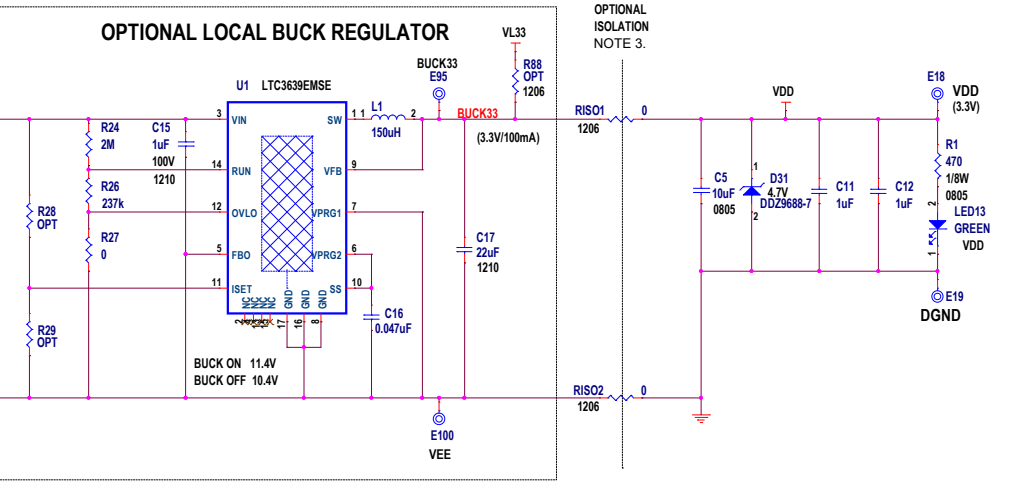
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	3			

**POWER SUPPLY**

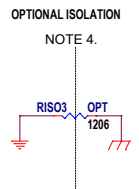
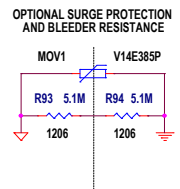
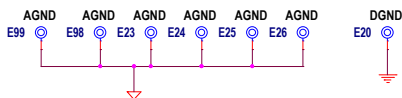
**MAIN SUPPLY INPUT**      **OPTIONAL OVERCURRENT, OVERVOLTAGE, AND REVERSE VOLTAGE PROTECTION**



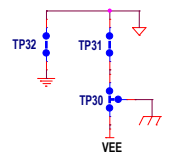
**OPTIONAL LOCAL BUCK REGULATOR**



OPTIONAL ISOLATION NOTE 3.



- NOTE: UNLESS OTHERWISE SPECIFIED**
1. ALL RESISTORS AND CAPACITORS ARE 0603.
  2. INSTALL SHUNTS AS SHOWN.
  3. REMOVE RESISTORS TO ISOLATE DIGITAL AND ANALOG DOMAINS
  4. POPULATE RESISTOR TO CONNECT DIGITAL GROUND TO CHASSIS

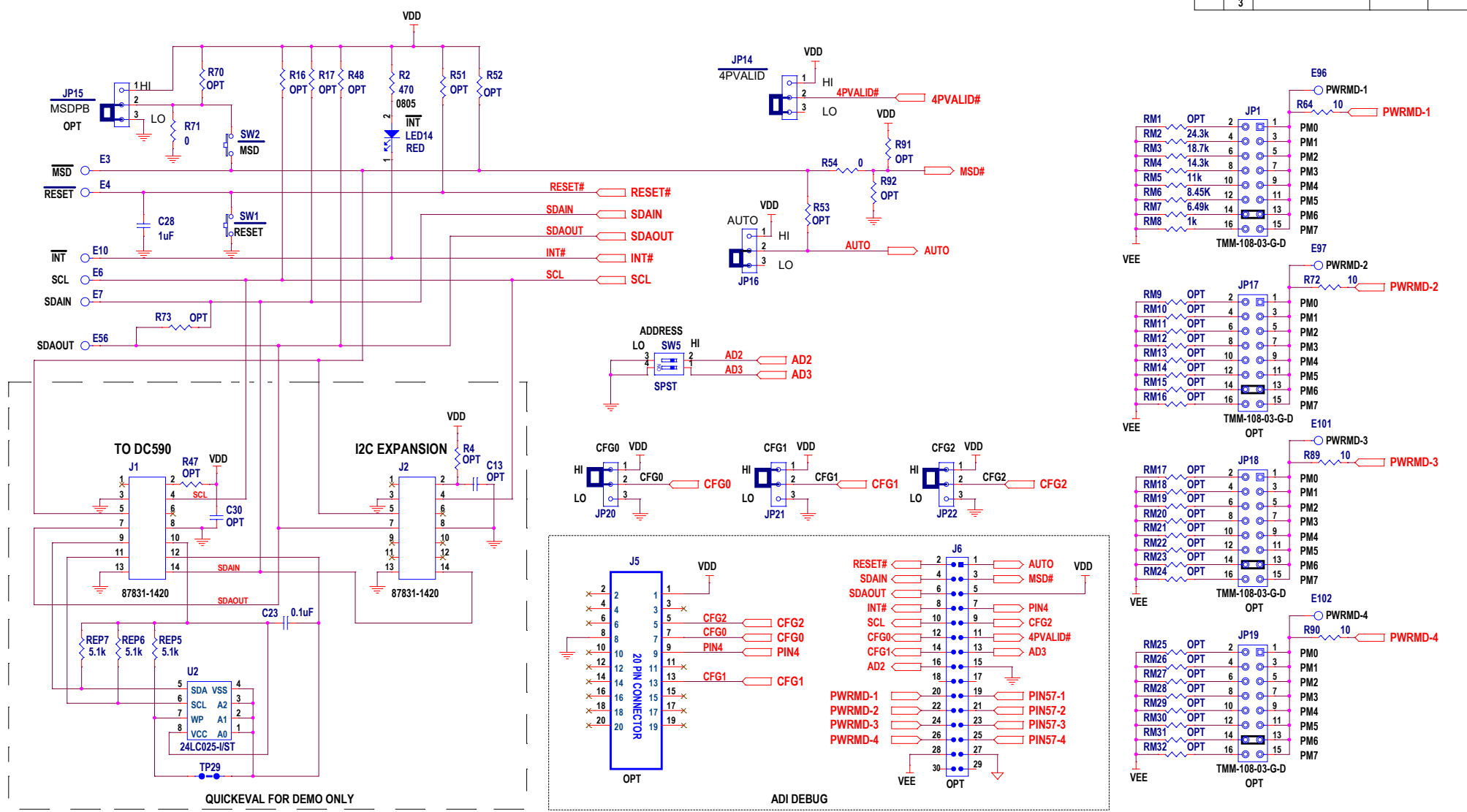


<p><b>CUSTOMER NOTICE</b></p> <p>ANALOG DEVICES HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS. HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND DESIRED OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT ANALOG DEVICES APPLICATION ENGINEERING FOR ASSISTANCE.</p> <p>THIS CIRCUIT IS PROPRIETARY TO ANALOG DEVICES AND SUPPLIED FOR USE WITH ANALOG DEVICES PARTS.</p>	<p><b>APPROVALS</b></p> <table border="1"> <tr> <th>PCB DES.</th> <th>MEM</th> </tr> <tr> <td>APP ENG.</td> <td>ERIC.H</td> </tr> </table>	PCB DES.	MEM	APP ENG.	ERIC.H	<p><b>ANALOG DEVICES</b>      <b>POWER BY LINAR</b></p> <p>www.analog.com      Phone: (408)432-1900</p> <p>TITLE: SCHEMATIC</p> <p>24-PORT IEEE 802.3M PSE MOTHER BOARD</p> <p>IC NO. LTC3639EMSE#TRPBF      SCHEMATIC NO. AND REVISION: 710-DC3017A-A_REV03</p> <p>SKU NO. DC3017A</p> <p>SIZE: N/A      DATE:</p> <p>SHEET 1 OF 11</p>
	PCB DES.	MEM				
	APP ENG.	ERIC.H				



# CONFIGURATIONS

ECO		REV	DESCRIPTION	APPROVED	DATE
		3			



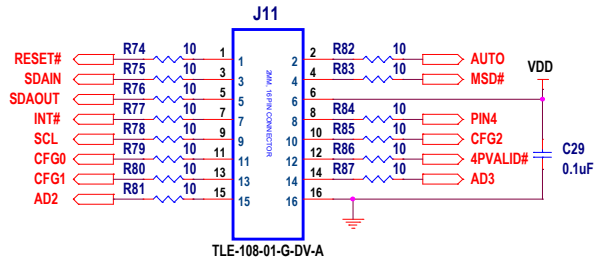
**NOTE: UNLESS OTHERWISE SPECIFIED**  
 1. ALL RESISTORS AND CAPACITORS ARE 0603.  
 2. INSTALL SHUNTS AS SHOWN.

CUSTOMER NOTICE		APPROVALS		
<small>ANALOG DEVICES HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT ANALOG DEVICES APPLICATIONS ENGINEERING FOR ASSISTANCE.</small>		PCB DES.	KEN.M	
<small>THIS CIRCUIT IS PROPRIETARY TO ANALOG DEVICES AND SUPPLIED FOR USE WITH ANALOG DEVICES PARTS.</small>		APP ENG.	ERIC.H	
				<b>TITLE: SCHEMATIC</b> <b>24-PORT IEEE 802.3bt PSE MOTHER BOARD</b>
IC NO. LTC3639EMSE#TRPBF		SCHEMATIC NO. AND REVISION:		
SKU NO. DC3017A		710-DC3017A-A_REV03		
SIZE: N/A	DATE:			SHEET 2 OF 11

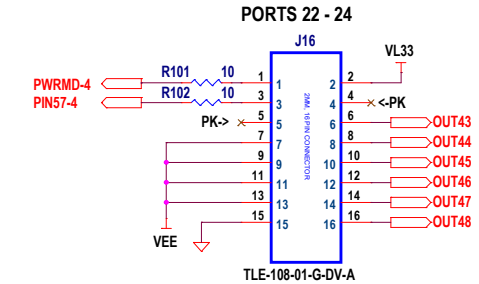
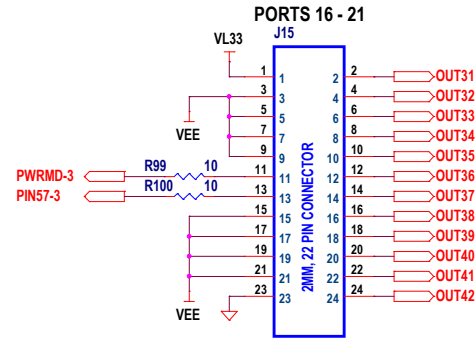
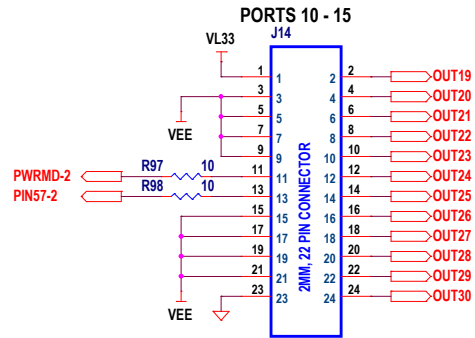
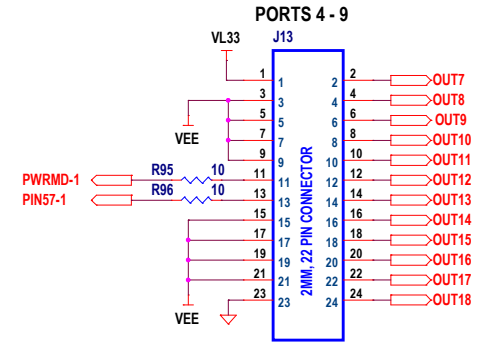
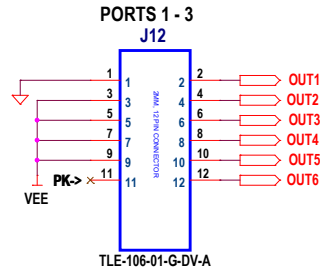


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	3			

DIGITAL CONNECTIONS



ANALOG CONNECTIONS



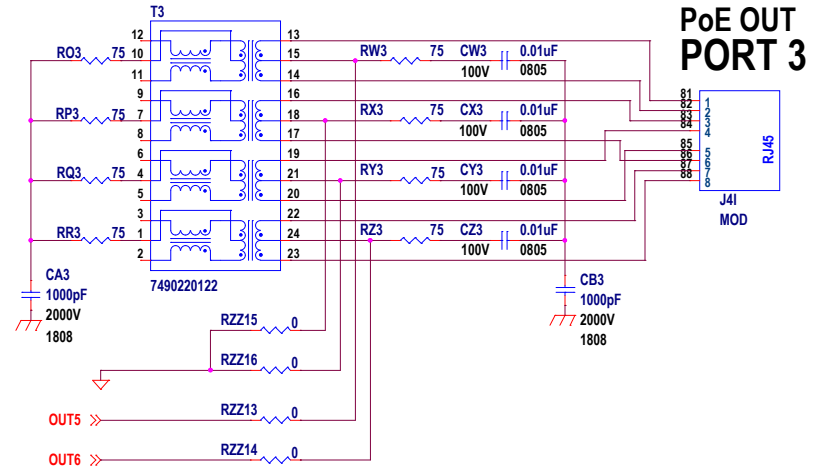
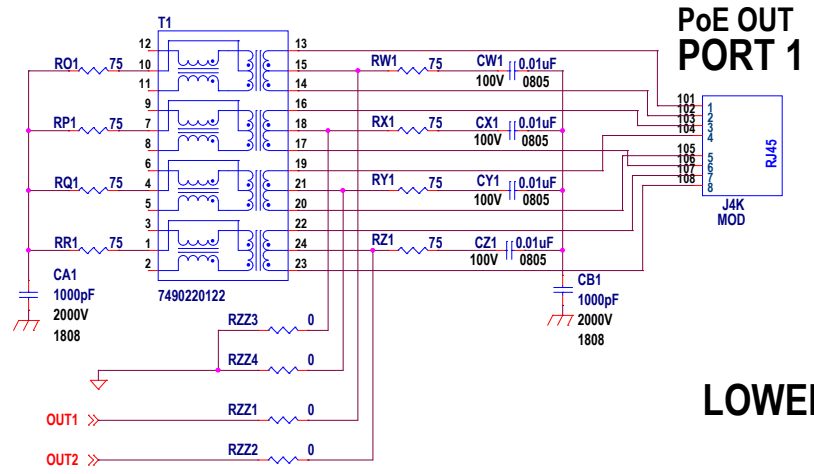
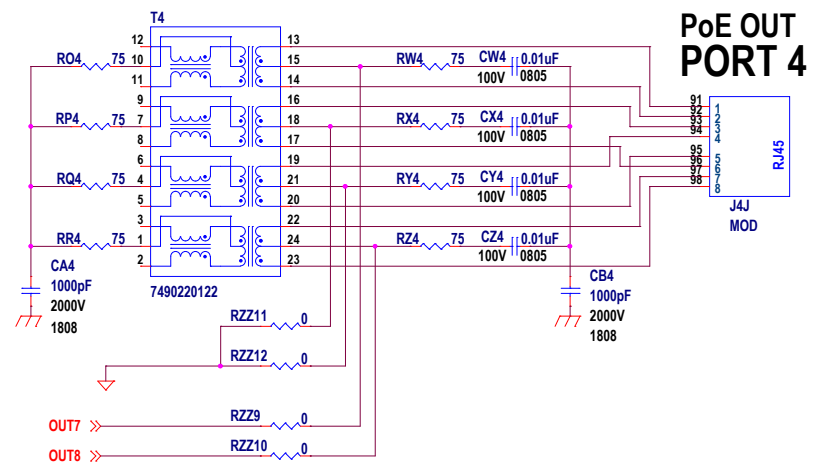
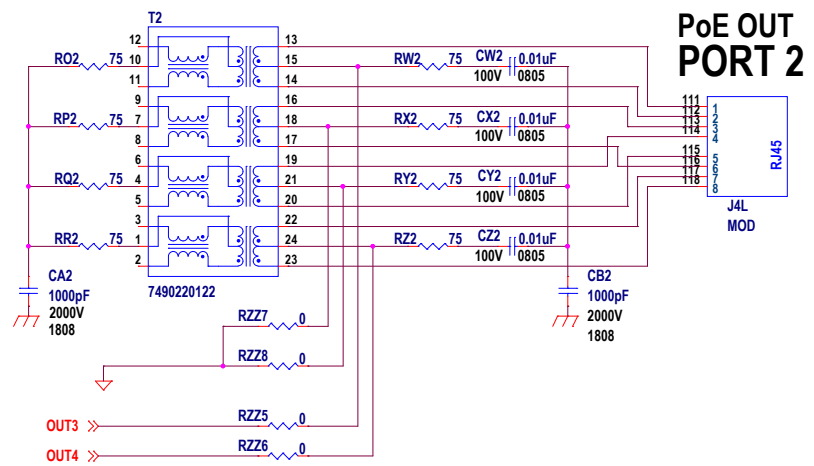
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	PCB DES.	KEM/M					
	APP ENG.	ERIC/H					
	<p>TITLE: SCHEMATIC</p> <p>24-PORT IEEE802.3bt PSE MOTHER BOARD</p>						
<p>IC NO. LTC3839EMSE#TRPBF</p> <p>SKU NO. DC3017A</p>		<p>SCHEMATIC NO. AND REVISION:</p> <p>710-DC3017A-A_REV03</p>					
<p>SIZE: N/A</p>	<p>DATE:</p>	<p>SHEET 3 OF 11</p>					

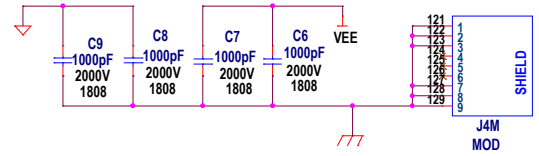
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	3			

PORTS 1 - 4

UPPER ROW



LOWER ROW



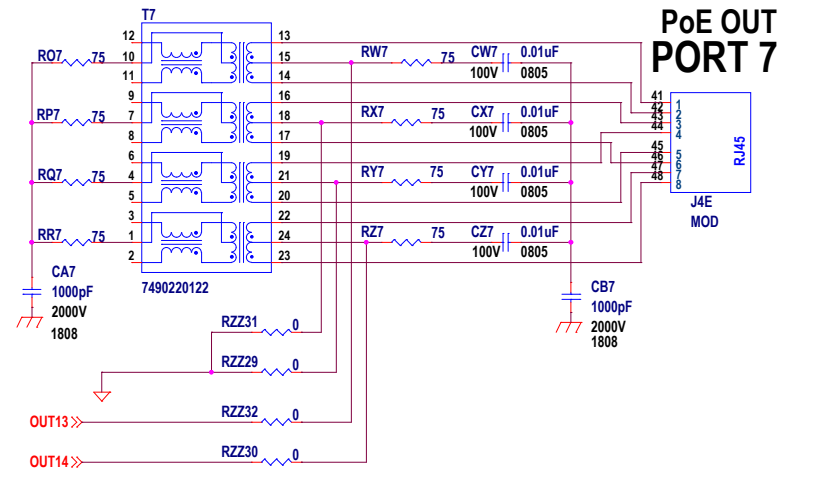
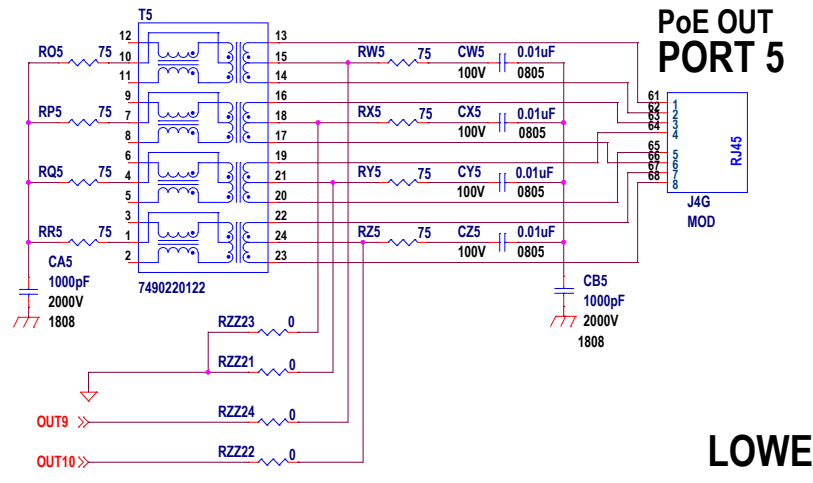
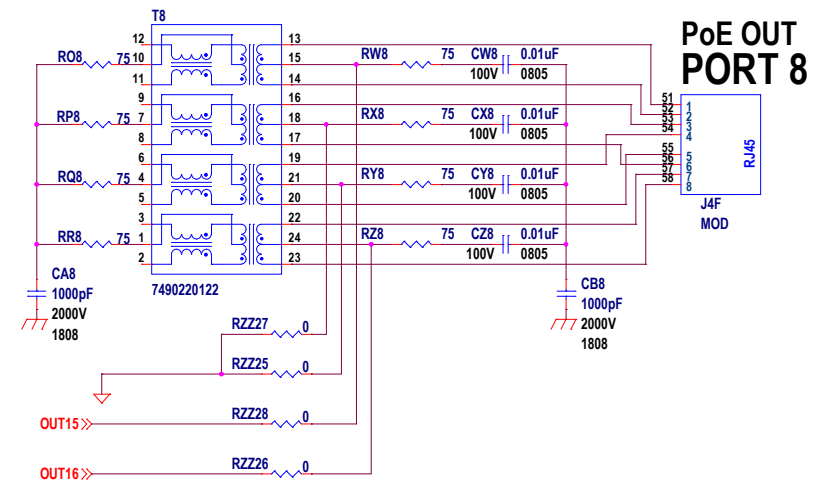
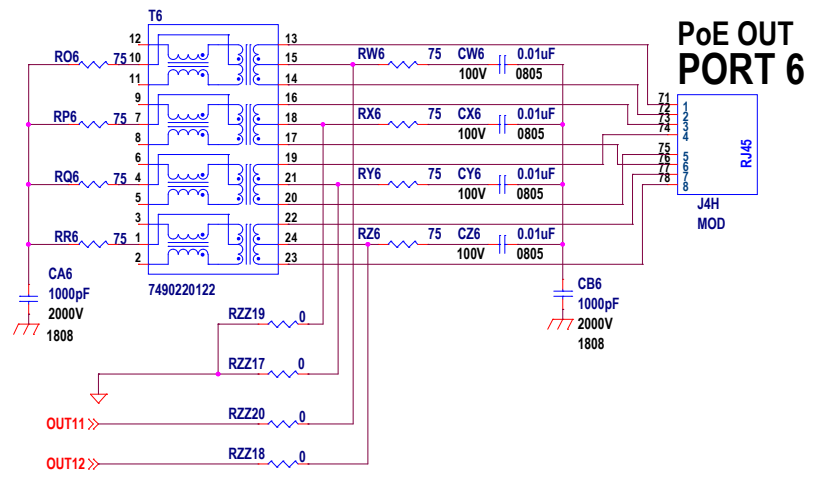
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CUSTOMER NOTICE		APPROVALS		ANALOG DEVICES		POWER BY LINEAR	
ANALOG DEVICES HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT ANALOG DEVICES APPLICATIONS ENGINEERING FOR ASSISTANCE.		PCB DES.	KEMM	www.analog.com		Phone: (408)432-1900	
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TITLE: SCHEMATIC				24-PORT IEEE802.3bt PSE MOTHER BOARD			
IC NO. LTC3839EMSE#TRPBF				SCHEMATIC NO. AND REVISION:			
SKU NO. DC3017A				710-DC3017A-A_REV03			
SIZE: N/A		DATE:		SHEET 4		OF 11	

REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	3			

PORTS 5 - 8

### UPPER ROW



### LOWER ROW

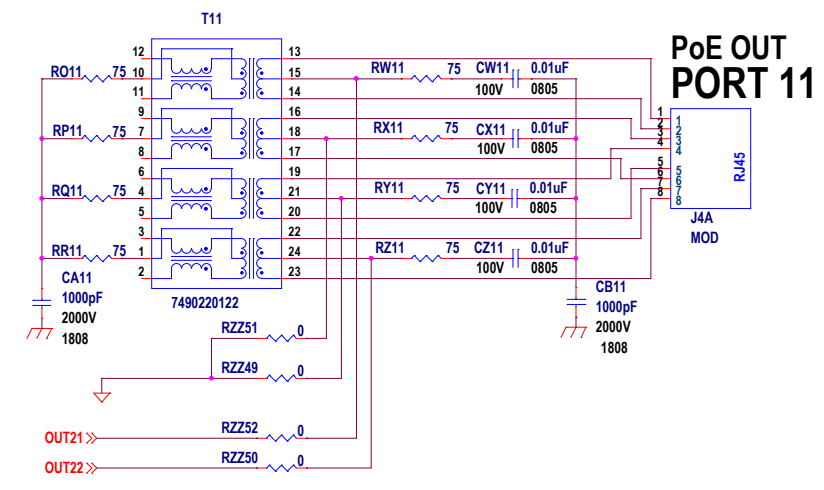
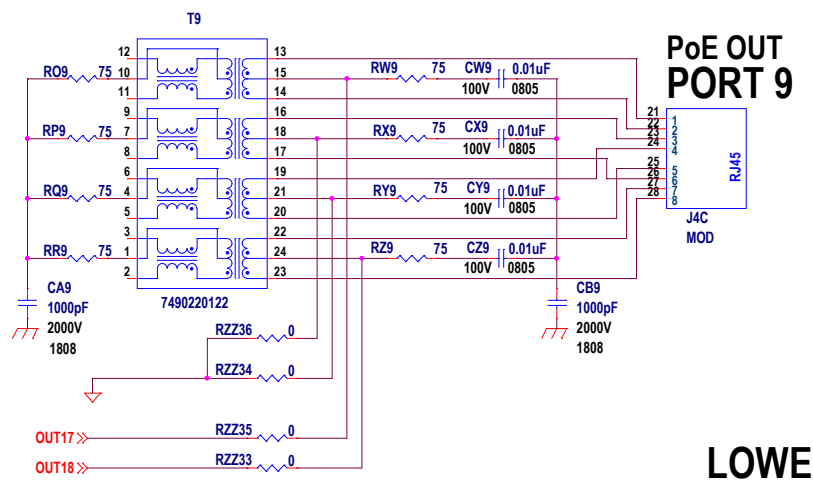
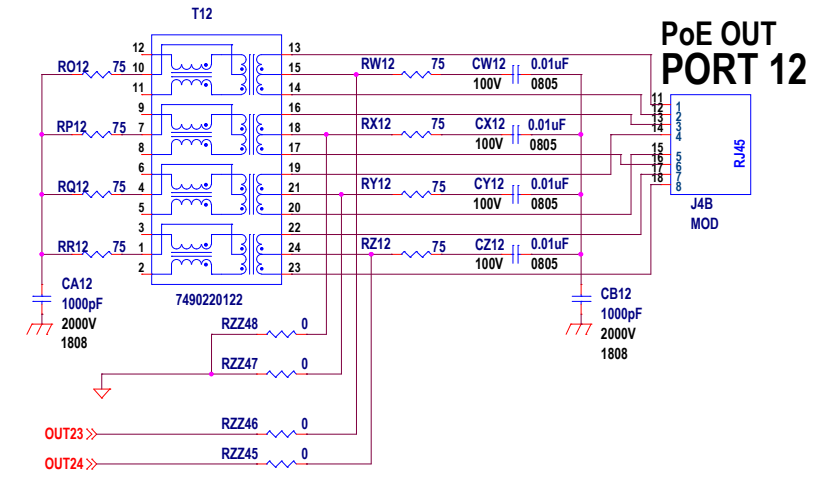
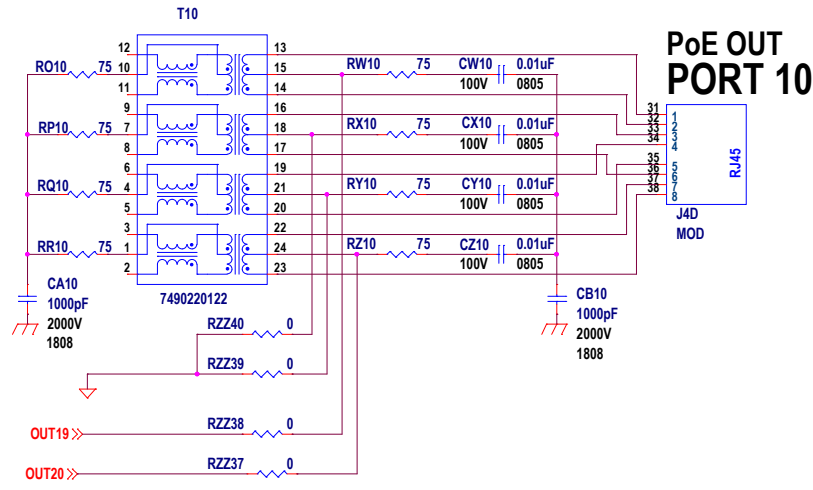
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	PCB DES.	KEM/M		
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			24-PORT IEEE802.3bt PSE MOTHER BOARD	
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			SKU NO. DC3017A	710-DC3017A-A_REV03
		SIZE: N/A	DATE:	SHEET 5 OF 11

UPPER ROW

REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	3			



LOWER ROW

**NOTE: UNLESS OTHERWISE SPECIFIED**

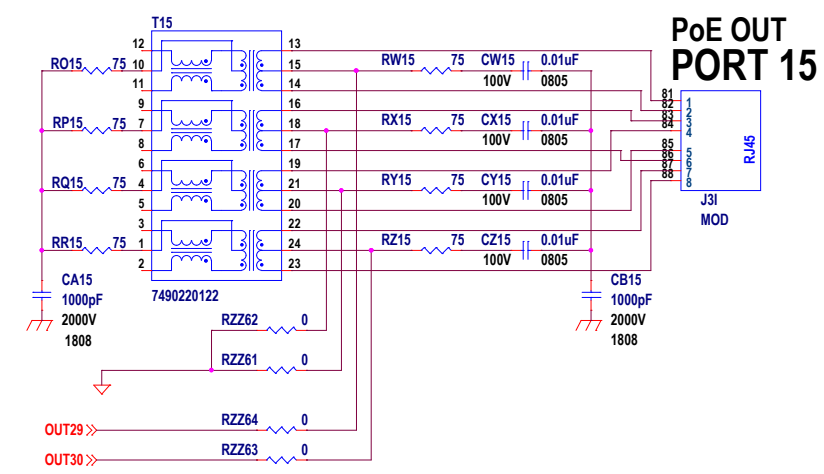
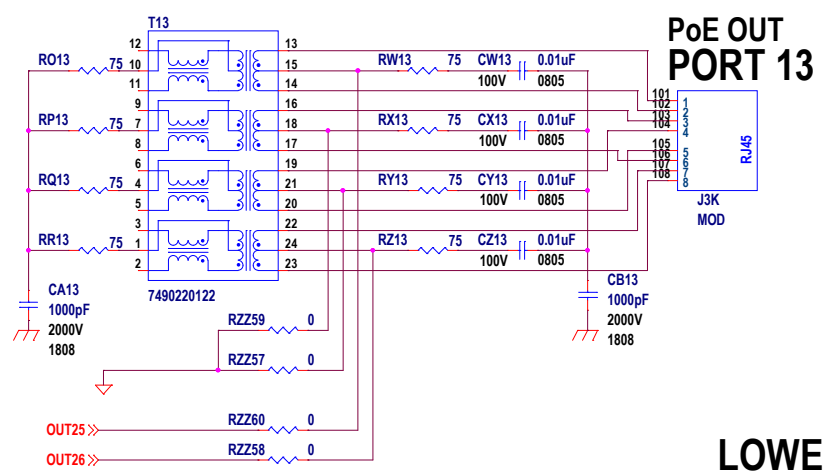
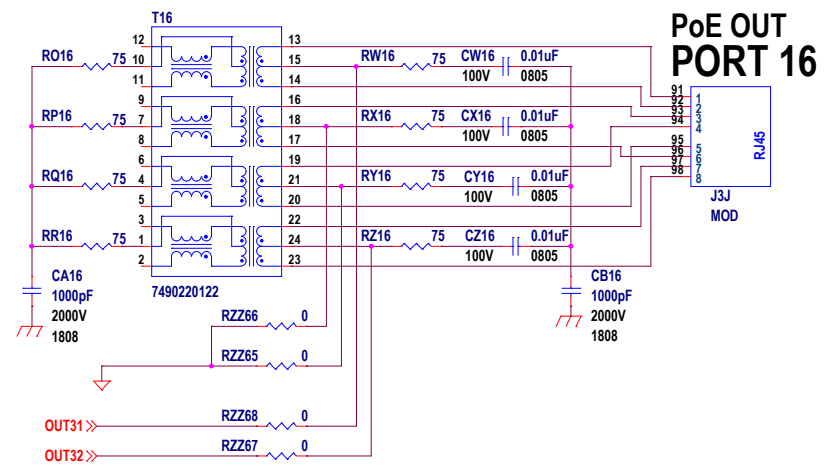
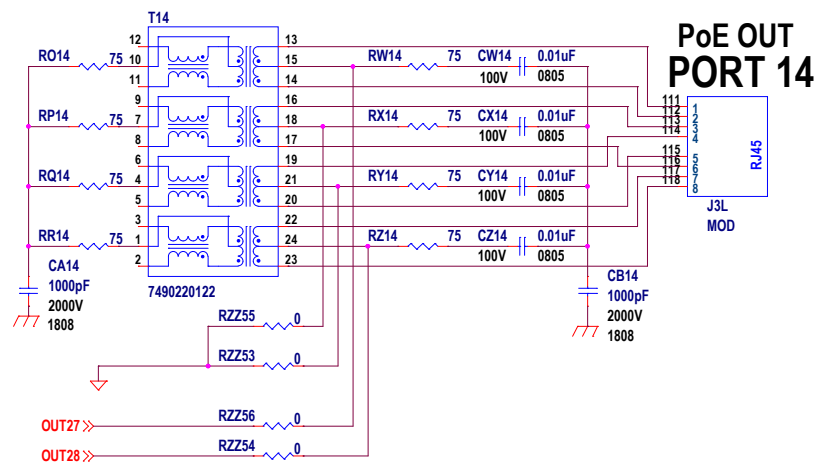
1. ALL RESISTORS AND CAPACITORS ARE 0603.
2. INSTALL SHUNTS AS SHOWN.

CUSTOMER NOTICE		APPROVALS		ANALOG DEVICES		POWER BY LINEAR	
ANALOG DEVICES HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT ANALOG DEVICES APPLICATIONS ENGINEERING FOR ASSISTANCE.		PCB DES.	KEM/M				
		APP ENG.	ERIC/H				
THIS CIRCUIT IS PROPRIETARY TO ANALOG DEVICES AND SUPPLIED FOR USE WITH ANALOG DEVICES PARTS.		TITLE: SCHEMATIC		www.analog.com Phone: (408)432-1900			
		24-PORT IEEE 802.3bt PSE MOTHER BOARD		IC NO. LTC3839EMSE#FTRPBF	SCHEMATIC NO. AND REVISION:		
		SKU NO. DC3017A		710-DC3017A-A_REV03			
		SIZE: N/A	DATE:			SHEET 6 OF 11	

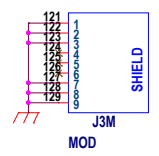
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	3			

PORTS 13 - 16

### UPPER ROW



### LOWER ROW

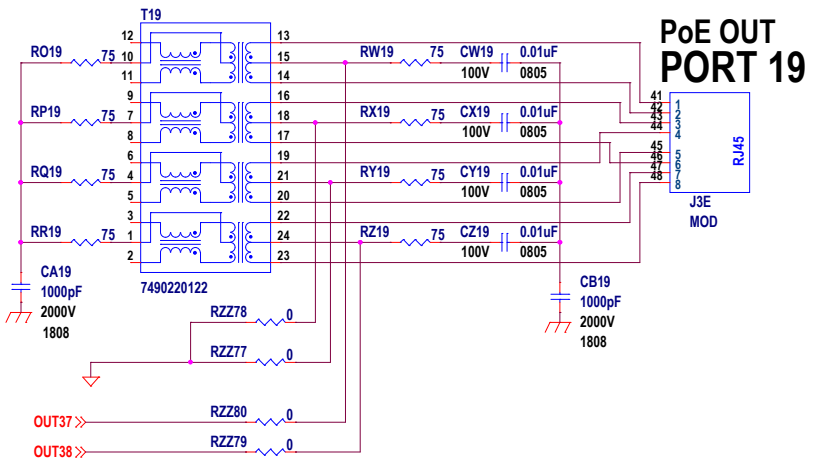
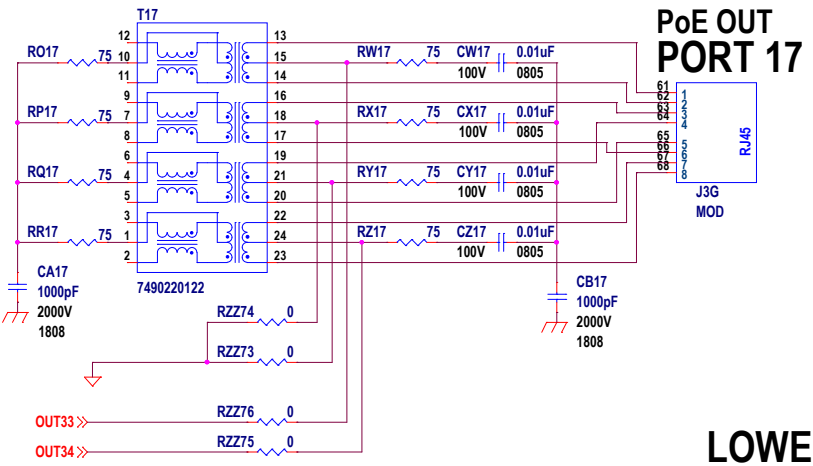
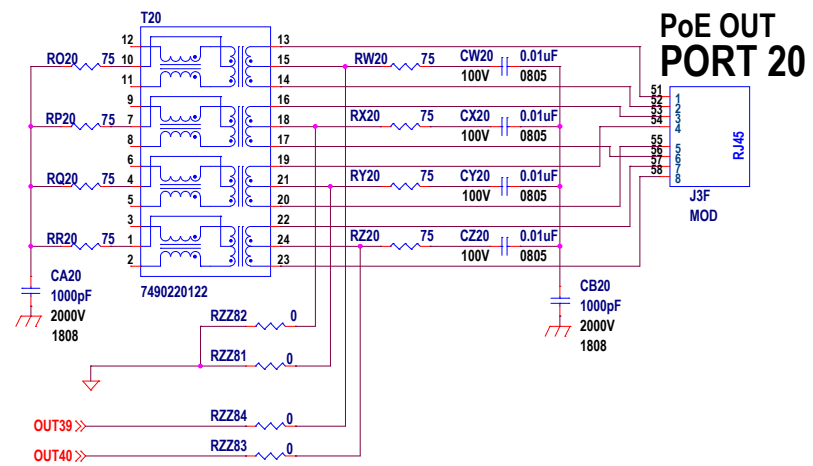
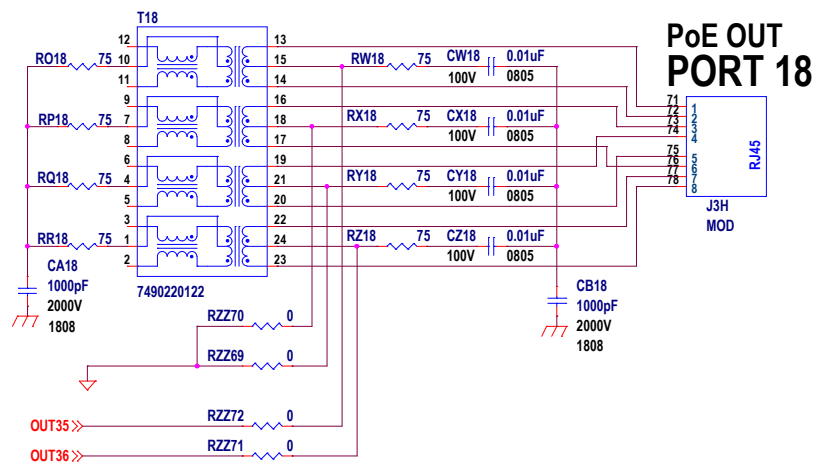


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THIS CIRCUIT IS PROPRIETARY TO ANALOG DEVICES AND SUPPLIED FOR USE WITH ANALOG DEVICES PARTS.		APP ENG.	ERIC/H	24-PORT IEEE802.3bt PSE MOTHER BOARD	
				IC NO. LTC3839EMSE#FTRPBF	SCHEMATIC NO. AND REVISION:
				SKU NO. DC3017A	710-DC3017A-A_REV03
				SIZE: N/A	DATE:
				SHEET 7 OF 11	

UPPER ROW

REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	3			



LOWER ROW

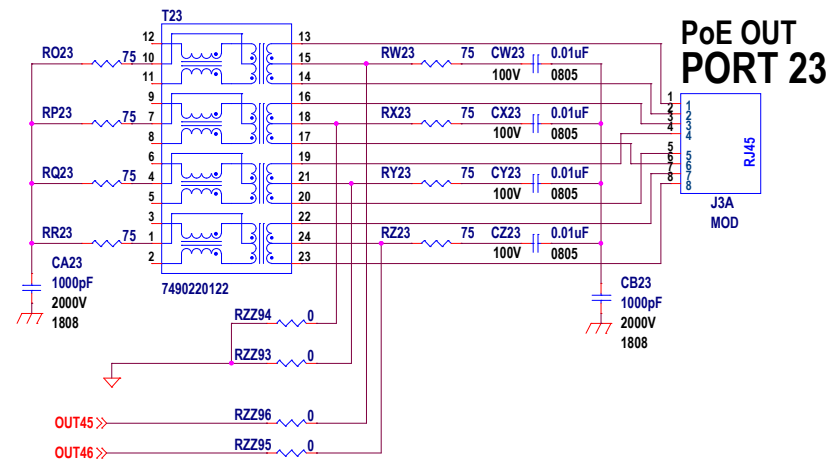
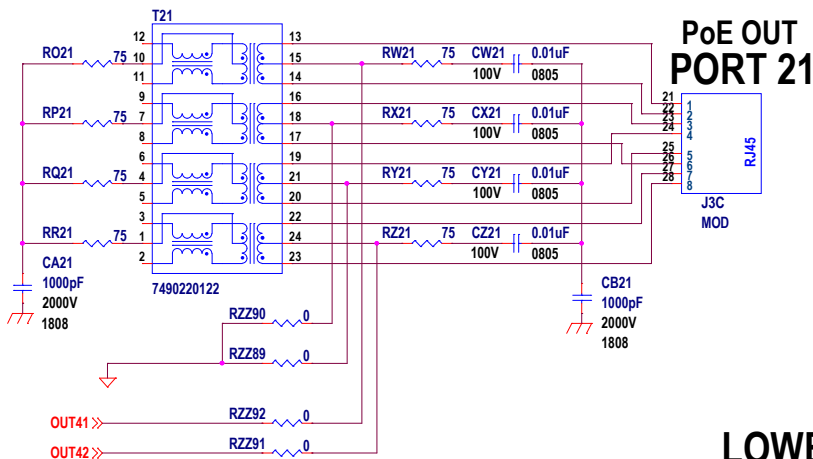
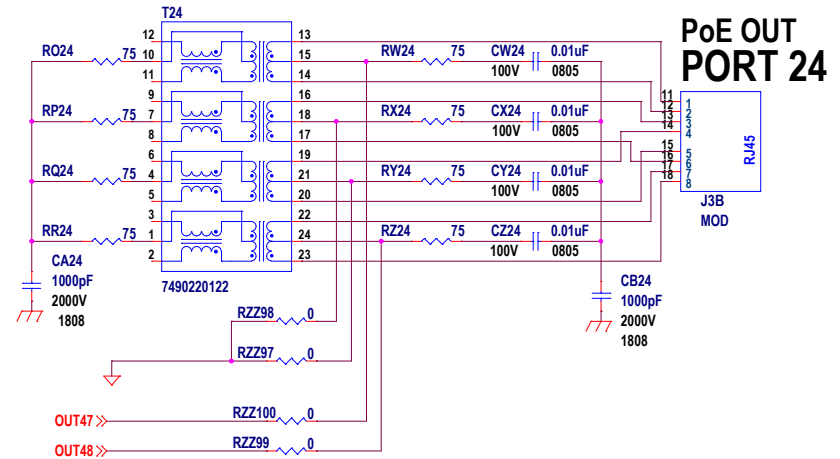
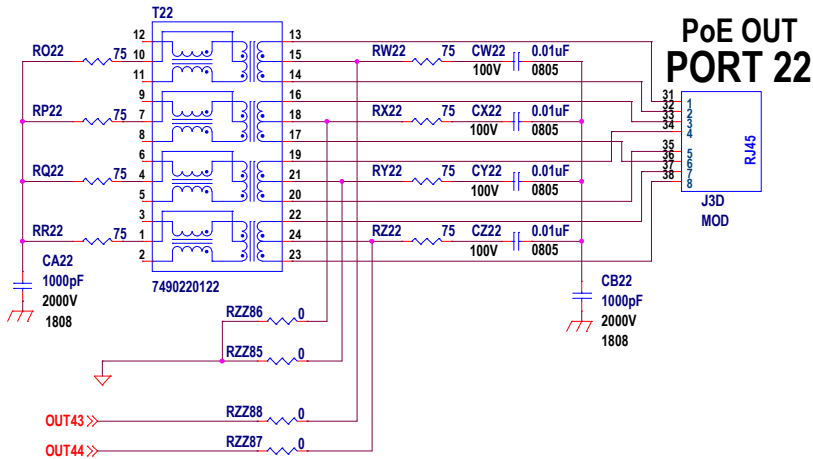
NOTE: UNLESS OTHERWISE SPECIFIED

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<p><b>CUSTOMER NOTICE</b></p> <p>ANALOG DEVICES HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT ANALOG DEVICES APPLICATIONS ENGINEERING FOR ASSISTANCE.</p> <p>THIS CIRCUIT IS PROPRIETARY TO ANALOG DEVICES AND SUPPLIED FOR USE WITH ANALOG DEVICES PARTS.</p>	<p><b>APPROVALS</b></p>		<p>www.analog.com Phone: (408)432-1900</p>
	PCB DES.	KEM/M	
	APP ENG.	ERIC/H	
	<p>TITLE: SCHEMATIC</p> <p>24-PORT IEEE 802.3bt PSE MOTHER BOARD</p>		
	IC NO. LTC3839EMSE#FTRPBF	SHEMATIC NO. AND REVISION:	
	SKU NO. DC3017A	710-DC3017A-A_REV03	
	SIZE: N/A	DATE:	SHEET 8 OF 11

UPPER ROW

REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	3			



LOWER ROW

NOTE: UNLESS OTHERWISE SPECIFIED

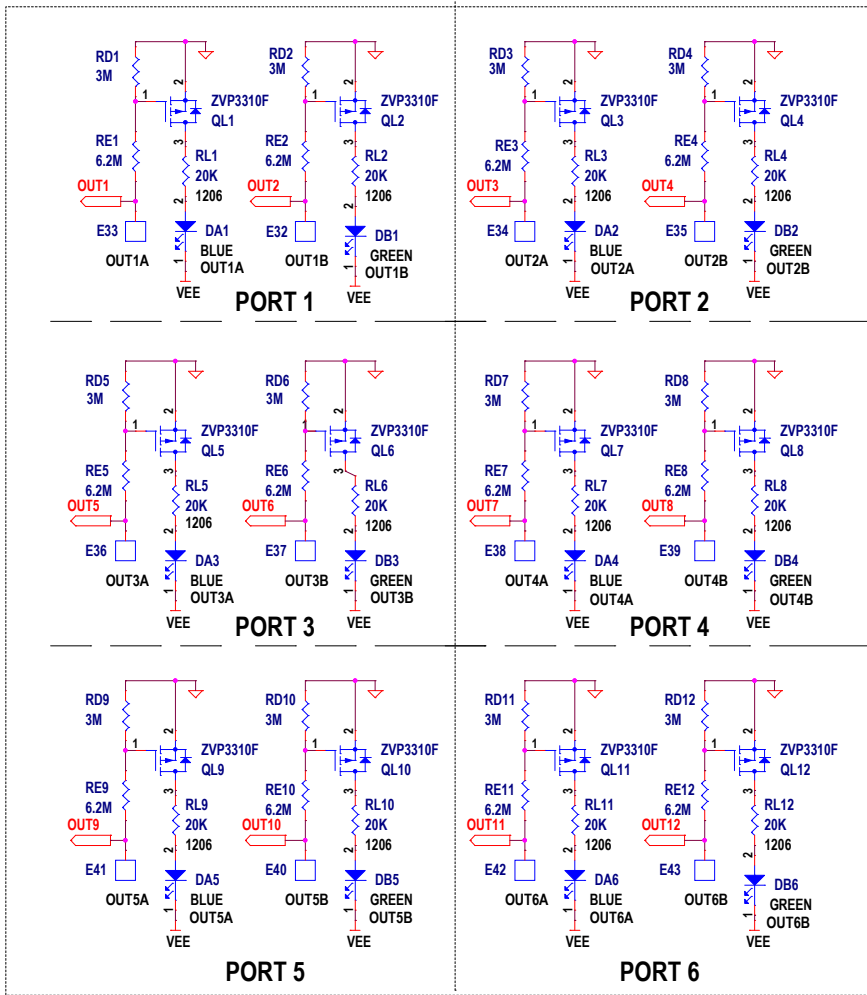
1. ALL RESISTORS AND CAPACITORS ARE 0603.
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CUSTOMER NOTICE		APPROVALS		ANALOG DEVICES www.analog.com	POWER BY LINEAR Phone: (408)432-1900
ANALOG DEVICES HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT ANALOG DEVICES APPLICATIONS ENGINEERING FOR ASSISTANCE.		PCB DES.	KEM/M		
THIS CIRCUIT IS PROPRIETARY TO ANALOG DEVICES AND SUPPLIED FOR USE WITH ANALOG DEVICES PARTS.		APP ENG.	ERIC.H	24-PORT IEEE802.3bt PSE MOTHER BOARD	
				IC NO. LTC3839EMSE#FTRPBF	SCHEMATIC NO. AND REVISION: 710-DC3017A-A_REV03
				SKU NO. DC3017A	
				SIZE: N/A	DATE:
				SHEET 9 OF 11	

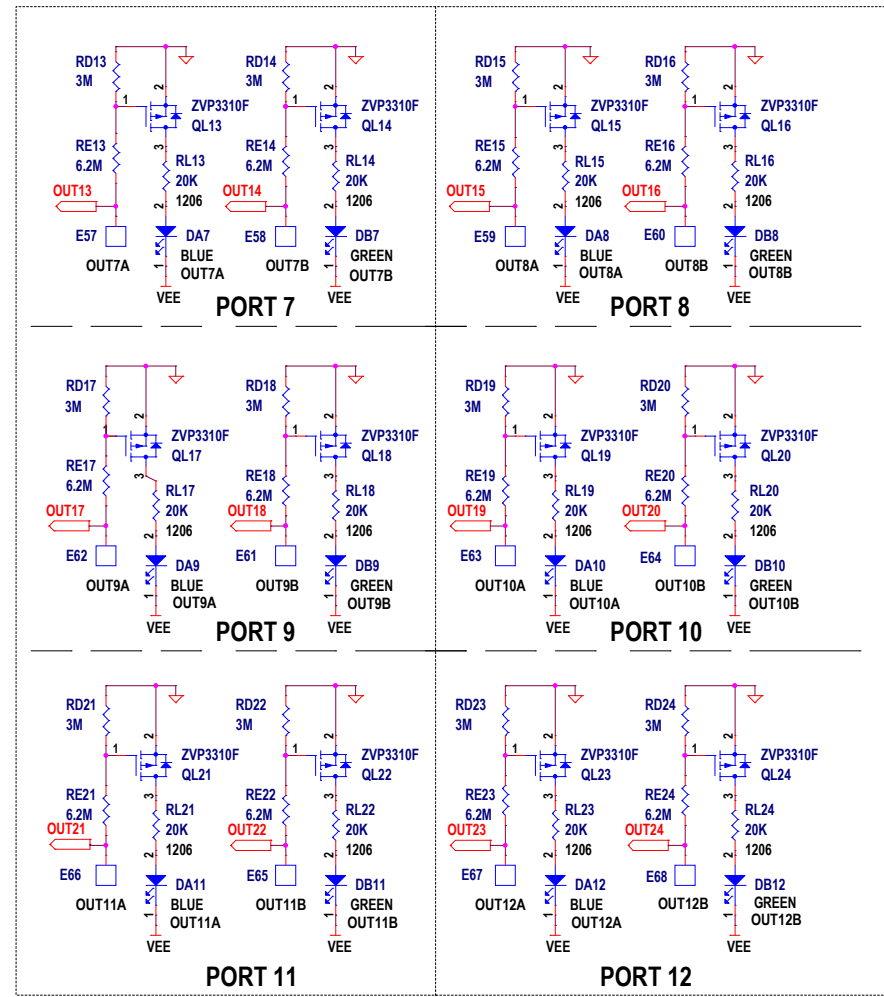


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	3			

OPTIONAL PORT LED DRIVERS - PORTS 1 TO 6



OPTIONAL PORT LED DRIVERS - PORTS 7 - 12



NOTE: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS AND CAPACITORS ARE 0603.
2. INSTALL SHUNTS AS SHOWN.

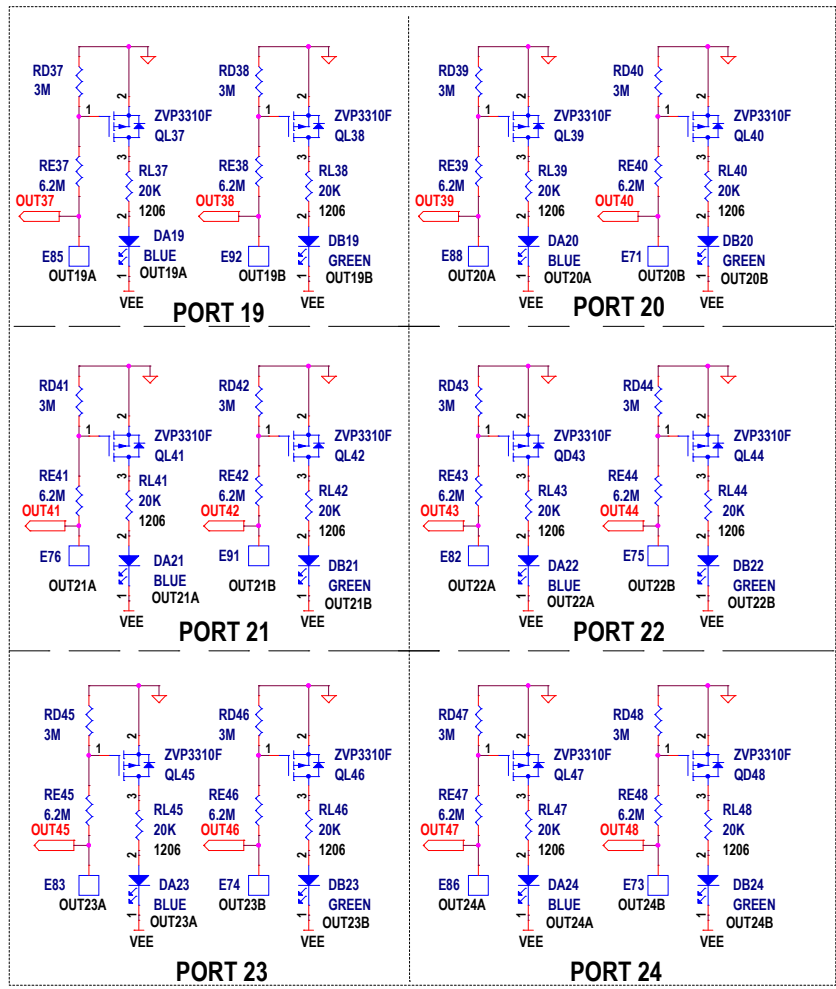
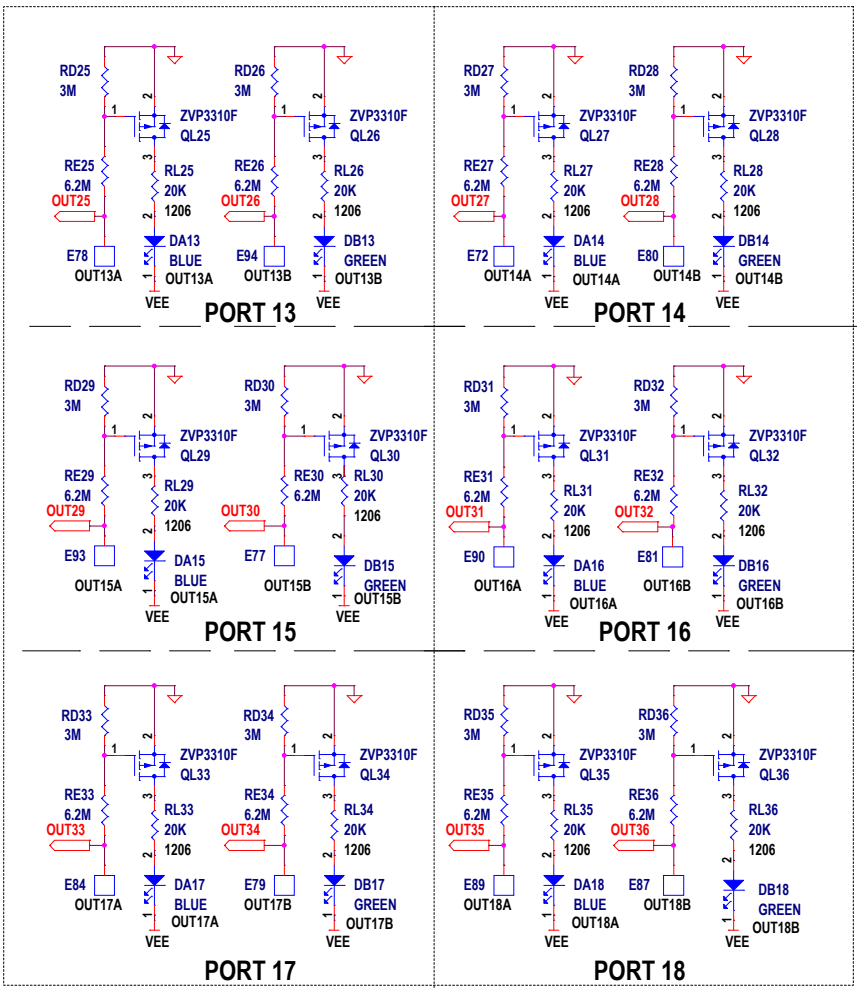
CUSTOMER NOTICE		APPROVALS		ANALOG DEVICES		POWER BY LINEAR			
<p>ANALOG DEVICES HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT ANALOG DEVICES APPLICATIONS ENGINEERING FOR ASSISTANCE.</p>		PCB DES.	KEMM	 www.analog.com Phone: (408)432-1900		TITLE: SCHEMATIC 24-PORT IEEE 802.3bt PSE MOTHER BOARD			
		APP ENG.	ERIC H						
		IC NO. LTC3839EMSE#FTRPBF SKU NO. DC3017A						SCHEMATIC NO. AND REVISION: 710-DC3017A-A_REV03	
		SIZE: N/A DATE:						SHEET 10 OF 11	
<p>THIS CIRCUIT IS PROPRIETARY TO ANALOG DEVICES AND SUPPLIED FOR USE WITH ANALOG DEVICES PARTS.</p>									

REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	3			

PORTS 13-24 LEDs

OPTIONAL PORT LED DRIVERS - PORTS 13 -18

OPTIONAL PORT LED DRIVERS - PORTS 19 -24



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**CUSTOMER NOTICE**

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THIS CIRCUIT IS PROPRIETARY TO ANALOG DEVICES AND SUPPLIED FOR USE WITH ANALOG DEVICES PARTS.

APPROVALS	
PCB DES.	KEMM
APP ENG.	ERIC.H

**ANALOG DEVICES** **POWER BY LINEAR**

www.analog.com Phone: (408)432-1900

TITLE: SCHEMATIC

24-PORT IEEE 802.3bt PSE MOTHER BOARD

IC NO. LTC3839EMSE#TRPBF	SCHEMATIC NO. AND REVISION:
SKU NO. DC3017A	710-DC3017A-A_REV03
SIZE: N/A	DATE:

SHEET 11 OF 11



# HARDWARE

## PCA ADDITIONAL PARTS

MP1	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
MP2	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
MP3	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
MP4	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
MP5	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
MP6	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
MP7	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
MP8	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
MP9	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
LB1	BOARD S/N LABEL 895-0154
PCB1	PCB, DC3017A-A REV03 (MAO BANG)
PK1	CONN.,POLARIZING KEY,PLUG,2.54mm PITCH,3.18mm DIM,ST,NATURAL THERMOPLASTIC
PK2	CONN.,POLARIZING KEY,PLUG,2.54mm PITCH,3.18mm DIM,ST,NATURAL THERMOPLASTIC
PK3	CONN.,POLARIZING KEY,PLUG,2.54mm PITCH,3.18mm DIM,ST,NATURAL THERMOPLASTIC

## NOTE: UNLESS OTHERWISE SPECIFIED

1. INSTALL SHUNTS AS SHOWN.

<b>CUSTOMER NOTICE</b> <small>ANALOG DEVICES HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT ANALOG DEVICES APPLICATIONS ENGINEERING FOR ASSISTANCE.</small>	<b>APPROVALS</b>		  <a href="http://www.analog.com">www.analog.com</a> Phone: (408)432-1900
	PCB DES.	KEN.M	
<small>THIS CIRCUIT IS PROPRIETARY TO ANALOG DEVICES AND SUPPLIED FOR USE WITH ANALOG DEVICES PARTS.</small>	APP ENG.	ERIC.H	<b>TITLE: SCHEMATIC</b> <b>24-PORT IEEE 802.3bt PSE MOTHER BOARD</b>
			<b>IC NO. LTC3639EMSE#TRPBF</b>
			<b>SKU NO. DC3017A</b>
			<b>SCHEMATIC NO. AND REVISION:</b> <b>710-DC3017A-A_REV03</b>
	SIZE: N/A	DATE:	SHEET 12 OF 0