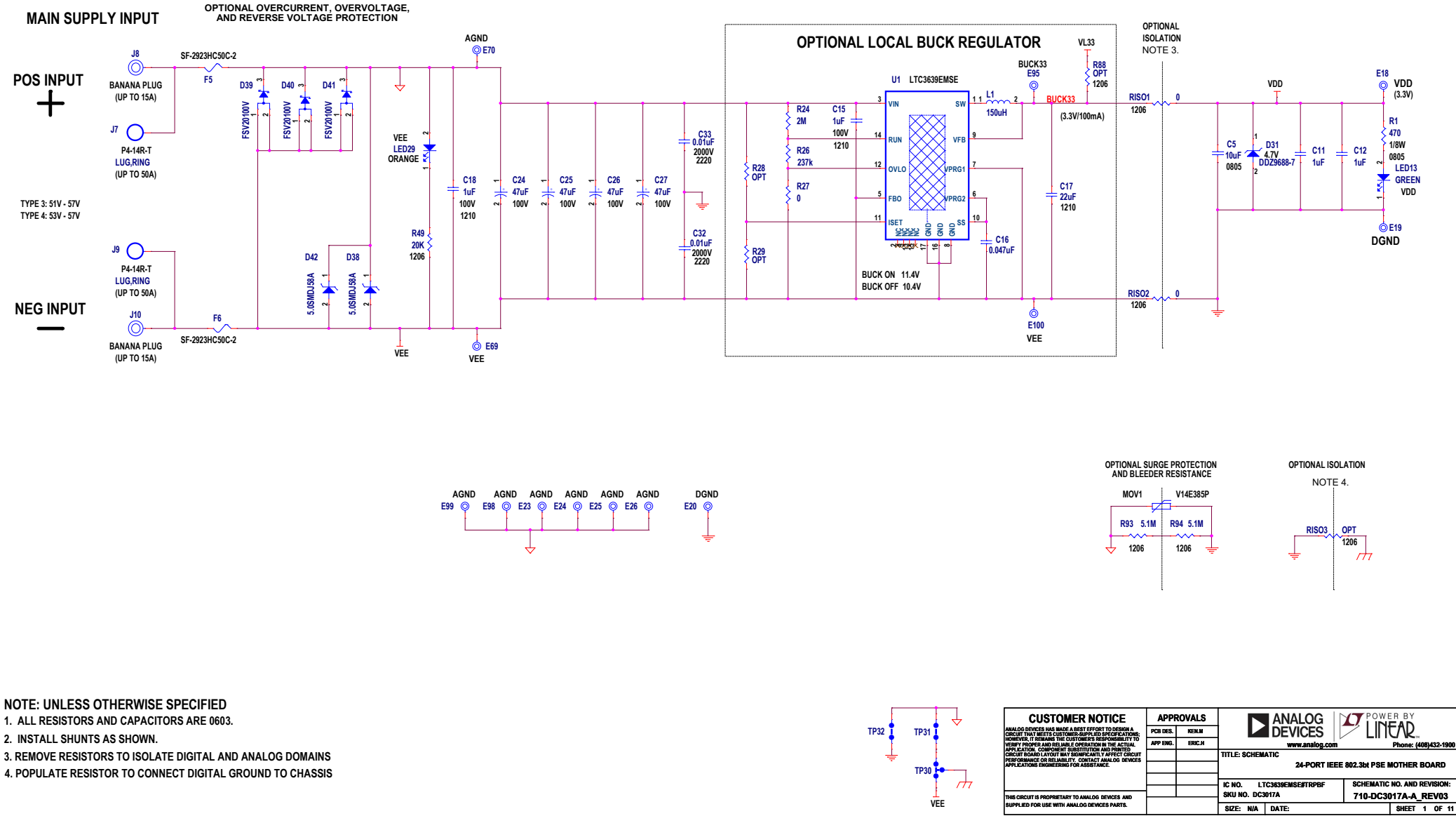


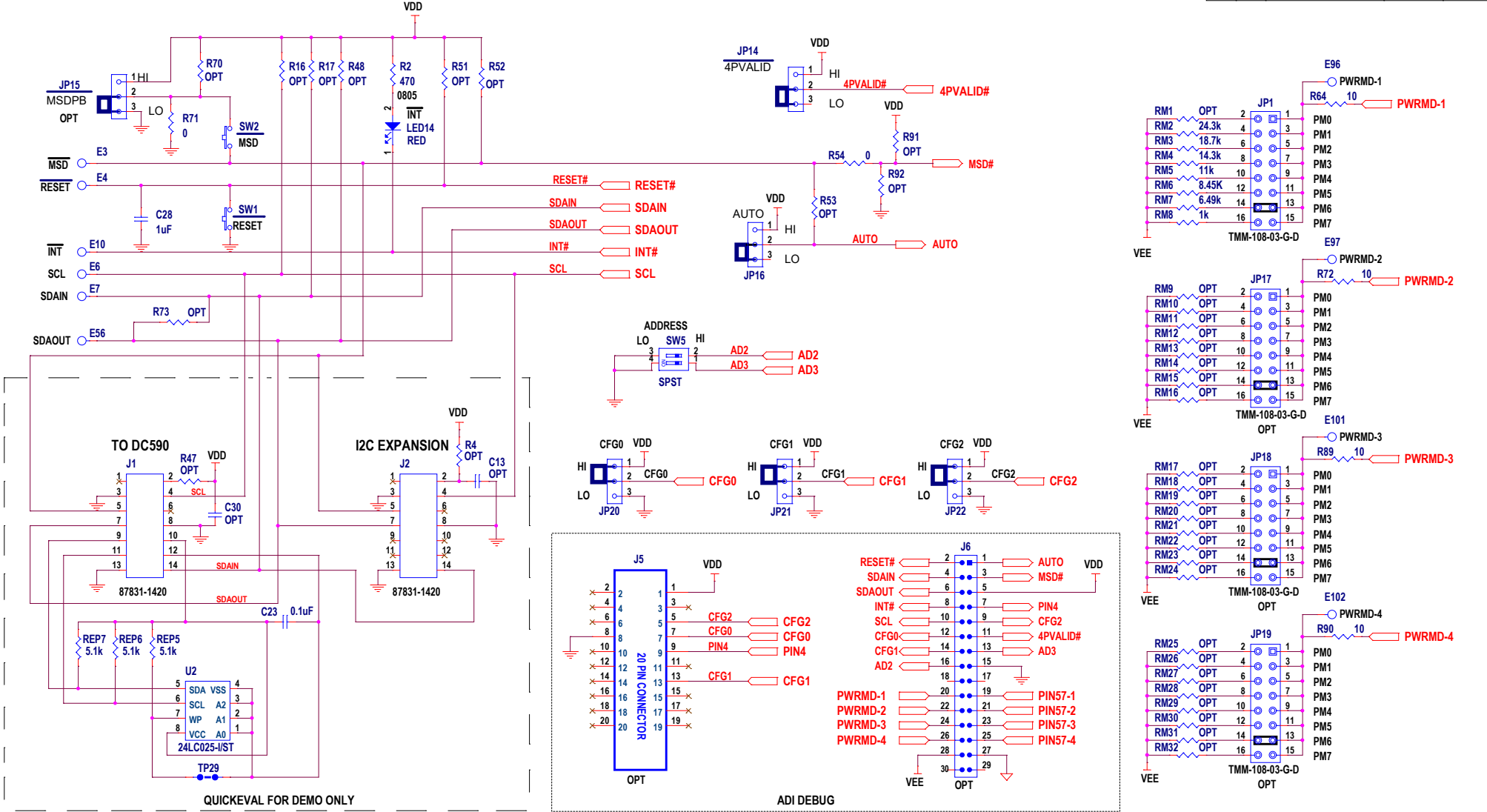
POWER SUPPLY

REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	3			





CONFIGURATIONS

REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	3			

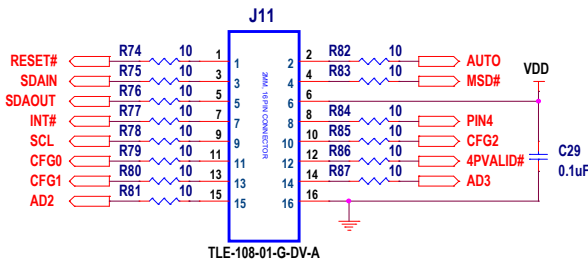


NOTE: UNLESS OTHERWISE SPECIFIED
1. ALL RESISTORS AND CAPACITORS ARE 0603.
2. INSTALL SHUNTS AS SHOWN.

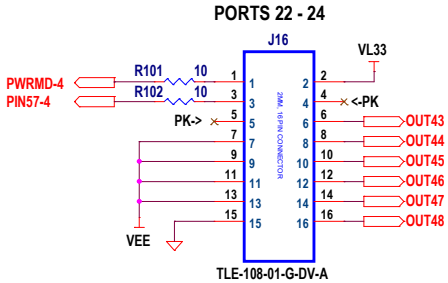
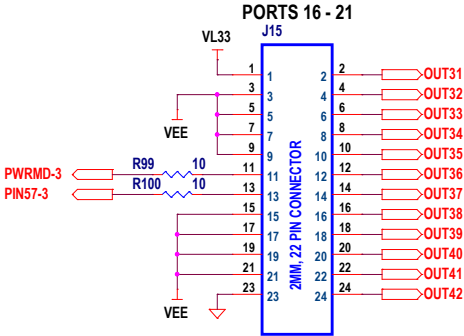
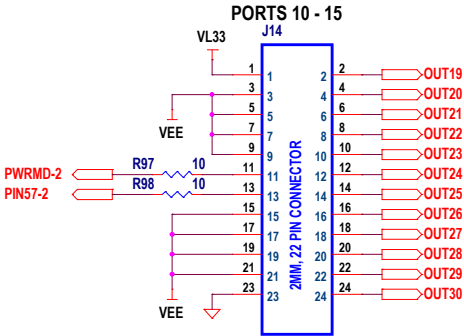
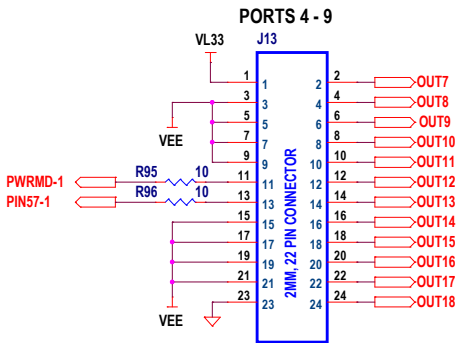
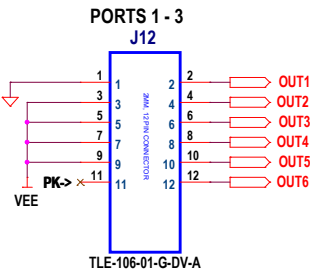
CUSTOMER NOTICE		APPROVALS		 ANALOG DEVICES		 POWER BY LINEAR	
ANALOG DEVICES HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT ANALOG DEVICES APPLICATIONS ENGINEERING FOR ASSISTANCE.		PCB DES.	KENM	www.analog.com		Phone: (408)432-1900	
		APP ENG.	ERIC.H				
THIS CIRCUIT IS PROPRIETARY TO ANALOG DEVICES AND SUPPLIED FOR USE WITH ANALOG DEVICES PARTS.		TITLE: SCHEMATIC				24-PORT IEEE 802.3bt PSE MOTHER BOARD	
		IC NO. LTC3639EMSE#TRPBF				SCHEMATIC NO. AND REVISION:	
		SKU NO. DC3017A				710-DC3017A-A_REV03	
		SIZE: N/A		DATE:		SHEET 2 OF 11	

REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	3			



DIGITAL CONNECTIONS



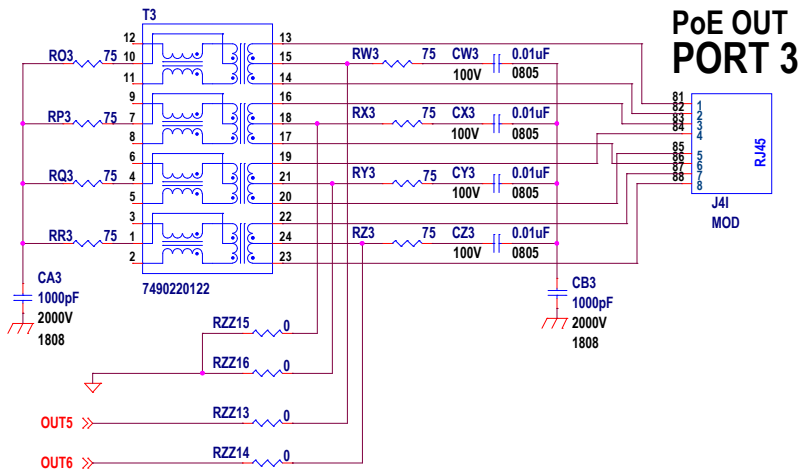
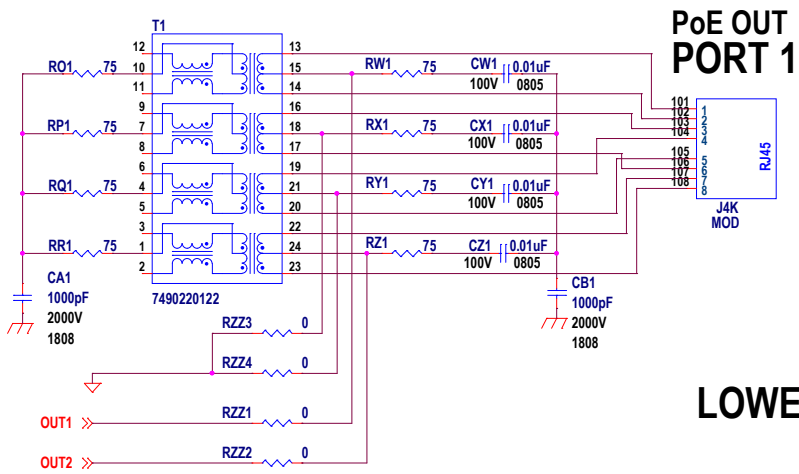
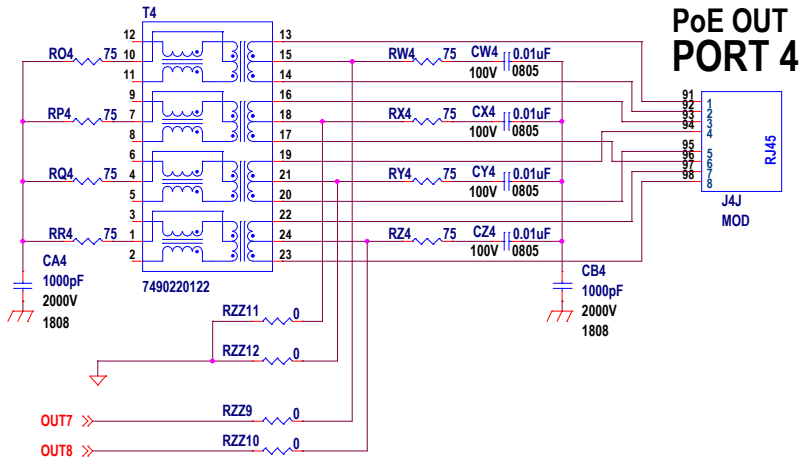
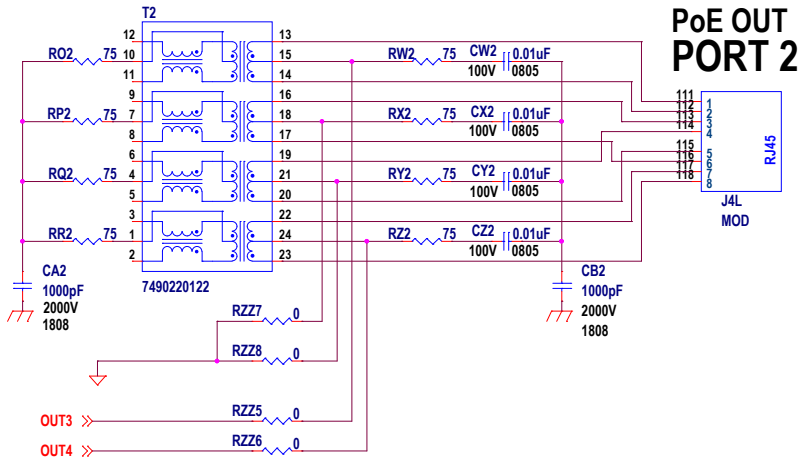
ANALOG CONNECTIONS



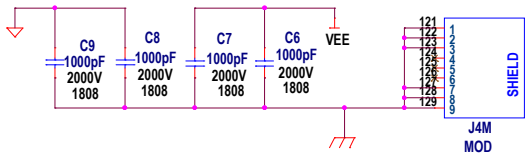
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		APP ENG.	ERIC.H				
				TITLE: SCHEMATIC			
				24-PORT IEEE 802.3bt PSE MOTHER BOARD			
				IC NO. LTC3839EMSE#TRPBF		SCHEMATIC NO. AND REVISION:	
				SKU NO. DC3017A		710-DC3017A-A_REV03	
THIS CIRCUIT IS PROPRIETARY TO ANALOG DEVICES AND SUPPLIED FOR USE WITH ANALOG DEVICES PARTS.				SIZE: N/A		DATE:	
						SHEET 3 OF 11	



REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	3			



LOWER ROW

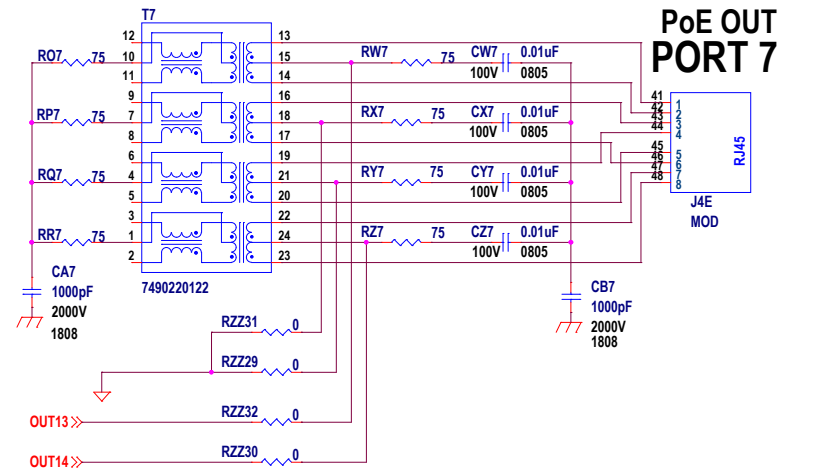
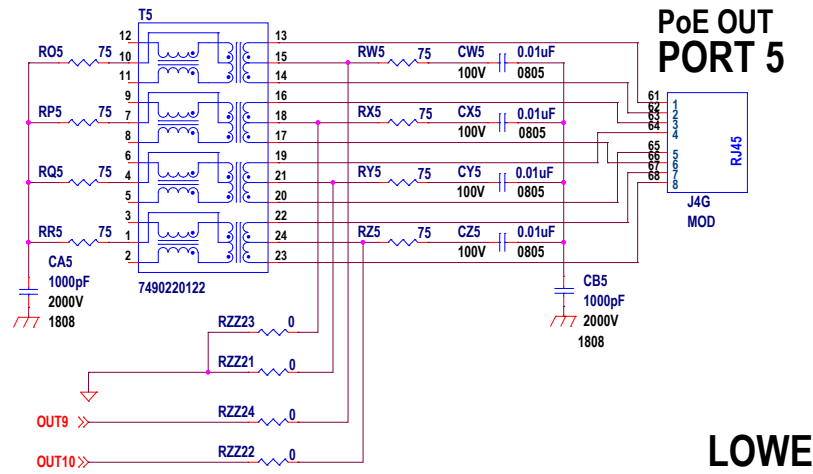
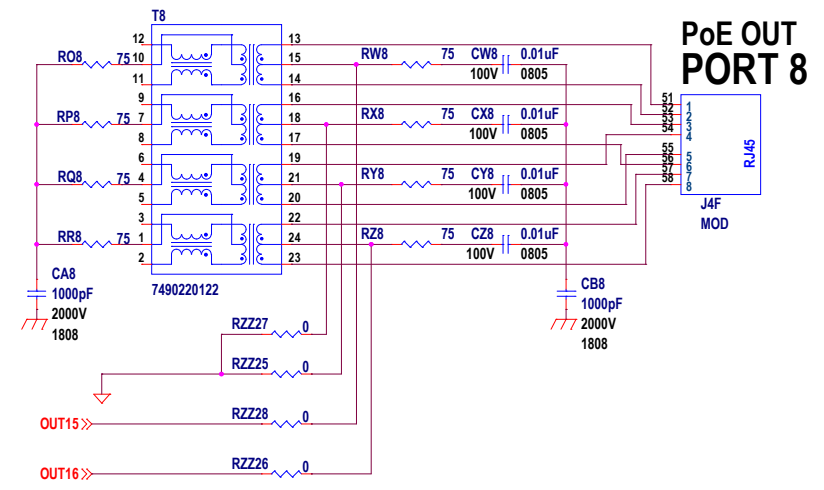
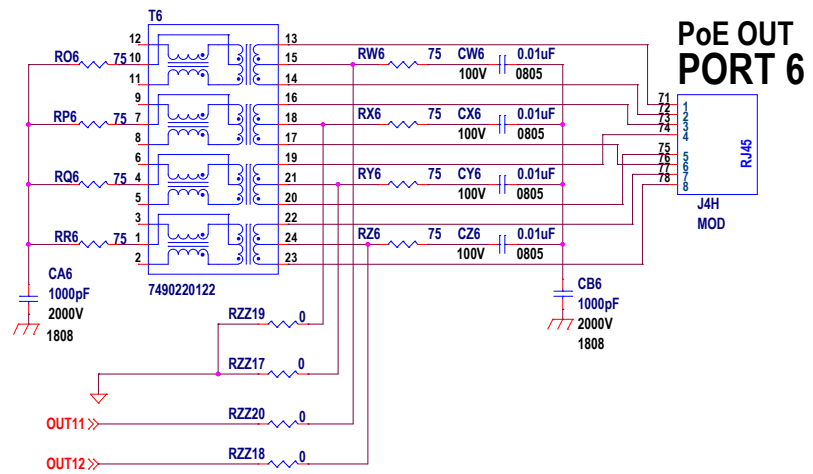


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		APP ENG.	ERIC.H						
						TITLE: SCHEMATIC			
						24-PORT IEEE 802.3bt PSE MOTHER BOARD			
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				SKU NO. DC3017A		710-DC3017A-A_REV03			
				SIZE: N/A DATE:		SHEET 4 OF 11			

UPPER ROW



REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	3			



LOWER ROW

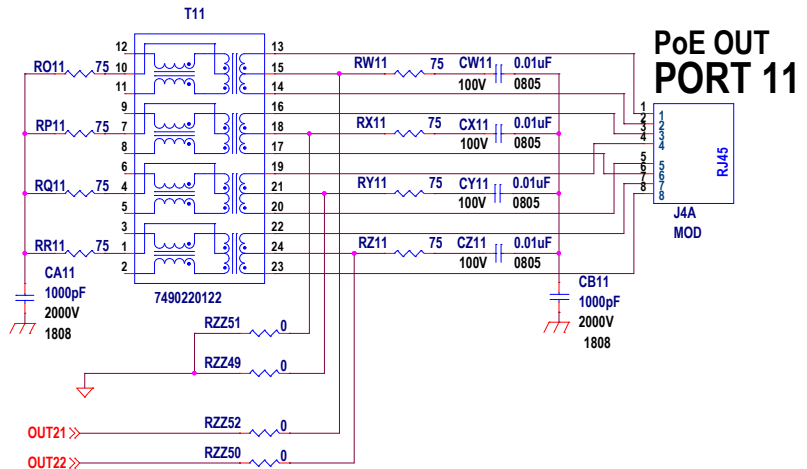
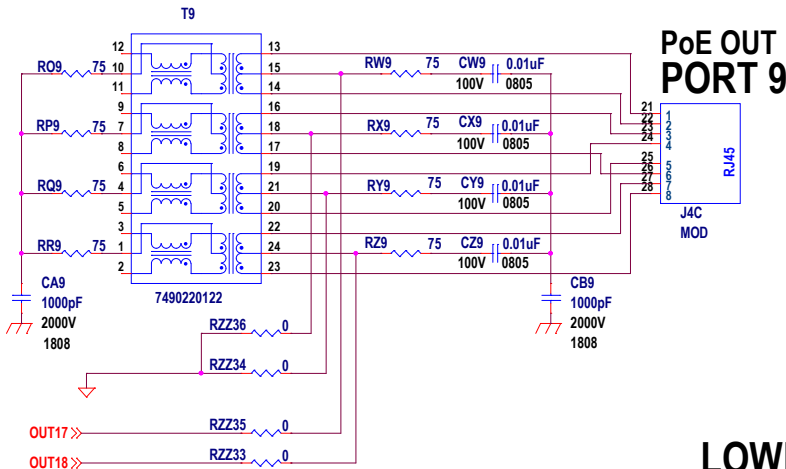
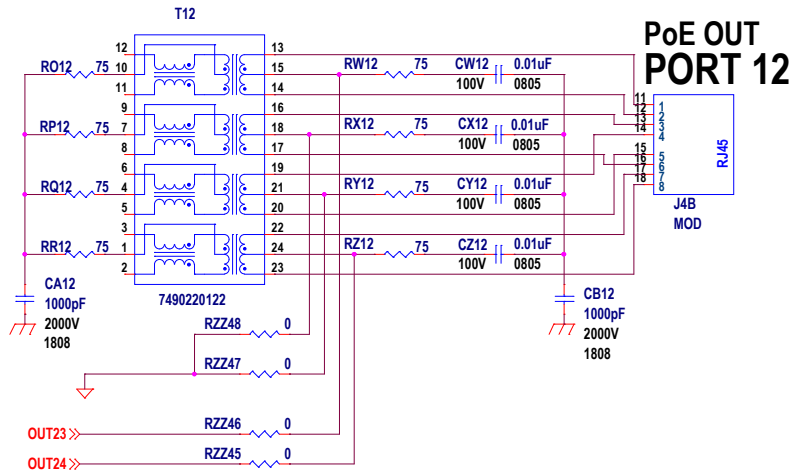
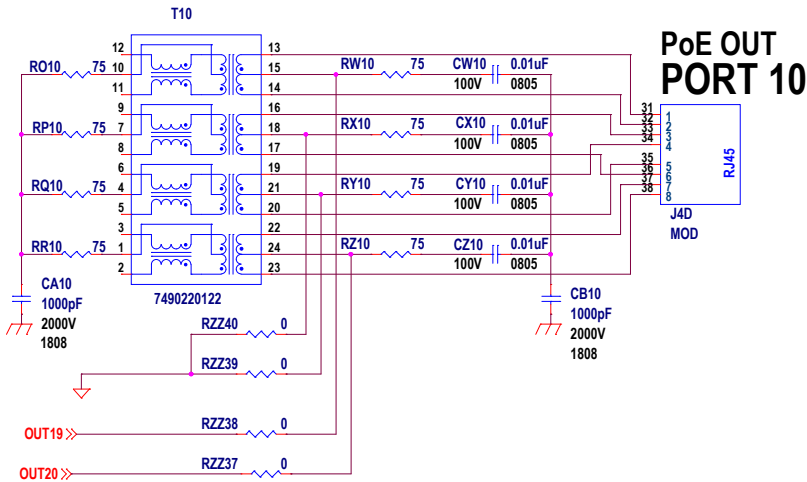
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CUSTOMER NOTICE ANALOG DEVICES HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT ANALOG DEVICES APPLICATIONS ENGINEERING FOR ASSISTANCE. THIS CIRCUIT IS PROPRIETARY TO ANALOG DEVICES AND SUPPLIED FOR USE WITH ANALOG DEVICES PARTS.	APPROVALS		 www.analog.com  Phone: (408)432-1900
	PCB DES.	KENLM	
	APP ENG.	ERIC.H	
TITLE: SCHEMATIC			24-PORT IEEE 802.3bt PSE MOTHER BOARD
IC NO. LTC3839EMSE#TRPBF		SCHEMATIC NO. AND REVISION:	
SKU NO. DC3017A		710-DC3017A-A_REV03	
SIZE: N/A		DATE:	SHEET 5 OF 11

UPPER ROW



REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	3			



LOWER ROW

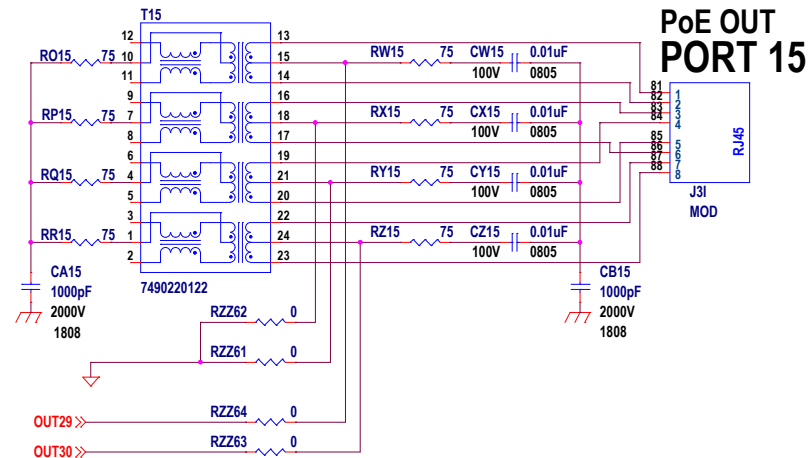
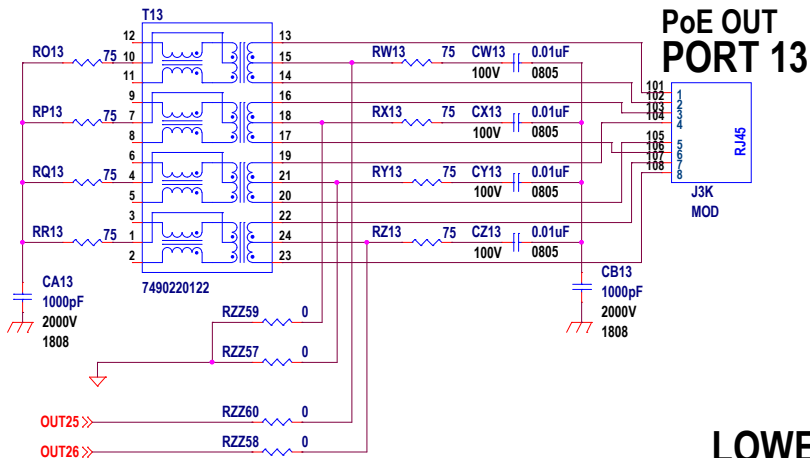
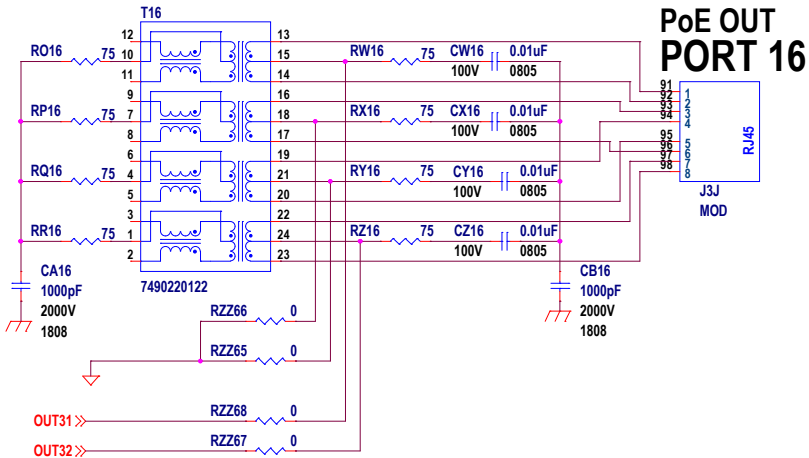
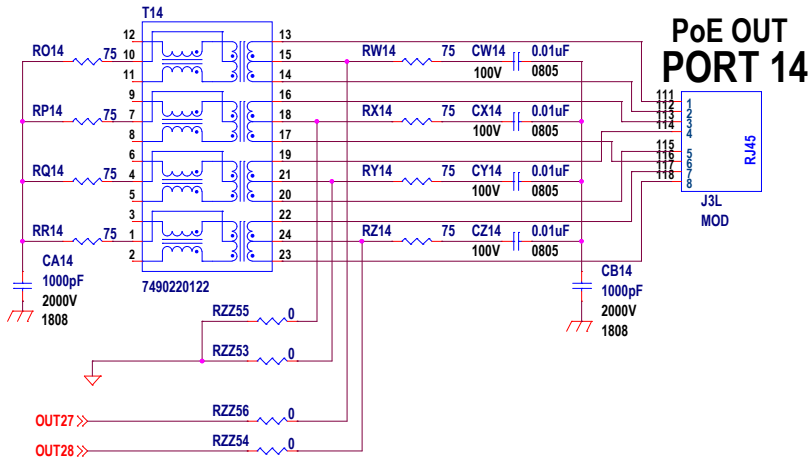
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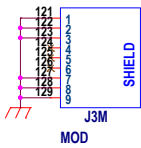
CUSTOMER NOTICE		APPROVALS		 ANALOG DEVICES		 POWER BY LINEAR™			
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		APP ENG.	ERIC.H						
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				24-PORT IEEE 802.3bt PSE MOTHER BOARD					
				IC NO. LTC3839EMSE#TRPBF		SCHEMATIC NO. AND REVISION:			
				SKU NO. DC3017A		710-DC3017A-A_REV03			
				SIZE: N/A		DATE:		SHEET 6 OF 11	

UPPER ROW

REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	3			





LOWER ROW



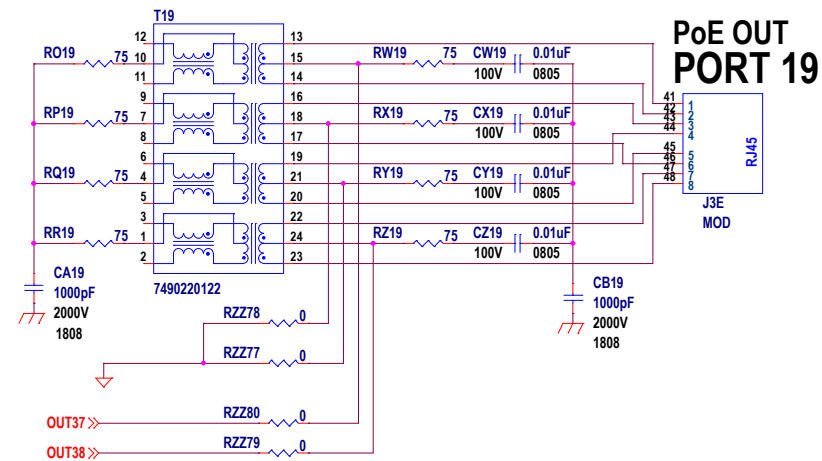
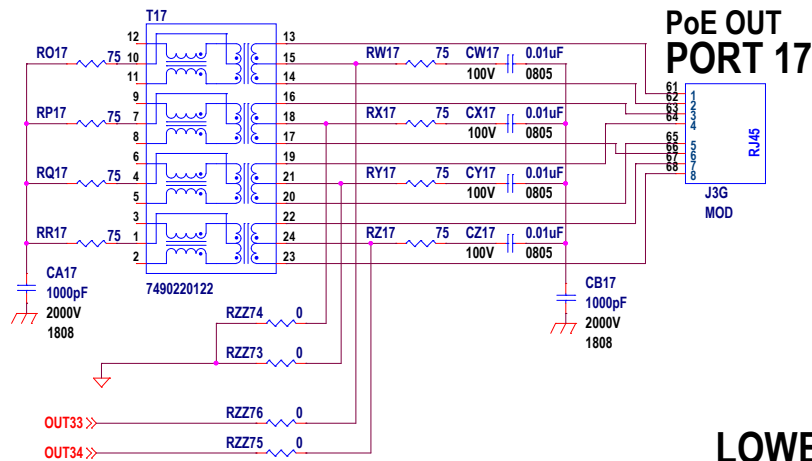
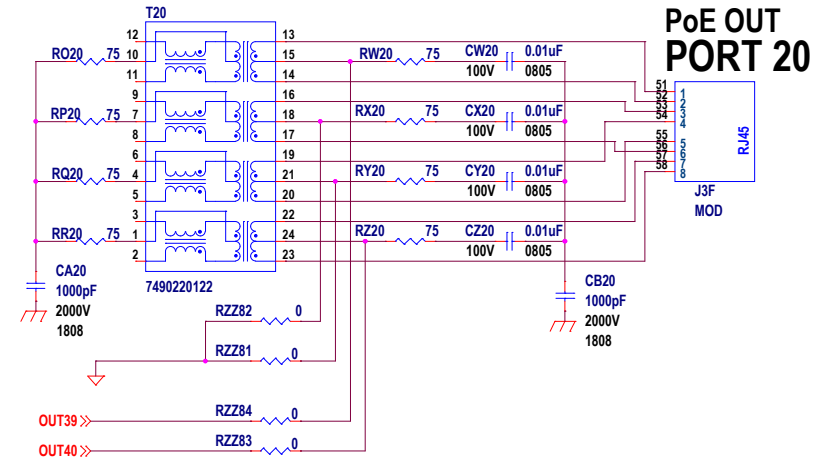
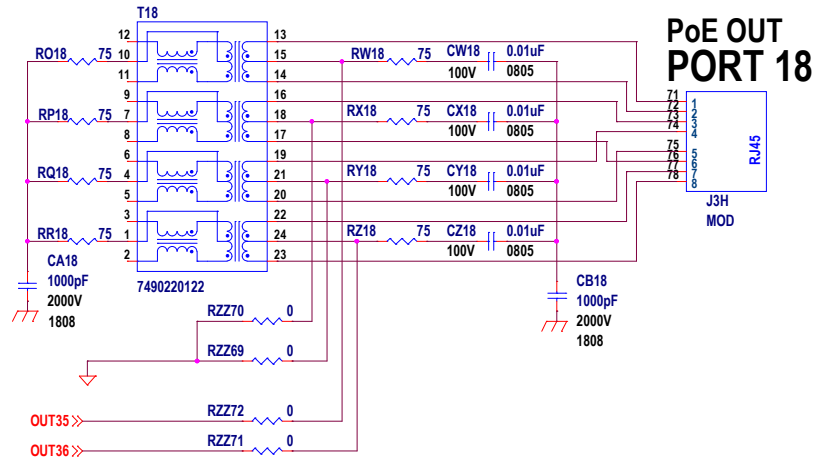
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		APP ENG.	ERIC.H				
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				IC NO. LTC3839EMSE#TRPBF		SCHEMATIC NO. AND REVISION:	
				SKU NO. DC3017A		710-DC3017A-A_REV03	
				SIZE: N/A		DATE:	
						SHEET 7 OF 11	

UPPER ROW



REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	3			



LOWER ROW

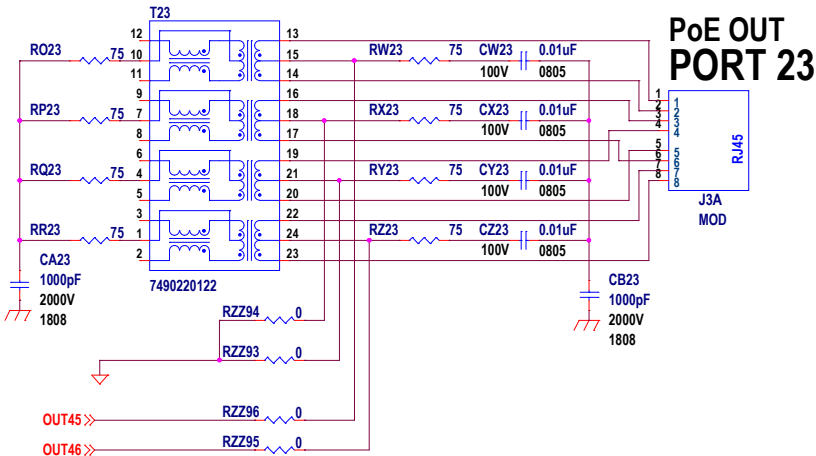
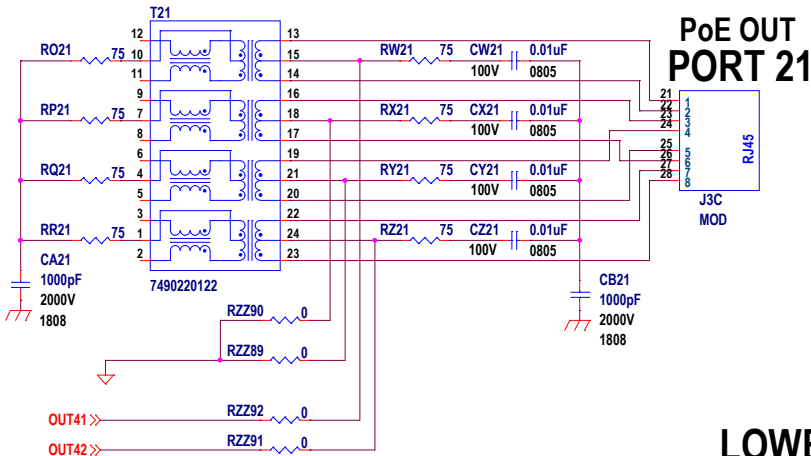
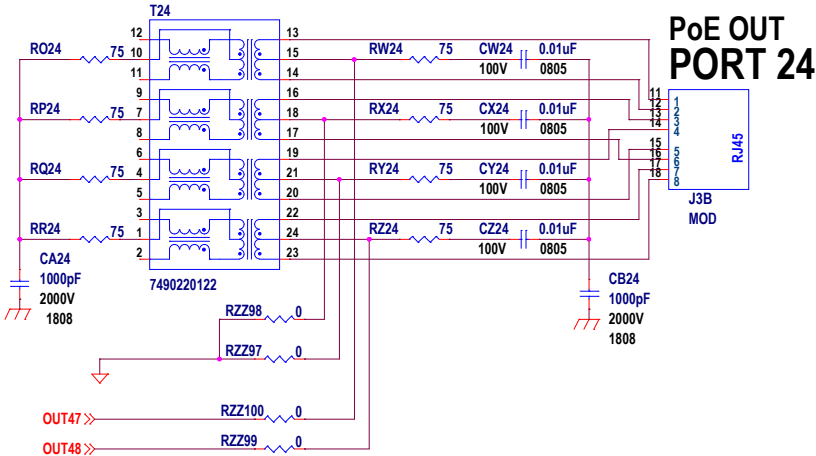
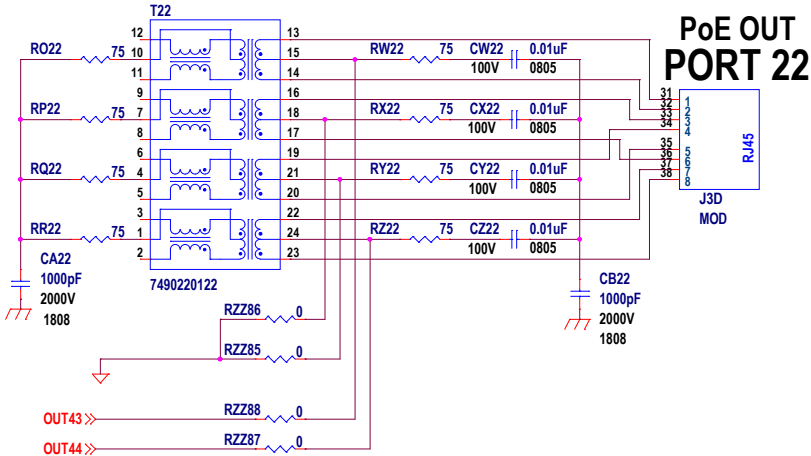
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		APP ENG.	ERIC.H				
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		IC NO. LTC3839EMSE#TRPBF		SCHEMATIC NO. AND REVISION:			
		SKU NO. DC3017A		710-DC3017A-A_REV03			
SIZE: N/A		DATE:		SHEET 8 OF 11			

UPPER ROW



REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	3			



LOWER ROW

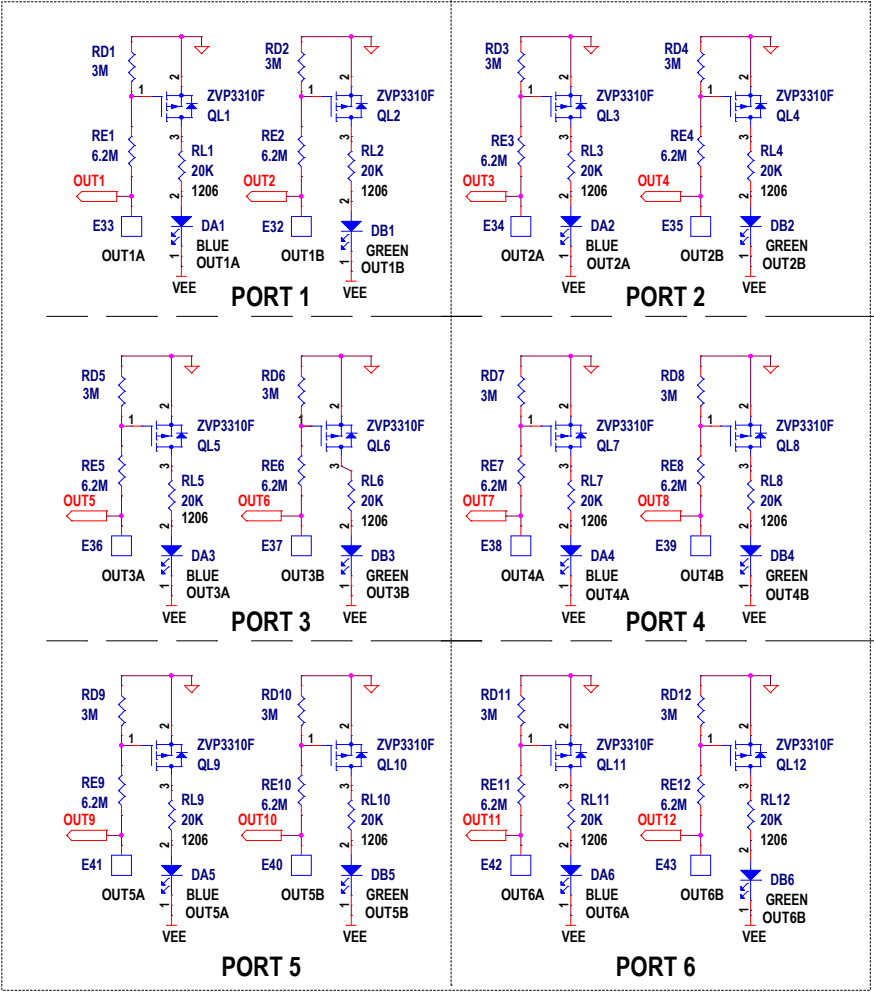
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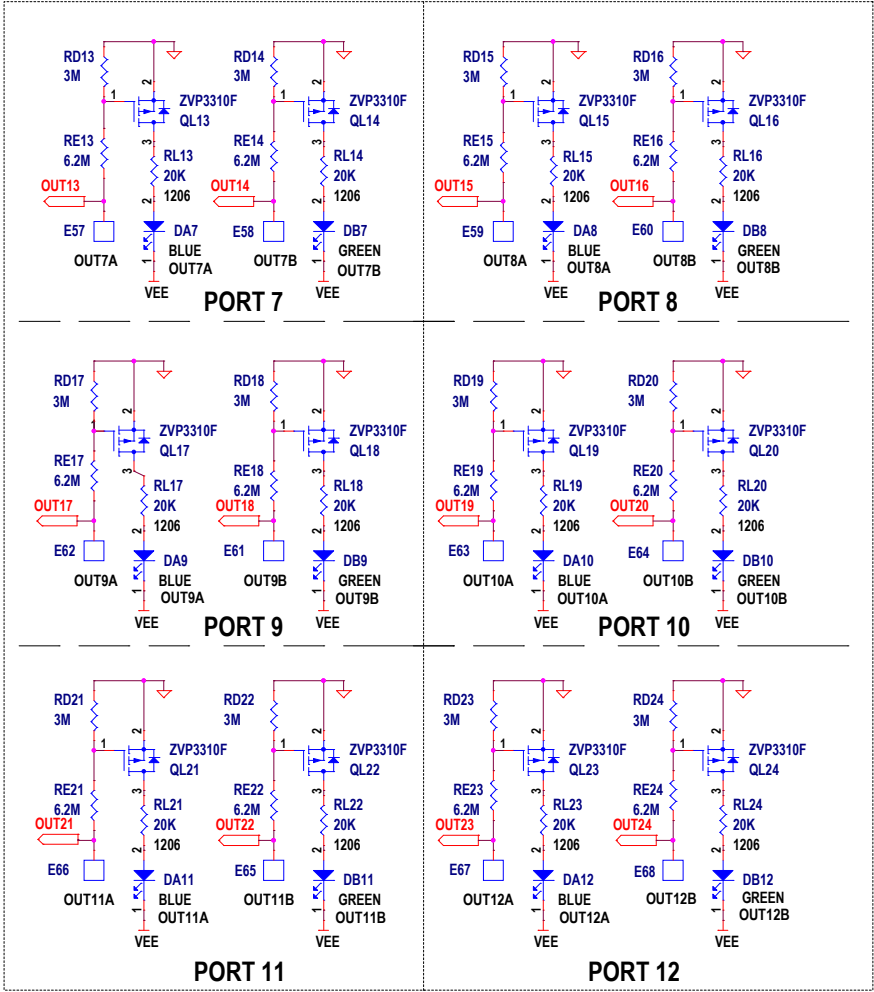
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		APP ENG.	ERIC.H				
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				24-PORT IEEE 802.3bt PSE MOTHER BOARD			
				IC NO. LTC3839EMSE#TRPBF		SCHEMATIC NO. AND REVISION:	
				SKU NO. DC3017A		710-DC3017A-A_REV03	
				SIZE: N/A		DATE:	
						SHEET 9 OF 11	

REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	3			

OPTIONAL PORT LED DRIVERS - PORTS 1 TO 6





OPTIONAL PORT LED DRIVERS - PORTS 7 - 12



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		APP ENG.	ERIC/H				
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				IC NO. LTC3839EMSE#TRPBF		SCHEMATIC NO. AND REVISION:	
				SKU NO. DC3017A		710-DC3017A-A_REV03	
				SIZE: N/A		DATE:	
						SHEET 10 OF 11	

HARDWARE

PCA ADDITIONAL PARTS

MP1	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
MP2	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
MP3	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
MP4	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
MP5	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
MP6	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
MP7	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
MP8	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
MP9	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
LB1	BOARD S/N LABEL 895-0154
PCB1	PCB, DC3017A-A REV03 (MAO BANG)
PK1	CONN.,POLARIZING KEY,PLUG,2.54mm PITCH,3.18mm DIM,ST,NATURAL THERMOPLASTIC
PK2	CONN.,POLARIZING KEY,PLUG,2.54mm PITCH,3.18mm DIM,ST,NATURAL THERMOPLASTIC
PK3	CONN.,POLARIZING KEY,PLUG,2.54mm PITCH,3.18mm DIM,ST,NATURAL THERMOPLASTIC

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CUSTOMER NOTICE

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APPROVALS

PCB DES.	KEN.M
APP ENG.	ERIC.H



ANALOG
DEVICES

www.analog.com



POWER BY
LINEAR™

Phone: (408)432-1900

TITLE: SCHEMATIC

24-PORT IEEE 802.3bt PSE MOTHER BOARD

IC NO. LTC3639EMSE#TRPBF

SKU NO. DC3017A

SCHEMATIC NO. AND REVISION:

710-DC3017A-A_REV03

SIZE: N/A

DATE:

SHEET 12 OF 0