



ASSEMBLY NOTES:

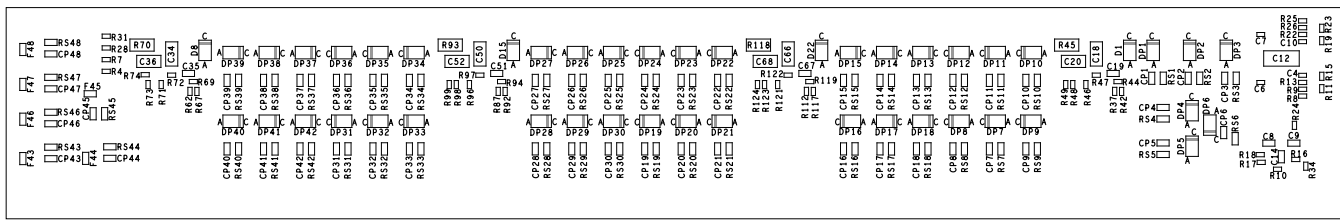
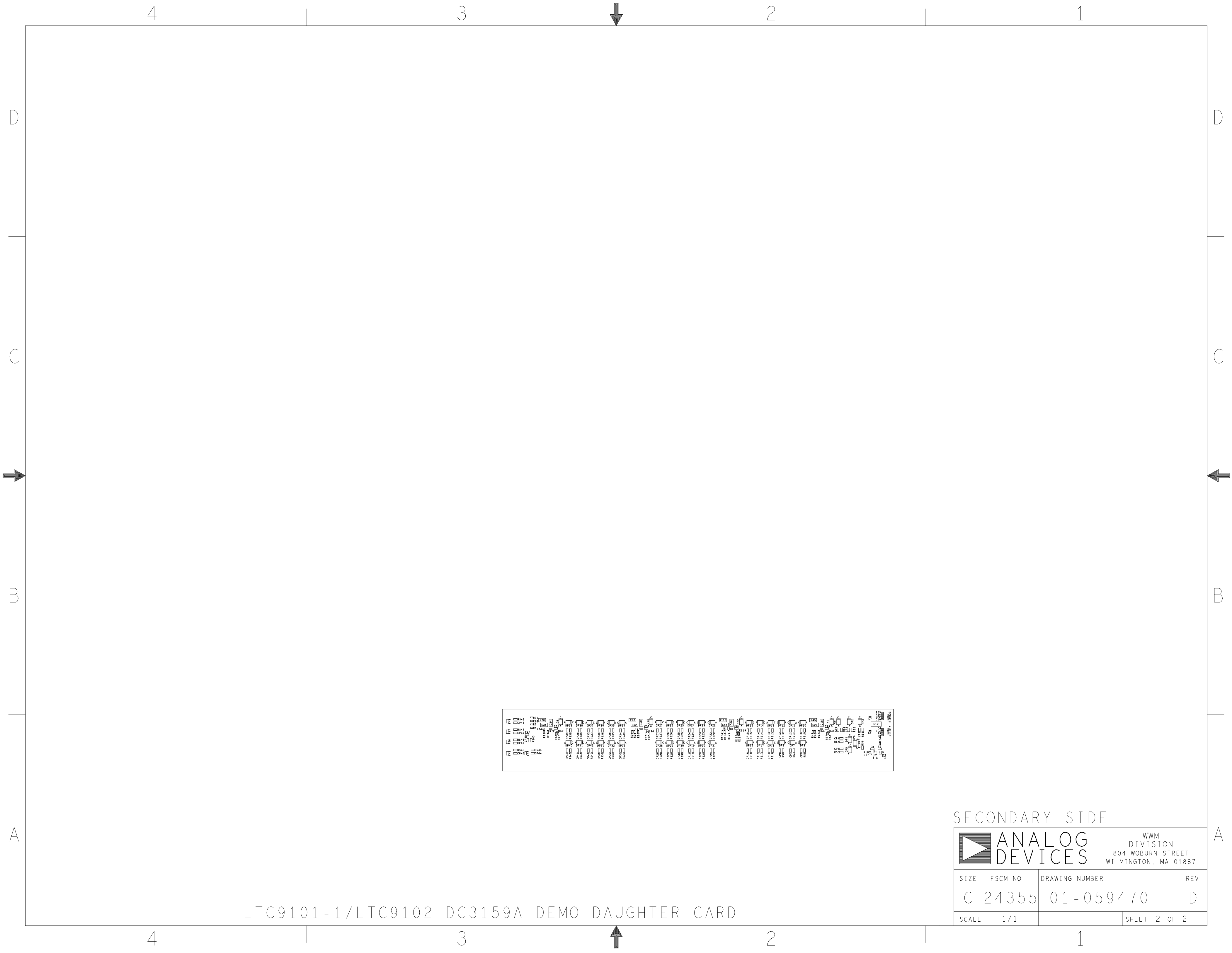
1. BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION).
2. REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED.
3. REPAIRS ARE NOT ALLOWED IN SOLDERMASK FREE AREAS ON EITHER SIDE OF THE BOARD.
4. RoHS COMPLIANCE: ASSEMBLY VENDOR SHOULD ASSURE COMPLIANCE WITH LEAD-FREE AND RoHS PCB ASSEMBLY STANDARDS (EU RoHS DIRECTIVE 2002/95/EC).
5. THIS BOARD CONTAINS ELECTROSTATIC DISCHARGE SENSITIVE DEVICES.
6. BOARDS TO BE SHIPPED SINGULATED AFTER ASSEMBLY PROCESS SMOOTHEN EDGES AND FREE FROM BURS AFTER DEPANELIZATION PROCESS

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	28NOV19	D.REYES
A1	AS PER ECR-097342	31AUG20	E.HORSMA
B	AS PER ECR-098354	07SEP20	E.HORSMA
B1	AS PER ECR-100937	11JAN21	E.HORSMA
C	AS PER ECR-102115	22APR21	E.HORSMA
D	AS PER ECR-105021	02NOV21	E.HORSMA

PRIMARY SIDE

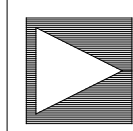
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES			APPROVAL		DATE		<div><div><div></div></div><div>ANALOG DEVICES</div></div> <div>WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887</div>		
TOLERANCES			TEMPLATE ENGINEER						
DECIMALS   FRACTIONS   ANGLES .XX   -.010   --1/32   -- 2 .XXX   -.005 .XXXX   -.0050			HARDWARE SERVICES M. JOSE		28NOV19		TITLE  ASSEMBLY  LTC9101-1 / LTC9102  DC3159A DEMO BRD Z		
MATERIAL			HARDWARE SYSTEMS						
			TEST ENGINEER						
			COMPONENT ENGINEER C. CAUILAN		28NOV19				
FINISH			TEST PROCESS				SIZE   FSCM NO   DRAWING NUMBER   REV  C   24355   01-059470   D		
			HARDWARE RELEASE K. JABATAN		28NOV19				
			DESIGNER N. CORDOVA		28NOV19				
DO NOT SCALE DWG			PTD ENGINEER D. REYES		28NOV19		SCALE   1 / 1   SHEET   1 OF 2		
			CHECKER E. HORSMA		28NOV19				

LTC9101-1/LTC9102 DC3159A DEMO DAUGHTER CARD



LTC9101-1/LTC9102 DC3159A DEMO DAUGHTER CARD

SECONDARY SIDE

		ANALOG DEVICES		WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887	
		SIZE	FSCM NO	DRAWING NUMBER	REV
C		24355	01-059470		D
SCALE		1 / 1		SHEET 2 OF 2	