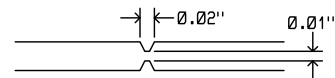


REVISION HISTORY

ECO	REV	DESCRIPTION	APPROVED	DATE
-	2	2ND PROTOTYPE	KWB	11-10-15

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HA or EQUIVALENT
-FINISHED THICKNESS TO BE $0.062'' \pm 0.005''$
-TOTAL OF 2 LAYERS WITH 1 OZ. CU
-FLAMMABILITY RATING: 94v-0 MINIMUM
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, $0.001''$ THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING
-HOLE LOCATION TOLERANCES ARE $\pm 0.003''$ IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN GLOSS
-GOLD IMMERSION (ENIG) BOTH SIDES
-SILKSCREEN TOP AND BOTTOM SIDES USE WHITE NON-CONDUCTIVE INK
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE
PAD SIZE MAY BE MODIFIED TO MEET END FINISH
- PCBs ARE TO BE RoHS COMPLIANT
- DESIGN HAS NON-SOLDER MASK DEFINED PADS ON U1
U1 SOLDER MASK SIZE IS 27 MIL AND PAD SIZE IS 20 MIL
DO NOT CHANGE ANY SIZE ON THIS COMPONENT
- SCORING FOR PANELIZED PCB:

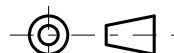


SIZE	QTY	SYM	PLATED	TOL
$0.010''$	100	x	YES	$\pm 0.003''$
$0.035''$	8	o	YES	$\pm 0.003''$
$0.040''$	17	⊗	YES	$\pm 0.003''$
$0.070''$	2	⊕	NO	$\pm 0.003''$
$0.090''$	4	◇	YES	$\pm 0.003''$

UNLESS OTHERWISE SPECIFIED

DIMENSIONS ARE IN INCHES
TOLERANCES:
 $0.XX'' = \pm 0.01''$
 $0.XXX'' = \pm 0.005''$
INTERPRET DIM AND TOL PER
ASME Y14.5M-1994

THIRD ANGLE PROJECTION



APPROVALS

PCB DES. KWB

APP ENG. KWB

SCALE = 1:1



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TITLE: FABRICATION DRAWING
REINFORCED USB ISOLATOR

SIZE
N/A

IC NO.

LTM2894IY
DEMO CIRCUIT 2438A

REV.
2

DATE: Tuesday, November 10th, 2015

SHEET 1 OF 1