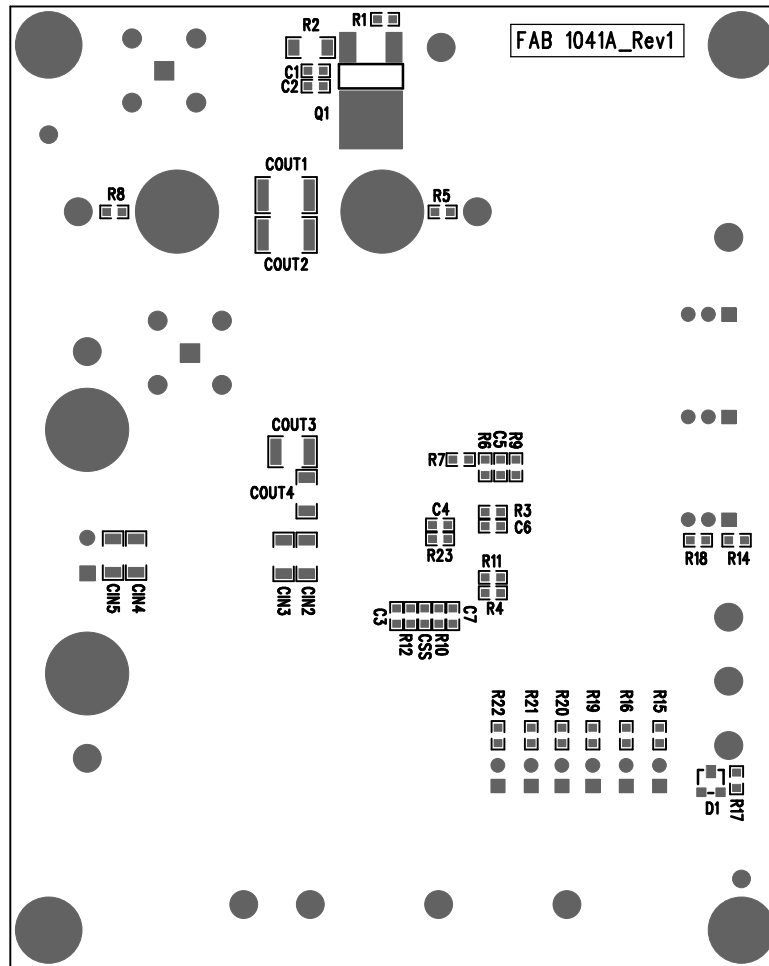



REVISION HISTORY			
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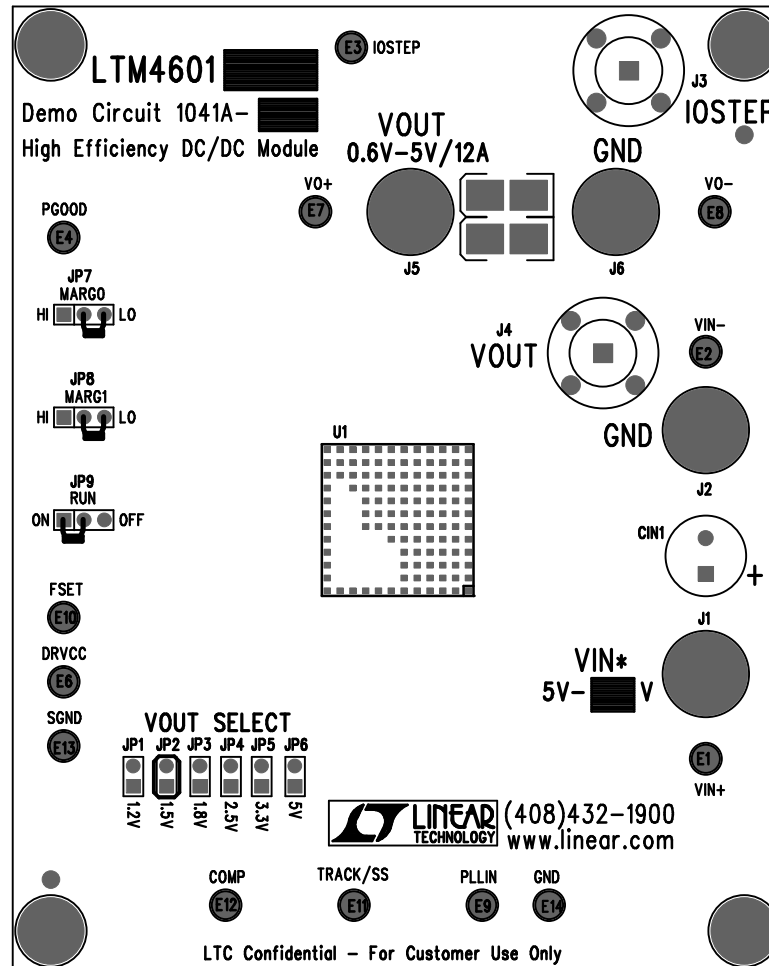
APPROVALS		LINEAR TECHNOLOGY CORPORATION		
DRAWN: Rudy Bautista		1630 MCCARTHY BLVD MILPITAS, CA. 95035		
ENGINEER: MXie		(408) 432-1900		
APPROVED:		(408) 434-0507 (FAX)		
CHECKED:		www.linear.com		
DCKKXA_00.PCB		LTC Confidential - For Customer Use Only		
xxxX_x.DSN		HIGH EFFICIENCY DC/DC MODULE		
Date:		Demo Circuit 1041A		
Wednesday, June 28, 2006		Rev 2		
Sheet		1 of 1		

2019 2020




Assembly Bottom Drawing

<h1>APPROVALS</h1>			 <div> LINEAR TECHNOLOGY </div>			1630 McCarthy Blvd. Milpitas, CA 95035 PH: (408)432-1900		
	INIT	DATE	<div>TITLE:</div> <div>High Efficiency DC/DC Module</div> <div> <div>SIZE</div> <div> NONE Demo Circuit 1041A </div> <div>REV. 1</div> </div> <div> <div>DES-23xxxx</div> <div>SHT 2 of 2</div> </div>					
DRAWN								
CHECK								
DESIGN	Rudy B							
ENGR	MXie							
SCALE = NONE								



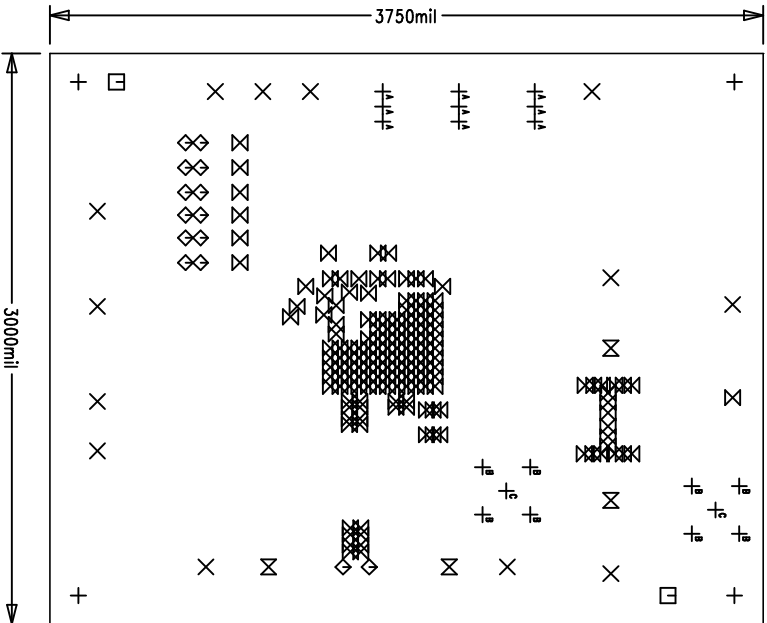
	U1	VIN*
DC1041A–A	LTM4601EV	5V–20V
DC1041A–B	LTM4601HVEV	5V–28V

Assembly, Top Drawing

APPROVALS			 LINEAR TECHNOLOGY 1630 McCarthy Blvd. Milpitas, CA 95035 PH: (408)432-1900
	INIT	DATE	
DRAWN			TITLE: High Efficiency DC/DC Module
CHECK			
DESIGN	RmB		
ENGR	MXie		
			SIZE NONE Demo Circuit 1041A REV. 1
SCALE = NONE			DES-23xxxx SHT 1 of 2

Linear Tech. Corp.
Demo Circuit 1041A

Component Side



SIZE	QTY	SYM	PLTD	TOL
187	4	+	YES	+/-3mil
63	13	X	YES	+/-3mil
70	2	□	NO	+/-3mil
35	14	◇	YES	+/-3mil
10	164	Σ	YES	+/-3mil
205	4	⊠	YES	+/-3mil
31	9	⊕	YES	+/-3mil
52	8	⊕	YES	+/-3mil
59	2	⊕	YES	+/-3mil

REVISIONS

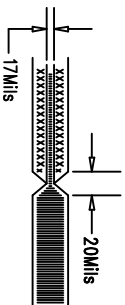
REV

APPR

DATE

NOTES: UNLESS OTHERWISE SPECIFIED:

1. ARTWORK P/N Demo Circuit 1041A Rev1
2. FAB PER IPC-A-600, 4-layers.
3. MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4 .062 +/- .005 INCH THICKNESS WITH 2 OZ. COPPER FINISH ON TWO OUTER LAYERS AND 1 OZ. COPPER ON TWO INTERNAL LAYERS. FLAMABILITY RATING: 94 V-2 MINIMUM .
4. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN. 0.00 ARE PRIMARY DATUMS.
5. BOARD: SELECTIVE PLATED BOARD. SOLDER MASK OVER BARE COPPER, COLOR, GREEN LPL. WHITE TIN IMMERSION (OMIKRON) BOTH SIDES. SILKSCREEN COMPONENT SIDE WITH WHITE NON-CONDUCTIVE INK.
PLATE THRU ALL HOLES WITH COPPER
MIN. PLATING THICKNESS: 1 OZ. EXCEPT WHERE PLATING NOT REQUIRED
6. DRILL: ALL HOLES SHALL BE DRILLED +/-3mil WITH RESPECT TO CTR. OF DRILLED PAD.
ALL HOLES FINISHED SIZE AFTER PLATING.
7. DROP ALL UNUSED PADS ON INNER LAYERS.
8. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
9. SCORING:



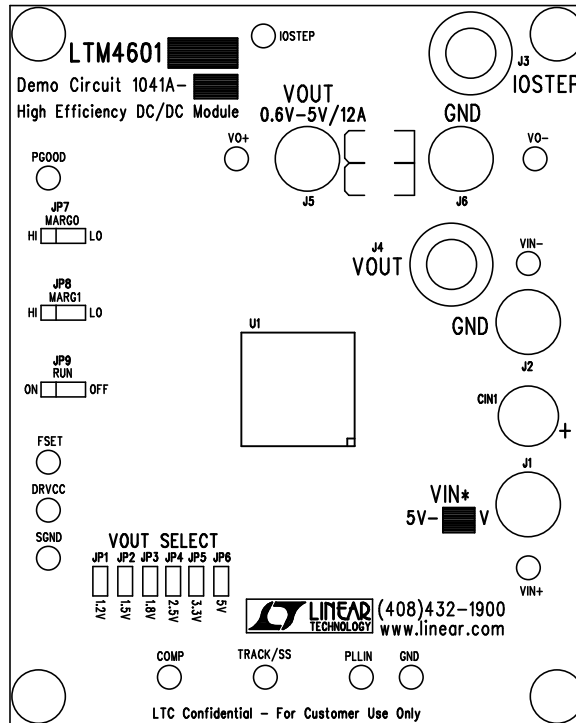
Fabrication Drawing

APPROVALS		TITLE:	
DRAWN	INT	DATE	
CHECK			
DESIGN	Rmb		
ENGR	Wte		
		SIZE	REV. 1
		NONE	
SCALE = NONE		DES-23xxx	SHT 1 of 1

LINEAR TECHNOLOGY
1630 McCarthy Blvd.
Milpitas, CA 95035
PH: (408)432-1900

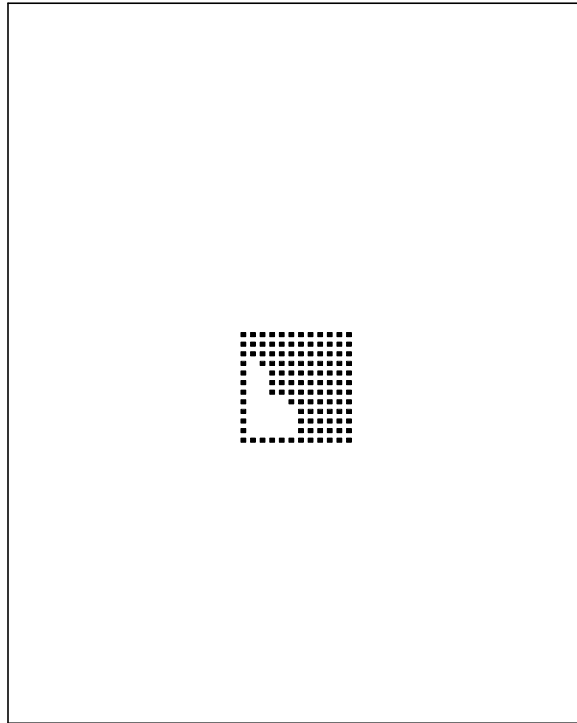
High Efficiency DC/DC Module
Demo Circuit 1041A

Linear Tech. Corp. Silkscreen Top
Demo Circuit 1041A



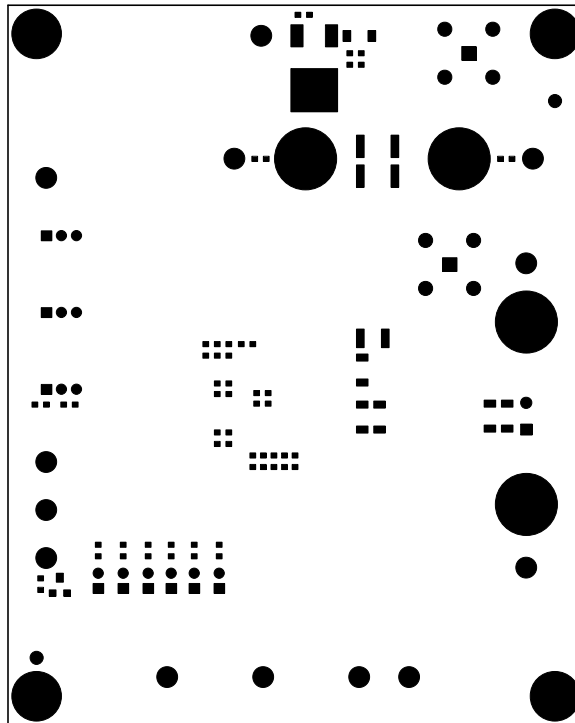
Linear Tech. Corp.
Demo Circuit 1041A

PasteMask Top

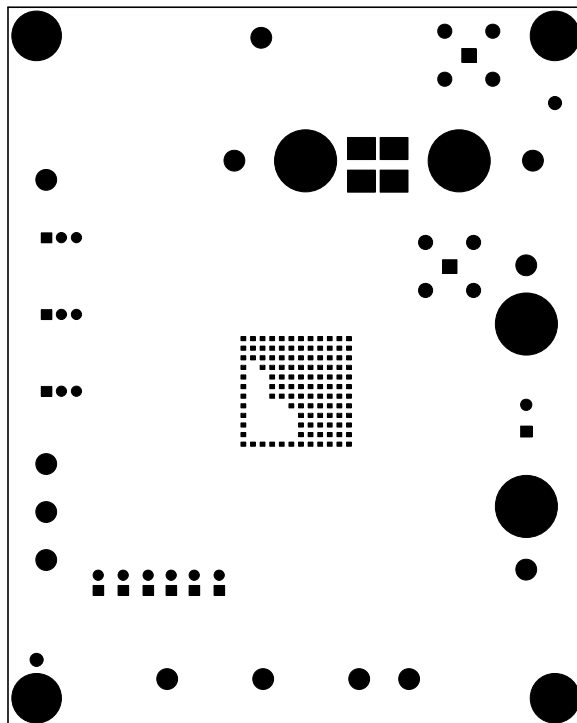


Linear Tech. Corp.
Demo Circuit 1041A

SolderMask Bottom

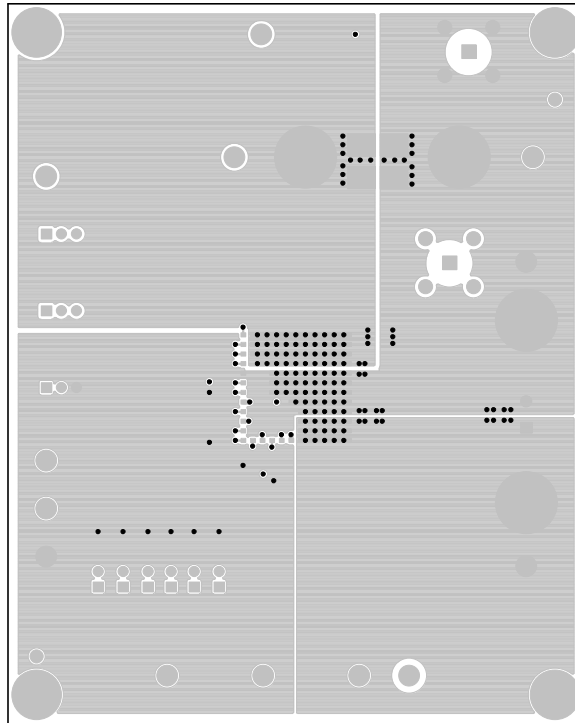


Linear Tech. Corp.
Demo Circuit 1041A
SolderMask Top



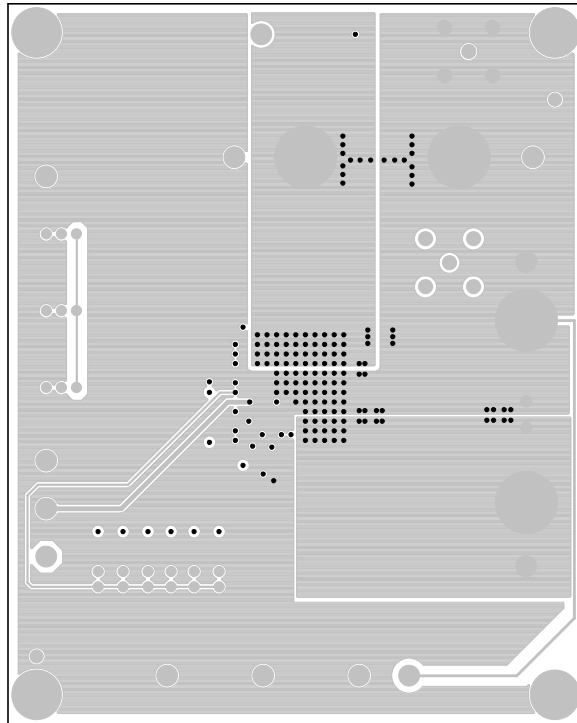
Linear Tech. Corp.
Demo Circuit 1041A

Component Side



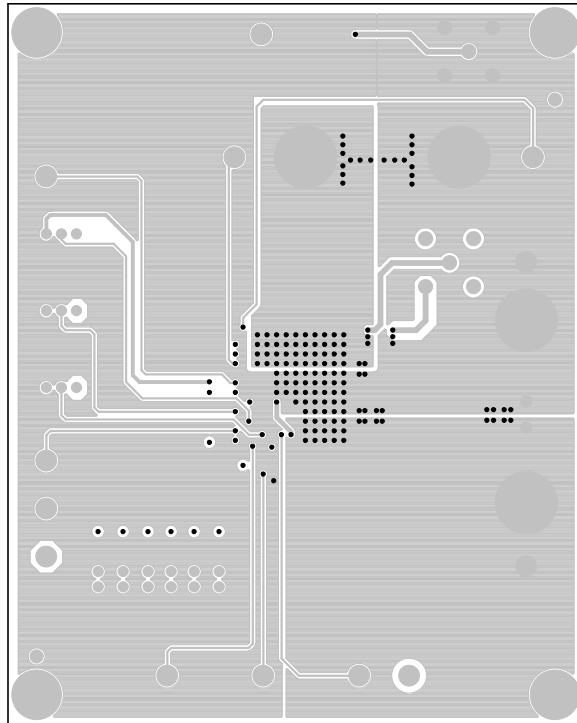
Linear Tech. Corp.
Demo Circuit 1041A

Layer2



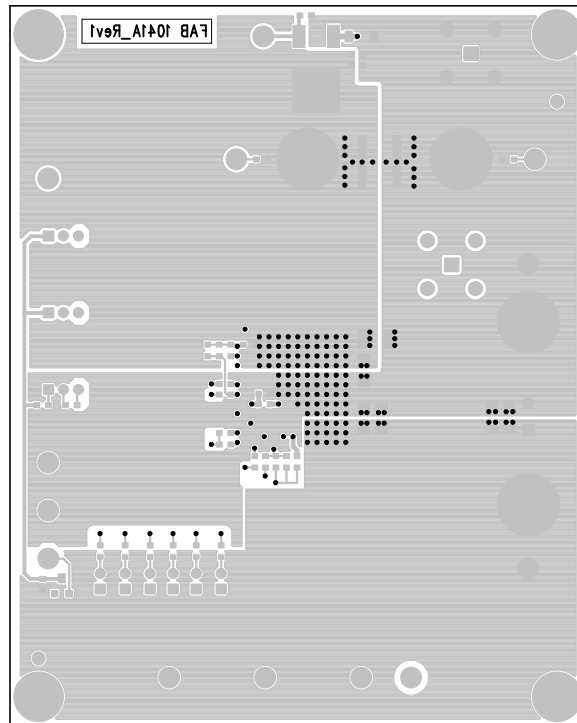
Linear Tech. Corp.
Demo Circuit 1041A

Layer3

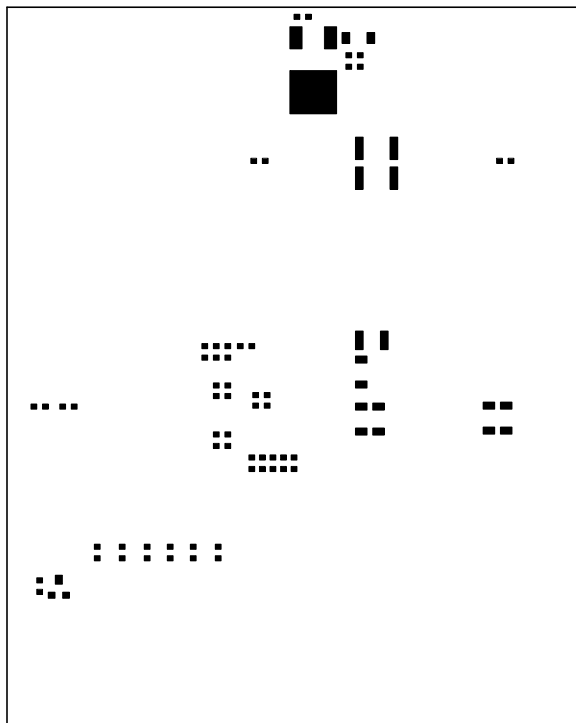


Linear Tech. Corp.
Demo Circuit 1041A

Solder Side



Linear Tech. Corp.
Demo Circuit 1041A PasteMask Bottom



Linear Tech. Corp.
Demo Circuit 1041A

Silkscreen Bottom

