

NOTE: THE "ZERO" OHM RESISTORS IN VDAC1 LINE AND BETWEEN PGND AND SGND AND BETWEEN THE OUTPUTS AND THE SENSE LINES ARE NOT COMPONENTS. THEY ARE TRACES ON THE PCB.

CONNECT SIGNAL GND PLANE TO PAD H12 AT ONE POINT

VDAC INJECTS THE CONTROL VOLTAGE FROM THE LTC2978 INTO THE FEEDBACK DIVIDER OF THE LTM4603 TO TRIM AND MARGIN VOUT

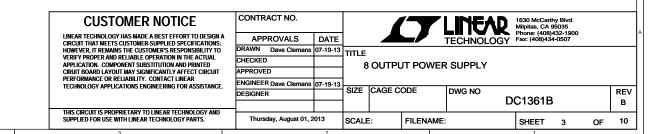
VDACM0 <<-

R103

ZERO

R21

13.3K 1%



3.3V_PU

R14

10K

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VOUT_EN0

