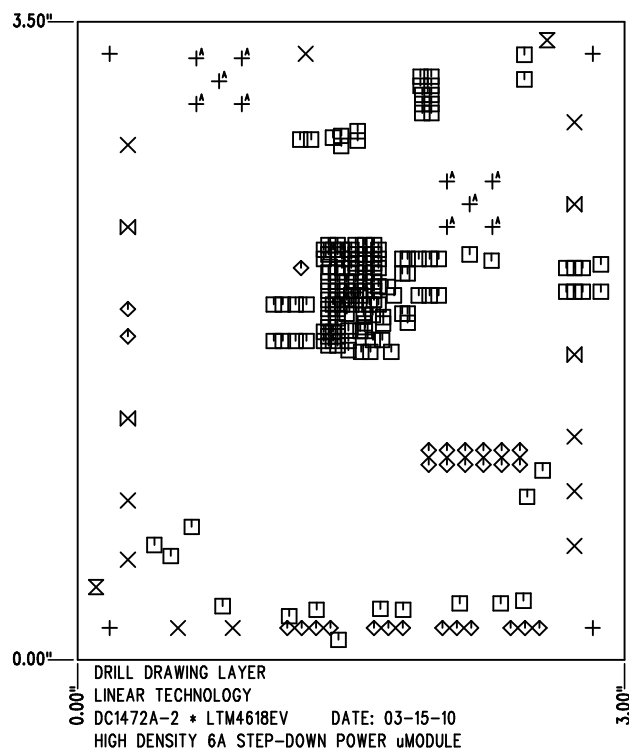
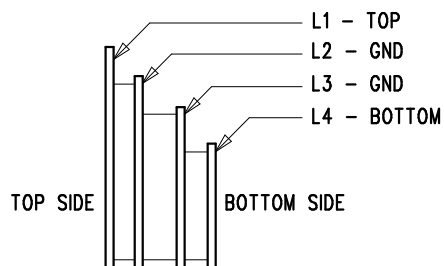


SHOWN FROM COMPONENT SIDE



LAYER STRUCTURE



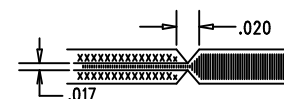
REVISION HISTORY

ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	2	PRODUCTION	SAM Y.	03-15-10

SIZE	QTY	SYM	PLATED	TOL
0.188	4	+	YES	+/-0.003"
0.094	10	X	YES	+/-0.003"
0.01	154	□	YES	+/-0.003"
0.035	28	◇	YES	+/-0.003"
0.07	2	⊗	NO	+/-0.003"
0.215	4	⊠	YES	+/-0.003"
0.055	10	+ ^A	YES	+/-0.003"

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS
AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003"
IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
(LEAD FREE SOLDER CAN BE USED FOR PROTOTYPE)
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX" = ± 0.01 " 0.XXX" = ± 0.005 " INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION	APPROVALS		LINEAR TECHNOLOGY 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY
	PCB DES.	KT	
	APP ENG.	SAM Y.	
			TITLE: FABRICATION DRAWING
			HIGH DENSITY 6A STEP-DOWN POWER μ MODULE
			SIZE IC NO. LTM4618EV
			N/A DEMO CIRCUIT 1472A
SCALE = NONE			REV 2
FILENAME: DC1472A-2.PCB			SHT 1 OF 1