

REVISION HISTORY					
ECO	REV	DESCRIPTION	APPR	DATE	
-	2	PRODUCTION	JEFF Z.	09-01-11	

**NOTES: UNLESS OTHERWISE SPECIFIED**

- WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
- REFLOW SOLDER TOP SIDE SMD.
- MAXIMUM SOLDER TEMPERATURE IS 240 DEG. C.
- LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
- INSTALL SHUNTS AS SHOWN ON ASSY DRAWING.
- PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS.
- DE-PANELIZE BOARDS AFTER ASSEMBLY AND ROUTE OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
- DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD.

**PCB : TOP**

**PCB : BOTTOM**

**TURETS**

**7. INSTALL 4 STANDOFFS AT 4 LOCATIONS AS SHOWN BELOW:**

**8. MARK ASSEMBLY TYPE AND VOLT CURRENT WITH BLACK PERMANENT MARKER BASED ON THE TABLE BELOW:**

ASSY	IC	Iout	R13	R34	Cin1,2,13,14,15	Cin6,7,18,19	Cou1,2,26	Cou3,34	Cou2,29	Cou3,37
-A	U1,U2	50A	OPT	OPT	OPT	OPT	OPT	OPT	OPT	OPT
-B	U1,U2,U3	75A	0	0	22uF		OPT	100uF	470uF	OPT
-C	U1,U2,U3,U4	100A	OPT	0	22uF		22uF	100uF	470uF	470uF

**LINEAR TECHNOLOGY DATE: 09-01-11  
LTM4620EV DC1780A Rev2 HIGH EFFICIENCY, POLYPHASE, STEP-DOWN POWER μMODULE LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY**

**LTM4620EV DC1780A Rev2 HIGH EFFICIENCY, POLYPHASE, STEP-DOWN POWER μMODULE**

**APPROVALS**

PCB DES.	RB	APP ENG.	JEFF Z.

**LINEAR TECHNOLOGY**  
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LCG DESIGN  
FOR QUOTATION  
RELEASE ONLY

**TITLE: TOP ASSEMBLY DRAWING  
HIGH EFFICIENCY, POLYPHASE,  
STEP-DOWN POWER μMODULE**

**SIZE** IC NO. LTM4620EV  
N/A  
**REV.** DEMO CIRCUIT 1780A  
2  
**SCALE** = NONE  
**FILENAME:** DC1780A-Rev2.pcb  
**SHT1 OF 2**