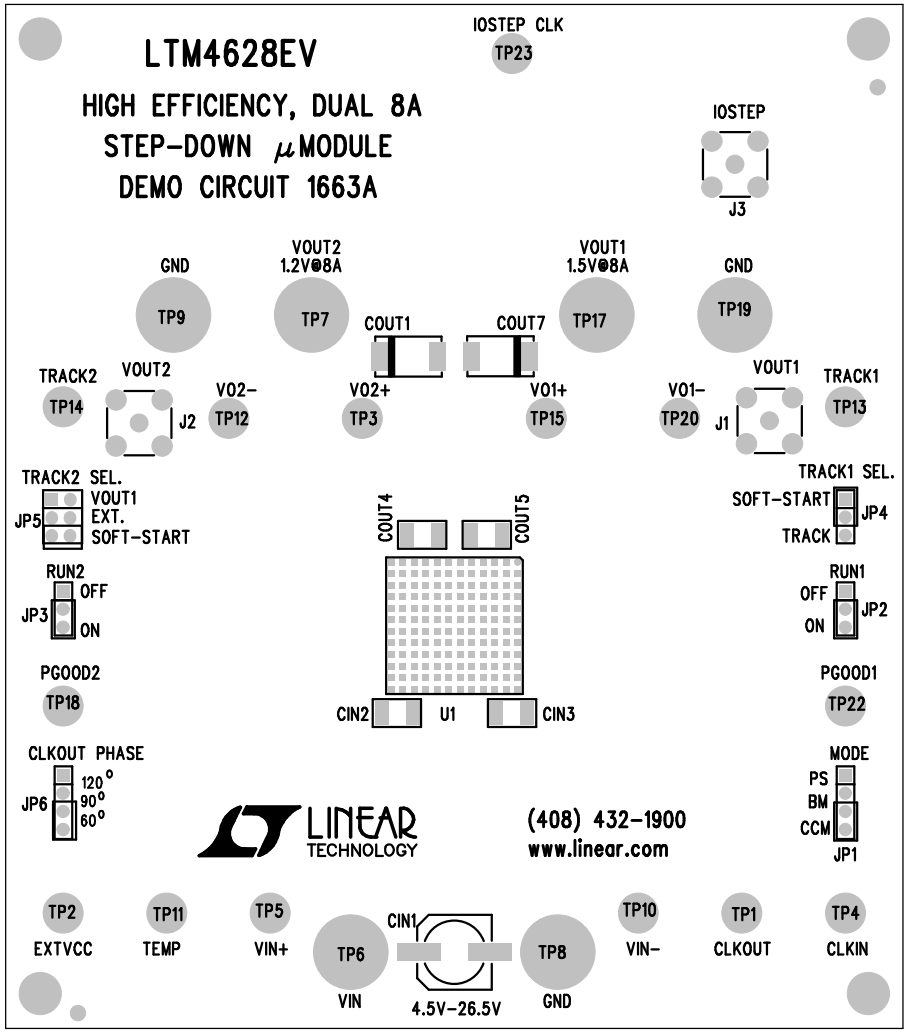
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
–	1	PRODUCTION	JEFF Z.	06-17-10

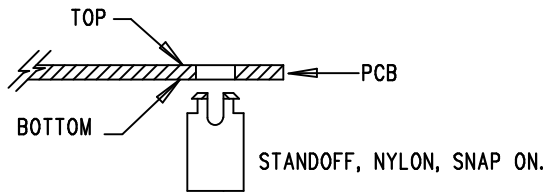


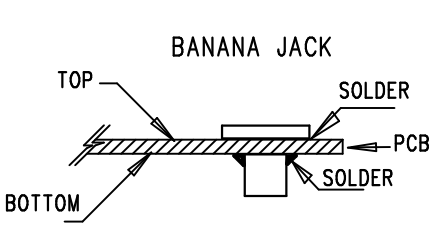
TOP SILKSCREEN

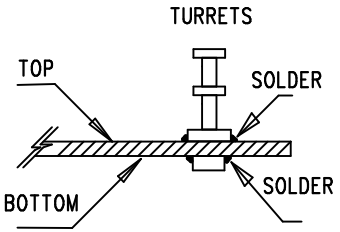
LINEAR TECHNOLOGY
DC1663A-LTM4628EV
HIGH EFFICIENCY, DUAL 8A
STEP-DOWN μMODULE
DATE: 06-17-10

NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS. LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. INSTALL SHUNTS AS SHOWN ON ASSY DRAWING.
5. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
6. DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD.
7. INSTALL 4 STANDOFFS, BANANA JACK, TURRETS.







APPROVALS	
PCB DES.	AK
APP ENG.	JEFF Z.
SCALE = NONE	

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FOR CUSTOMER USE ONLY

TITLE: TOP ASSEMBLY DRAWING
HIGH EFFICIENCY, DUAL 8A
STEP-DOWN μMODULE

SIZE	IC NO.	REV.
N/A	LTM4628EV DEMO CIRCUIT 1663A	1

FILENAME: DC1663A-1.PCB SHT 1 OF 2