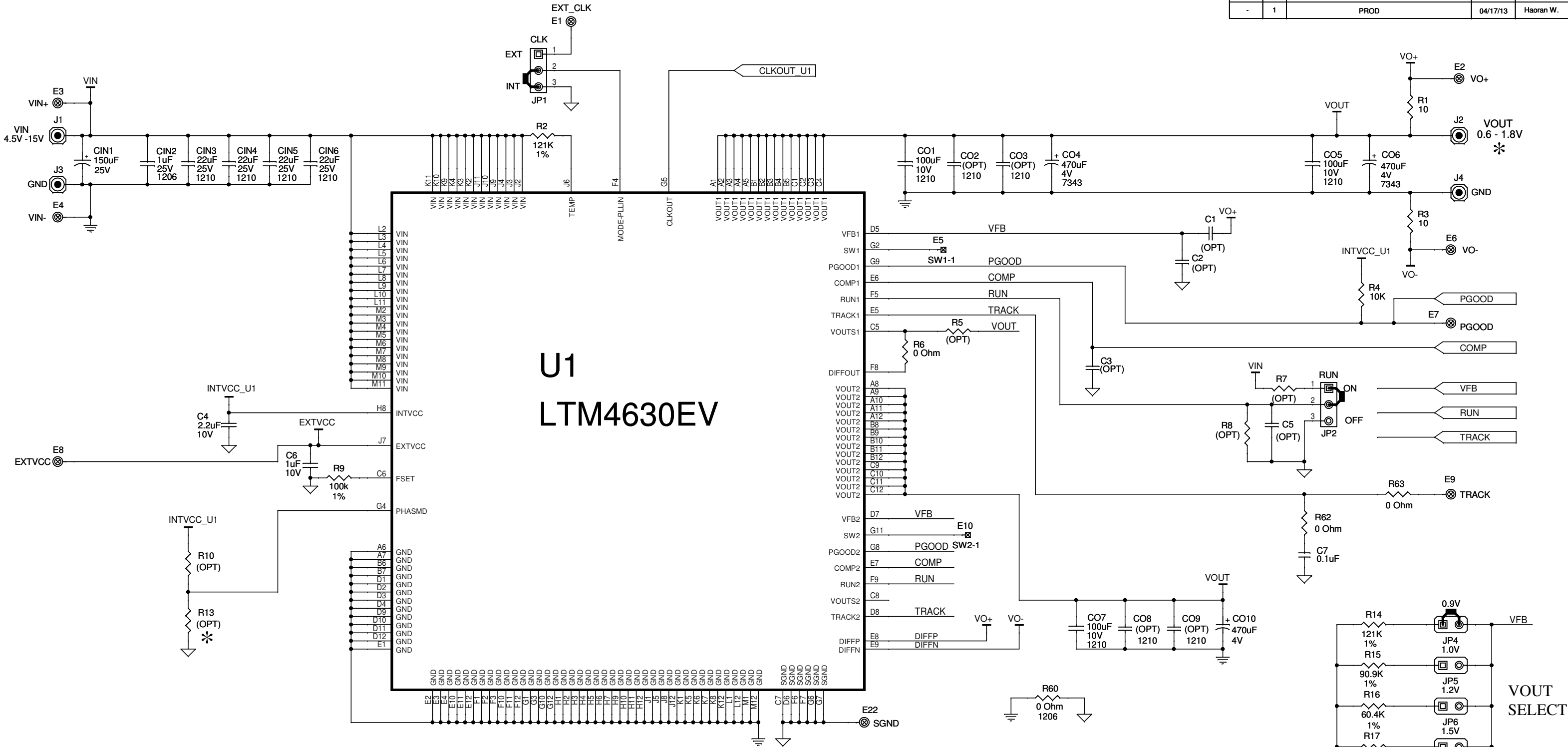
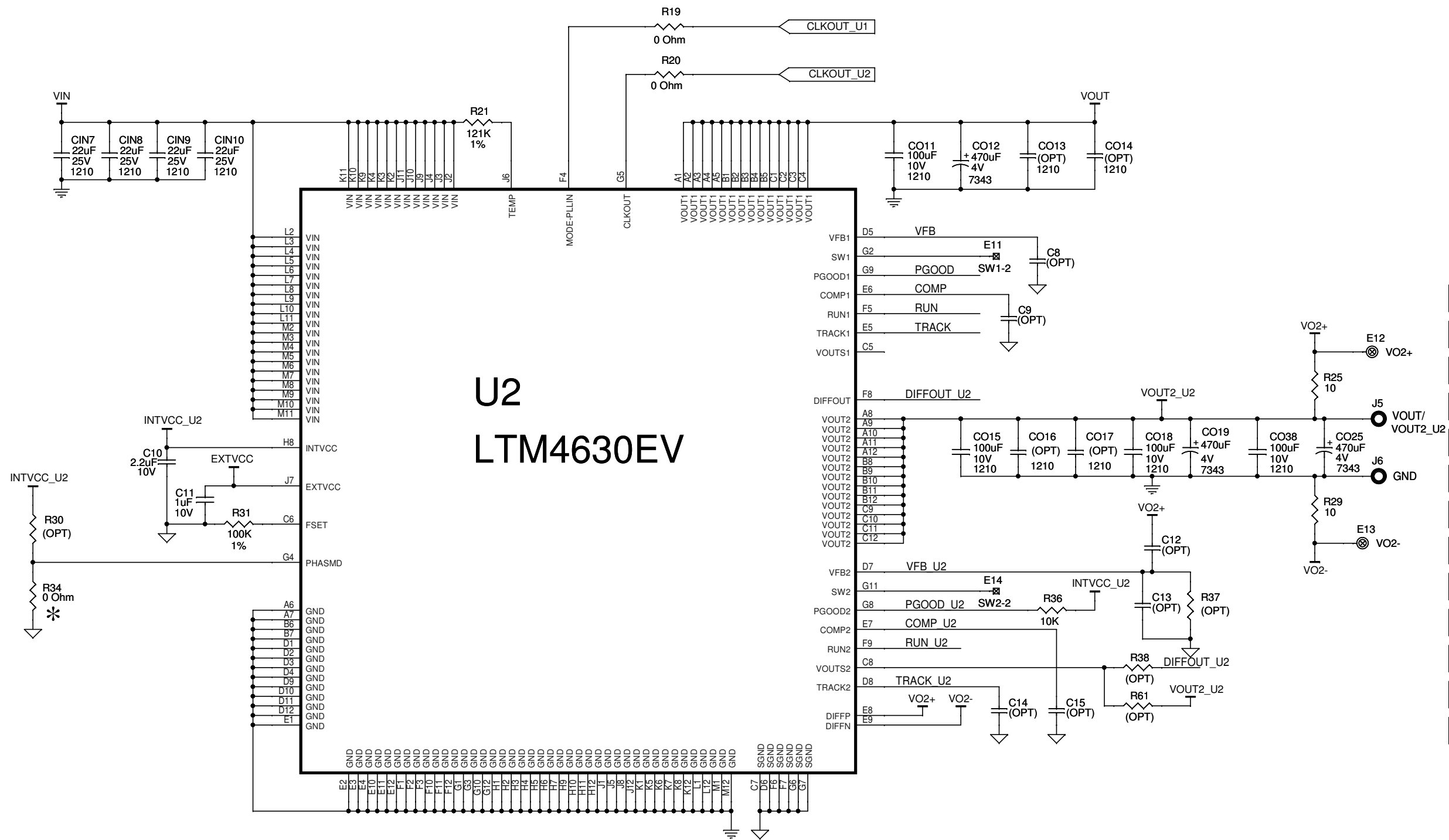
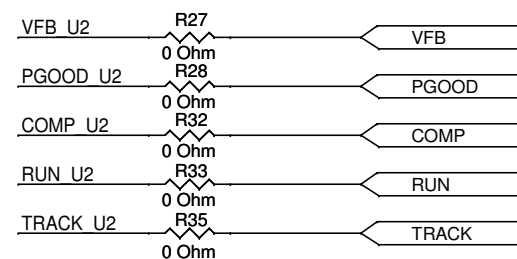
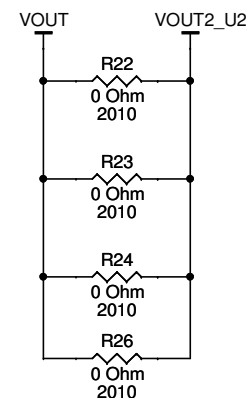


REVISION HISTORY				
ECO	REV	DESCRIPTION	DATE	APPROVED
-	1	PROD	04/17/13	Haoran W.





REMOVE 0 OHM JUMPERS  
FOR SEPARATE VOUT2\_U2.



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C:\DESIGN\20\2007A-1\2007A-1.DSN

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

## APPROVALS

PCB DES.	MI/RB
APP ENG.	Haoran W.

SCALE = NONE



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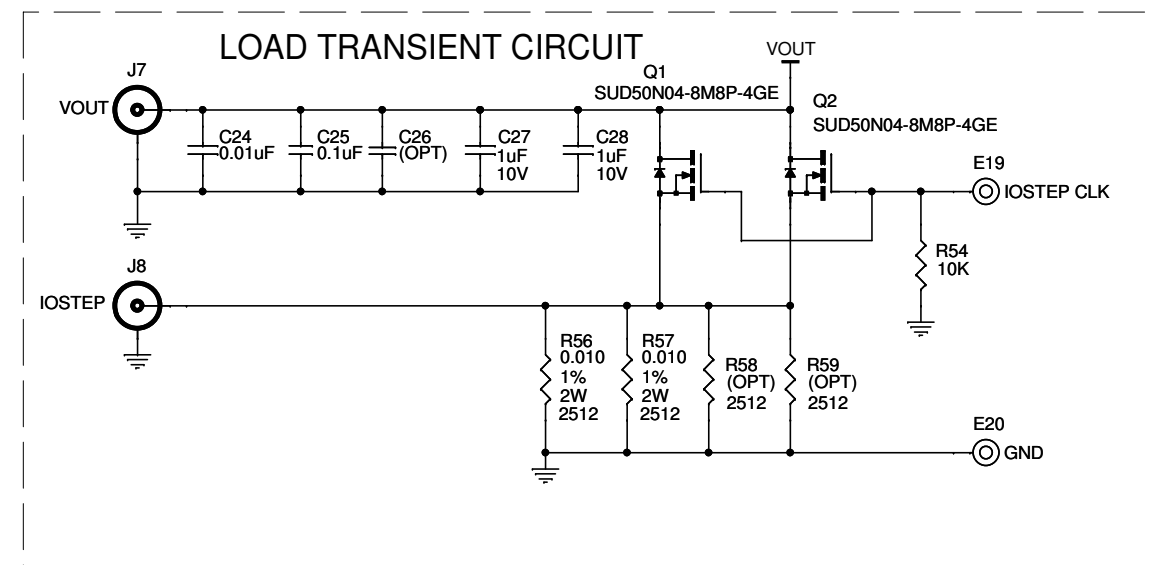
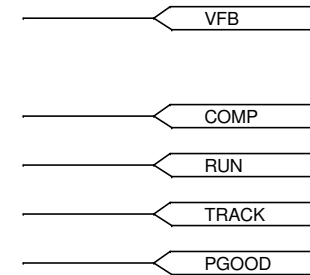
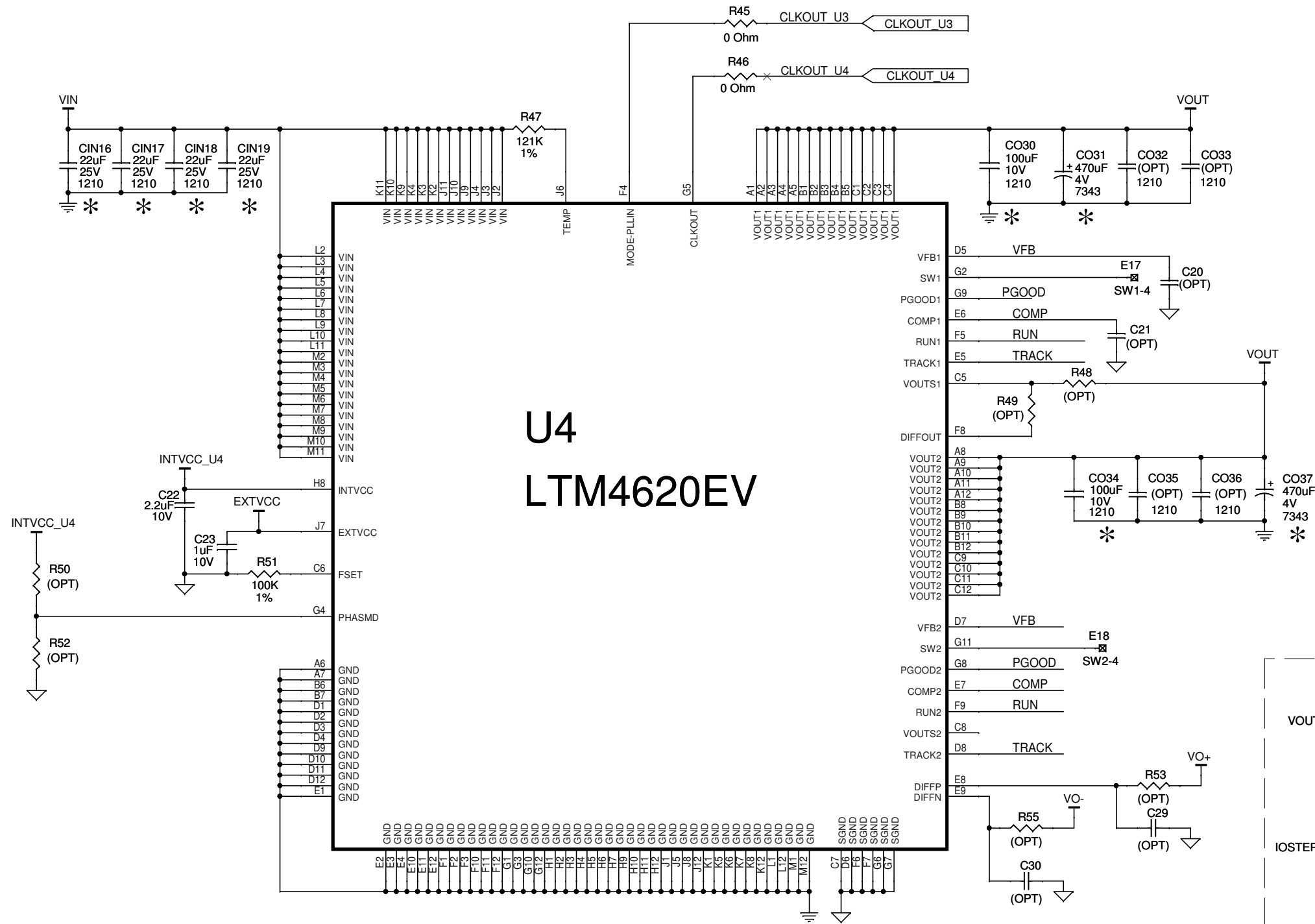
TITLE: SCHEMATIC

HIGH EFFICIENCY, POLYPHASE,  
STEP-DOWN POWER  $\mu$ MODULE REGULATOR

SIZE	IC NO.	REV.
N/A	LTM4630EV	1
	DEMO CIRCUIT 2007A	

DATE: 05/16/13 04:49:29 SHEET 2 OF 4





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C:\DESIGN\20\2007A-1\2007A-1.DSN

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### APPROVALS

PCB DES.	R.B.
APP ENG.	Haoran W.

SCALE = NONE



TITLE: SCHEMATIC

HIGH EFFICIENCY, POLYPHASE,  
STEP-DOWN POWER  $\mu$ MODULE REGULATOR

SIZE N/A IC NO. LTM4630EV  
DEMO CIRCUIT 2007A

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