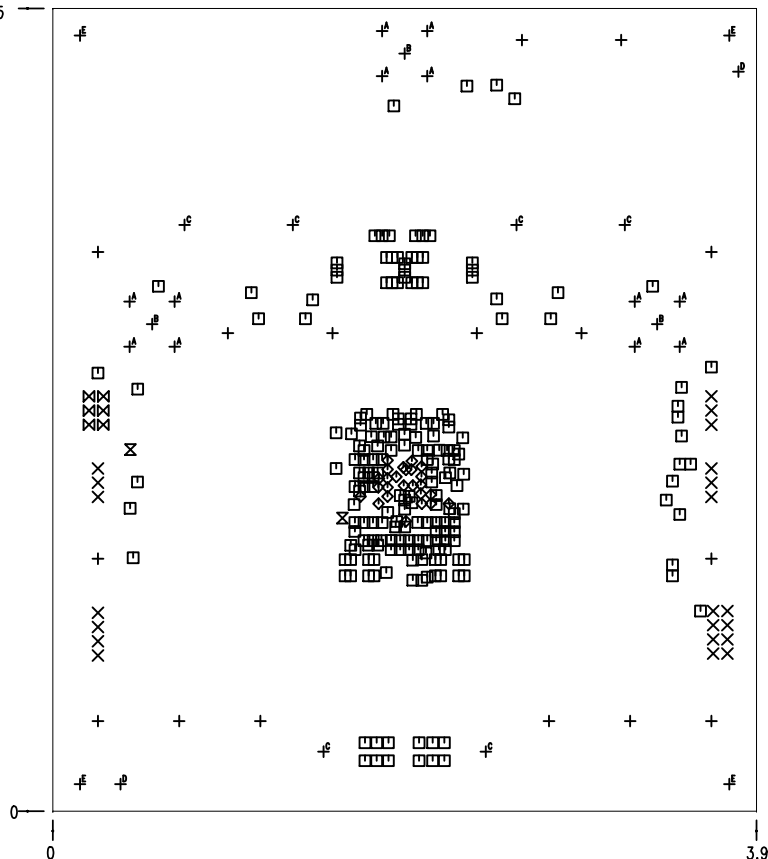


4.45



SIZE	QTY	SYM	PLATED	TOL
0.008	25	◇	YES	+/-0.003
0.01	203	□	YES	+/-0.003
0.015	2	⊗	YES	+/-0.003
0.031	21	⊗	YES	+/-0.003
0.039	6	⊗	YES	+/-0.003
0.055	3	⊕ <sup>B</sup>	YES	+/-0.003
0.059	12	⊕ <sup>A</sup>	YES	+/-0.003
0.07	2	⊕ <sup>D</sup>	NO	+/-0.003
0.095	16	⊕	YES	+/-0.003
0.187	4	⊕ <sup>E</sup>	NO	+/-0.005
0.215	6	⊕ <sup>C</sup>	YES	+/-0.005

## REVISION HISTORY

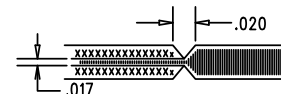
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	1	PRODUCTION	YL	01-18-16

## LAYER STRUCTURE

—	LAYER 1	COMPONENT SIDE
—	LAYER 2	GND
—	LAYER 3	SIGNAL
—	LAYER 4	SOLDER SIDE

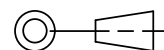
## NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.  
-FINISHED THICKNESS TO BE 0.062 +/- .005  
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON ALL LAYERS  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0/0 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



- DESIGN HAS SOLDER MASK DEFINED PADS ON U1  
WITH 0.029 PAD, 0.025 SOLDER MASK, AND 0.024 SOLDER PASTE.  
DO NOT CHANGE ANY SIZE ON THIS COMPONENT.

UNLESS OTHERWISE SPECIFIED  
DIMENSIONS ARE IN INCHES  
TOLERANCES:  
0.XX = ± 0.01  
0.XXX = ± 0.005  
INTERPRET DIM AND TOL  
PER ASME Y14.5M-1994  
THIRD ANGLE PROJECTION



## APPROVALS

PCB DES.	LT
APP ENG.	YL
SCALE = NONE	



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TITLE: FABRICATION DRAWING  
HIGH EFFICIENCY, ULTRATHIN DUAL 10A  
STEP-DOWN  $\mu$ MODULE REGULATOR

SIZE N/A	IC NO. LTM4631EV DEMO CIRCUIT 2147A	REV 1
FILENAME: DC2147A-1.PCB	SHT 1 OF 1	