

# LTM4631 Dual 10A or Single 20A μModule Regulator

## DESCRIPTION

Demonstration circuit 2268A-G is a high efficiency, high density, dual 10A, switch mode step-down power supply on a compact 1.5-inch × 1.2-inch PCB. It features the [LTM®4631](#) μModule® regulator. The input voltage is from 4.5V to 15V. The output voltage is programmable from 0.6V to 1.8V. DC2268A-G can deliver up to 10A maximum in each channel. As explained in the data sheet, output current derating is necessary for certain  $V_{IN}$ ,  $V_{OUT}$ , and thermal conditions. The board operates in continuous conduction mode in heavy load conditions. For high efficiency at low load currents, the resistor jumper (R1/R2) selects pulse-skipping mode for noise sensitive applications or Burst Mode® operation in less noise sensitive applications. Two outputs can be connected in parallel for a single 20A output solution with optional jumper resistors. The

board allows the user to program how its output ramps up and down through the TRACK/SS pin. Remote output voltage sensing is available for improved output voltage regulation at the load point. An optional input inductor L1 reduces the EMI noise for noise sensitive applications. DC2268A can be easily inserted to an edge connector for testing and debugging. These features and the availability of the LTM4631 in a compact 16mm × 16mm × 4.41mm LGA package make it ideal for use in many high density point-of-load regulation applications. The LTM4631 data sheet must be read in conjunction with this demo manual for working on or modifying the DC2268A-G.

**[Design files for this circuit board are available.](#)**

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## BOARD PHOTO



Figure 1. LTM4631/DC2268A-G Demo Board

### PERFORMANCE SUMMARY

Specifications are at  $T_A = 25^\circ\text{C}$

PARAMETER	CONDITIONS/NOTES	VALUE
Input Voltage Range		4.5V ~ 15V
Output Voltage $V_{OUT1}$	$V_{IN} = 4.5V \sim 15V$ , $I_{OUT1} = 0A \sim 10A$	$1V \pm 1.5\%$
Output Voltage $V_{OUT2}$	$V_{IN} = 4.5V \sim 15V$ , $I_{OUT2} = 0A \sim 10A$	$1.5V \pm 1.5\%$
Per Channel Maximum Continuous Output Current	Derating is necessary for certain $V_{IN}$ , $V_{OUT}$ and thermal conditions	10A (per channel)
Default Operating Frequency		650kHz
Resistor Programmable Frequency Range		250kHz to 780kHz
External Clock Sync. Frequency Range		500kHz to 780kHz
Efficiency of Channel 1	$V_{IN} = 5V$ , $V_{OUT1} = 1.0V$ , $I_{OUT1} = 10A$ , $f_{SW} = 650kHz$	81.1% (see Figure 3)
Efficiency of Channel 2	$V_{IN} = 5V$ , $V_{OUT2} = 1.5V$ , $I_{OUT2} = 10A$ , $f_{SW} = 650kHz$	86.0% (see Figure 4)
Load Transient of Channel 1	$V_{IN} = 12V$ , $V_{OUT1} = 1V$ , $I_{STEP} = 5A \sim 7.5A$	$V_{OPP} = 62mV$ (see Figure 5)
Load Transient of Channel 2	$V_{IN} = 12V$ , $V_{OUT2} = 1.5V$ , $I_{STEP} = 5A \sim 7.5A$	$V_{OPP} = 69mV$ (see Figure 6)

### QUICK START PROCEDURE

Demonstration circuit DC2268A-G is easy to set up to evaluate the performance of the LTM4631. It can be easily inserted to an edge connector (SAMTEC MEC2-20-01-L-DV-TR) for testing and debugging. Please refer to Figure 2 for proper measurement setup and follow the procedure below:

1. Pull-up the RUN1 (J1 pin 22) and RUN2 (J1 pin 24) between 1.4V – 5V or leave them floating.
2. With power off, connect the input power supply, load and meters as shown in Figure 2. Preset the load to 0A and  $V_{IN}$  supply to 12V.
3. Turn-on the power supply at the input. The output voltage in channel 1 should be  $1.0V \pm 1.5\%$  (0.985V ~ 1.015V) and the output voltage in channel 2 should be  $1.5V \pm 1.5\%$  (1.478V ~ 1.523V).
4. Once the proper output voltage is established, adjust the load within the operating range and observe the output voltage regulation, output voltage ripple, efficiency and other parameters. Output ripple should be measured at  $C_{O3}$  and  $C_{O4}$ .
5. (Optional) LTM4631 can be synchronized to an external clock signal. Remove R2 and apply a clock signal (0V ~ 5V, square wave) to MODE-PLLIN pin.
6. (Optional) LTM4631 can be configured for a 2-phase single output at up to 20A on DC2268A-G. Install 0 $\Omega$  resistors on R26, R27, R28, R29 and R32, and remove R14, R18 and R30. Output voltage is set by R7 based on equation  $V_{OUT} = 0.6V (1 + 60.4k/R7)$ .

### QUICK START PROCEDURE

Table 1. DC2268A Demo Circuit

DEMO BOARD NUMBER	μModule REGULATOR ON THE BOARD	OUTPUT CURRENT
DC2268A-A	LTM4620	13A, 13A
DC2268A-B	LTM4620A	13A, 13A
DC2268A-C	LTM4628	8A, 8A
DC2268A-D	LTM4630	18A, 18A
DC2268A-E	LTM4630-1	18A, 18A
DC2268A-F	LTM4630A	18A, 18A
DC2268A-G	LTM4631	10A, 10A
DC2268A-H	LTM4650-1	25A, 25A
DC2268A-I	LTM4650A-1	25A, 25A

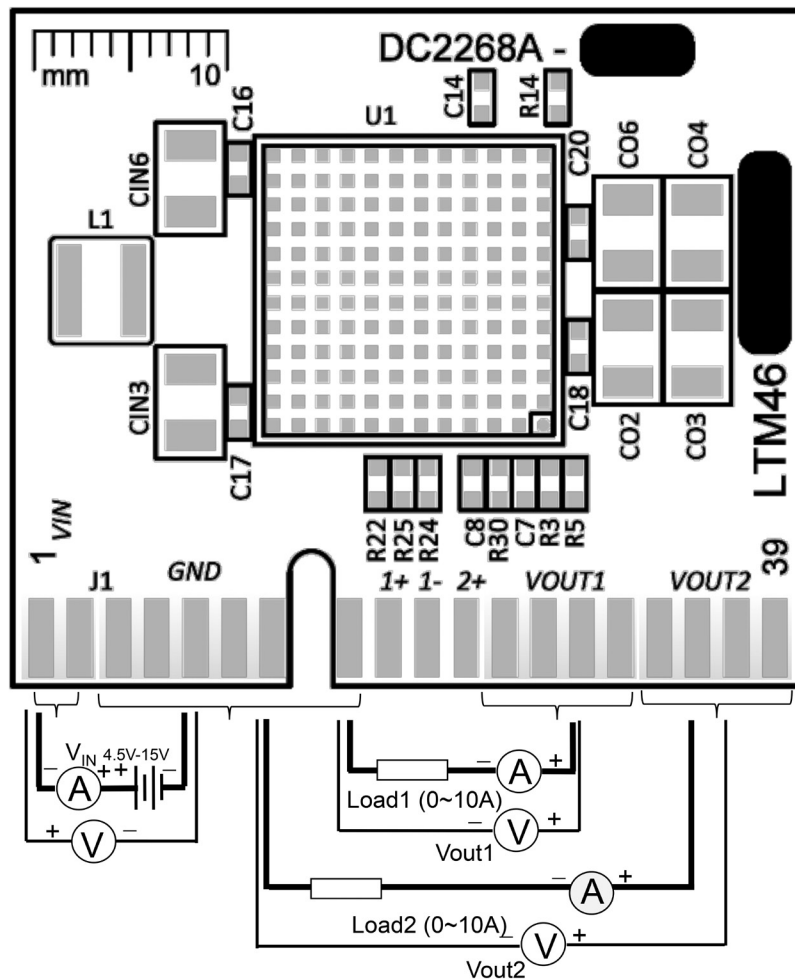


Figure 2. Test Setup of DC2268A-G

QUICK START PROCEDURE

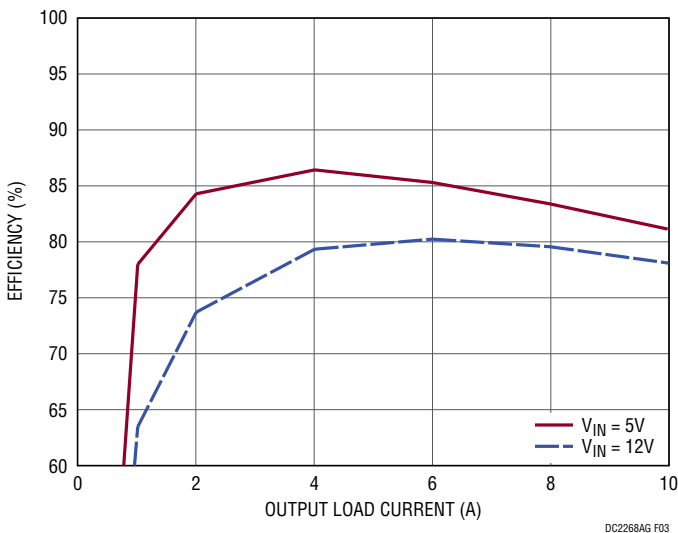


Figure 3. Measured Efficiency on Channel 1 ( $V_{OUT1} = 1.0V$ ,  $f_{SW} = 650kHz$ , Channel 2 Disabled)

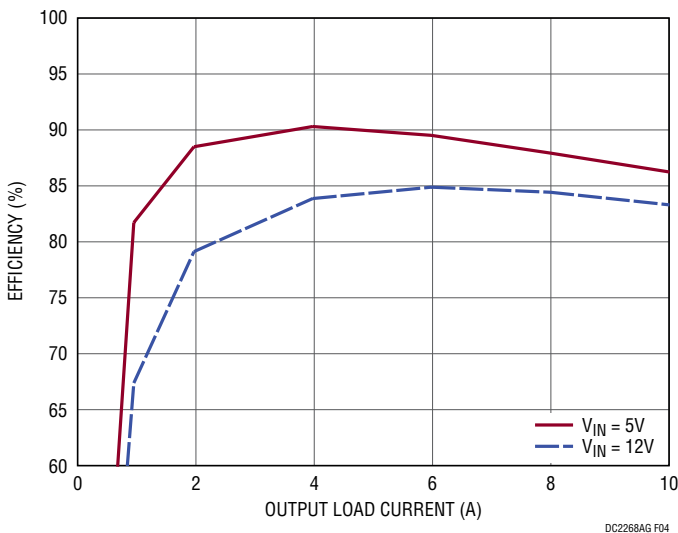


Figure 4. Measured Efficiency on Channel 2 ( $V_{OUT2} = 1.5V$ ,  $f_{SW} = 650kHz$ , Channel 1 Disabled)

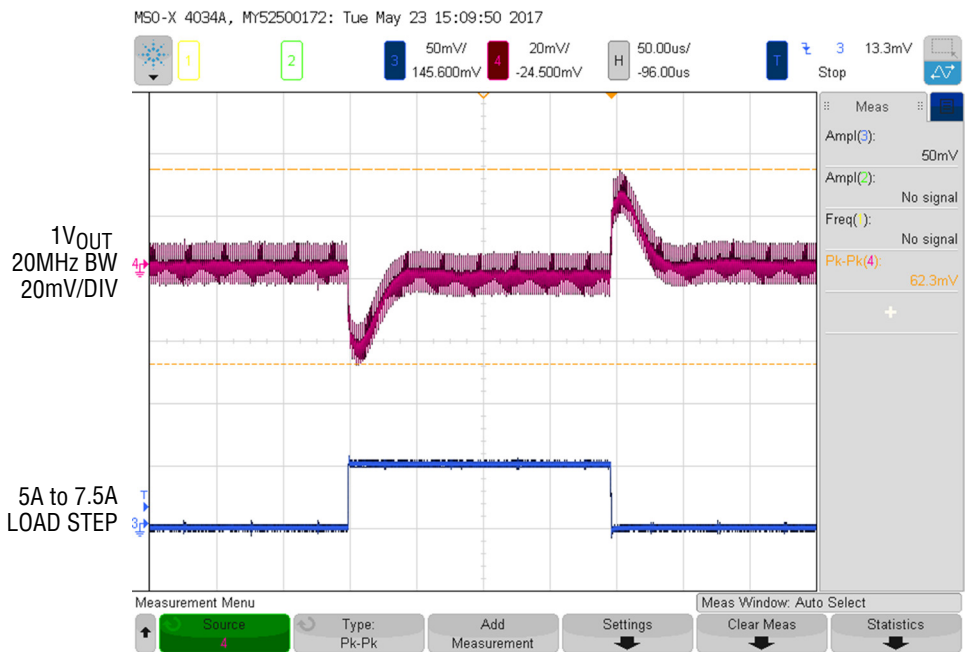


Figure 5. Measured Channel 1, 5A to 7.5A Load Transient ( $V_{IN} = 12V$ ,  $V_{OUT1} = 1.0V$ )

### QUICK START PROCEDURE

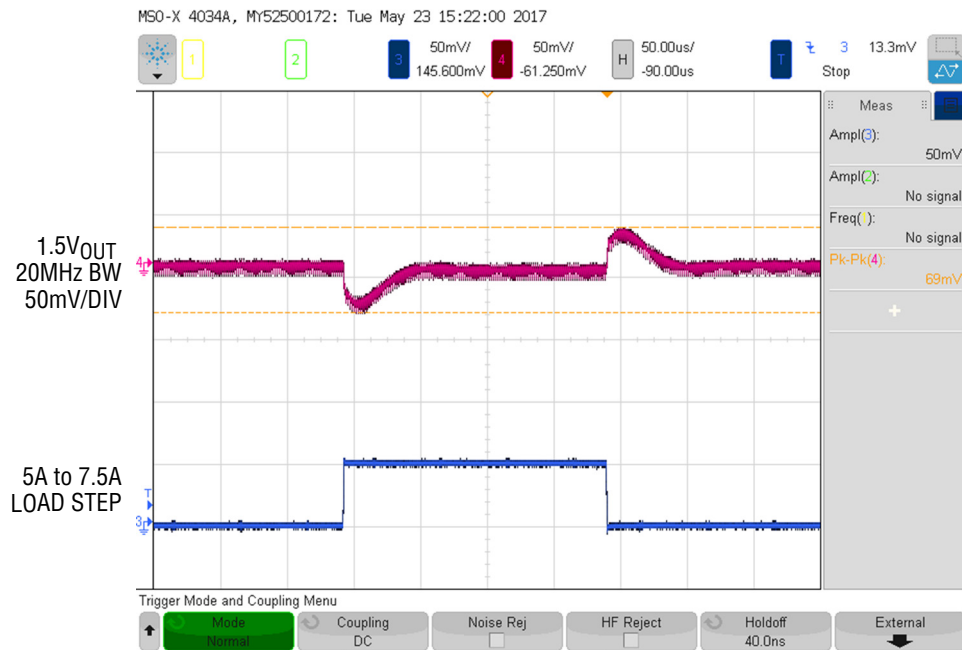


Figure 6. Measured Channel 2, 5A to 7.5A Load Transient ( $V_{IN} = 12V$ ,  $V_{OUT2} = 1.5V$ )

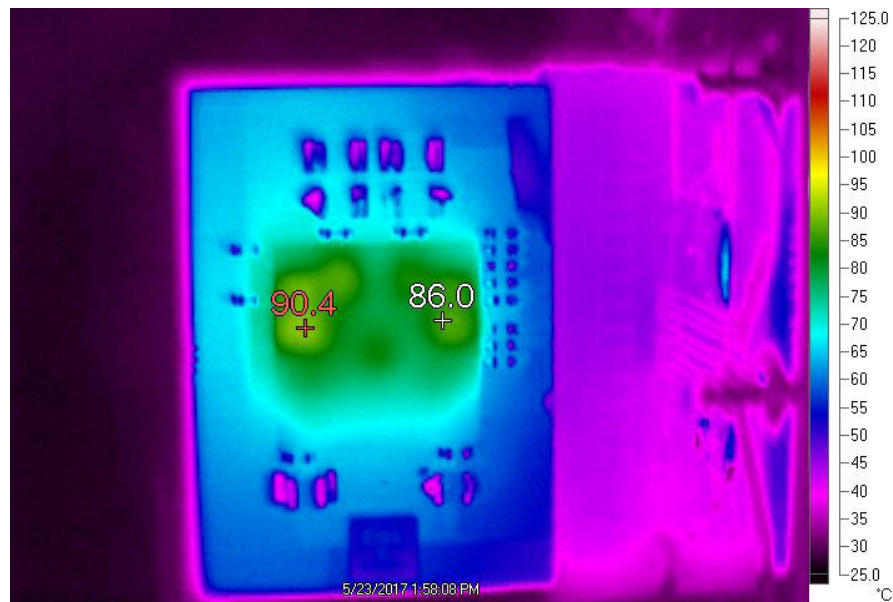


Figure 7. Thermal Performance at  $V_{IN} = 12V$ ,  $V_{OUT1} = 1.0V/9A$ ,  $V_{OUT2} = 1.5V/9A$ ,  $f_{sw} = 650kHz$ ,  $T_A = 23^\circ C$ , 400LFM Airflow

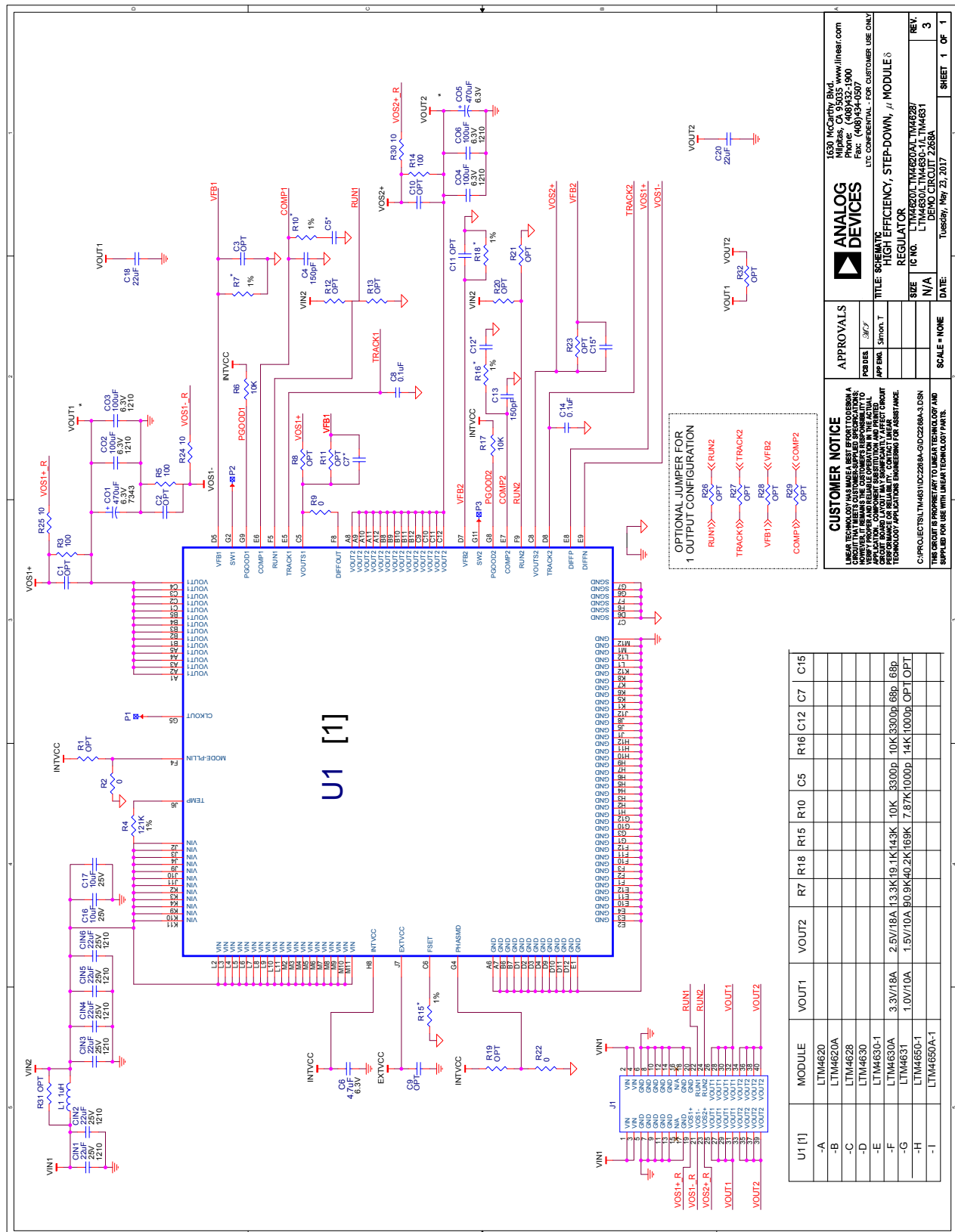
# DEMO MANUAL

## DC2268A-G

### PARTS LIST

ITEM	QTY	REFERENCE	PART DESCRIPTION	MANUFACTURER/PART NUMBER
<b>Required Circuit Components</b>				
1	6	CIN1, CIN2, CIN3, CIN4, CIN5, CIN6	CAP, 1210 22 $\mu$ F 10% 25V X5R	AVX 12103D226KAT2A
2	2	C01, C05	CAP, 7343 470 $\mu$ F 20% 6.3V POSCAP	PANASONIC 6TPF470MAH
3	4	C02, C03, C04, C06	CAP, 1210 100 $\mu$ F 10% 6.3V X5R	AVX 12106D107KAT2A
4	1	C6	CAP, 0603 4.7 $\mu$ F 20% 6.3V X5R	AVX 06036D475MAT2A
5	2	C8, C14	CAP, 0603 0.1 $\mu$ F 10% 25V X7R	AVX 06033C104KAT2A
6	2	C5, C12	CAP, 0603 1000pF 5% 16V C0G	AVX 0603YC102JAT2A
7	2	C16, C17	CAP, 0603 10 $\mu$ F 20% 25V X5R	TDK C1608X5R1E106M080AC
8	2	C18, C20	CAP, 0603 22 $\mu$ F 20% 6.3V X5R	TDK C1608X5R0J226M080AC
9	1	L1	IND, 1.0 $\mu$ H	COILCRAFT XAL5030-102MEC
10	2	R2, R22	RES, 0603 0 $\Omega$ JUMPER	VISHAY CRCW06030000Z0EA
11	3	R3, R5, R14	RES, 0603 100 $\Omega$ 5% 0.1W	VISHAY CRCW0603100RJNEA
12	1	R4	RES, 0603 121k $\Omega$ 1% 0.1W	VISHAY CRCW0603121KFKEA
13	2	R6, R17	RES, 0603 10k $\Omega$ 5% 0.1W	VISHAY CRCW060310K0JNEA
14	1	R7	RES, 0603 90.9k $\Omega$ 1% 0.1W	VISHAY CRCW060390K9FKEA
15	1	R9	RES, 0603 0 $\Omega$	VISHAY CRCW06030000Z0EA
16	1	R10	RES, 0603 7.87k $\Omega$ 1% 0.1W	VISHAY CRCW0607K87FKEA
17	1	R16	RES, 0603 14k $\Omega$ 1% 0.1W	VISHAY CRCW060314K0FKEA
18	1	R15	RES, 0603 169k $\Omega$ 1% 0.1W	VISHAY CRCW0603169KFKEA
19	1	R18	RES, 0603 40.2k $\Omega$ 1% 0.1W	VISHAY CRCW060340K2FKEA
20	3	R24, R25, R30	RES, 0603 10 $\Omega$ 5% 0.1W	VISHAY CRCW060310R0JNEA
21	1	U1	IC, VOLTAGE REGULATOR LGA	ANALOG DEVICES LTM4628EV#PBF
<b>Additional Demo Board Circuit Components</b>				
1	0	C1, C2, C3, C4, C7, C9, C10, C11, C13, C15	CAP, 0603 OPTION	OPTION
2	0	R1, R8, R11, R12, R13, R19, R20, R21, R23, R26, R27, R28, R29	RES, 0603 OPTION	OPTION
3	0	R31, R32	RES, 2512 OPTION	OPTION
<b>Hardware</b>				
1	1	J1	CONN., CARD EDGE 1.6mm	SAMTEC MEC2-20-01-L-DV-TR

### SCHEMATIC DIAGRAM



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**REGULATOR**  
HIGH EFFICIENCY, STEP-DOWN,  $\mu$  MODULE  $\delta$

**SIZE** C NO. LTM4620/LTM4620A/LTM4628/LTM4630/LTM4630-1/LTM4631  
REV. 3

**DATE:** Tuesday, May 23, 2017  
**SCALE:** NONE  
**SHEET** 1 OF 1

# DEMO MANUAL

## DC2268A-G

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### ESD Caution

**ESD (electrostatic discharge) sensitive device.** Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

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