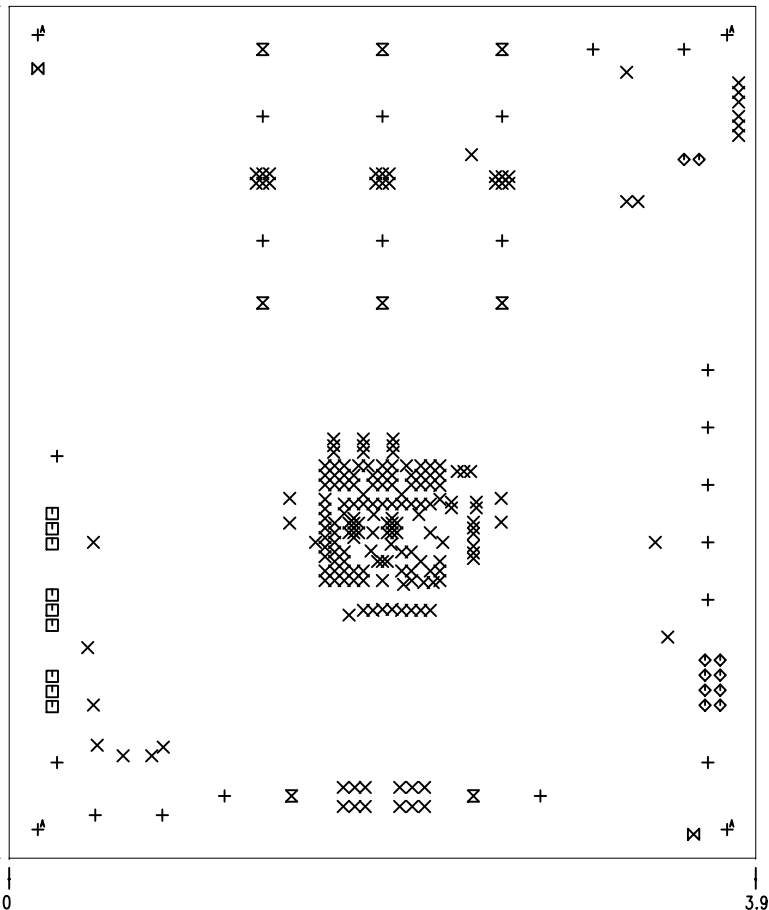


4.45



SIZE	QTY	SYM	PLATED	TOL
0.01	197	✕	YES	+/-0.003
0.031	9	□	YES	+/-0.003
0.035	10	◇	YES	+/-0.003
0.07	2	⊠	NO	+/-0.003
0.095	20	+	YES	+/-0.003
0.187	4	⊕ ^A	NO	+/-0.003
0.215	8	⊗	YES	+/-0.003

REVISION HISTORY

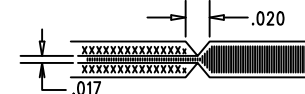
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	2	PRODUCTION	YAN L.	08-14-14

LAYER STRUCTURE

	LAYER 1 (COMPONENT SIDE)
	LAYER 2 (GND)
	LAYER 3 (SIGNAL/GND/GND_SIG)
	LAYER 4 (SOLDER SIDE)

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062 +/- .005
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0/0 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



- DESIGN HAS SOLDER MASK DEFINED PADS ON U1 WITH 0.029 PAD, 0.025 SOLDER MASK, AND 0.024 SOLDER PASTE.
DO NOT CHANGE ANY SIZE ON THIS COMPONENT.

UNLESS OTHERWISE SPECIFIED		APPROVALS		 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
DIMENSIONS ARE IN INCHES		PCB DES.	LT		
TOLERANCES:		APP ENG.	YAN L.	TITLE: FABRICATION DRAWING HIGH EFFICIENCY, TRIPLE OUTPUTS STEP-DOWN μ MODULE REGULATOR	
0.XX = ± 0.01					
0.XXX = ± 0.005				SIZE	
INTERPRET DIM AND TOL				IC NO. LTM4634EY	
PER ASME Y14.5M-1994				DEMO CIRCUIT 2121A	
THIRD ANGLE PROJECTION				REV	
				2	
		SCALE = NONE		FILENAME: DC2121A-2.PCB	
				SHT 1 OF 1	

